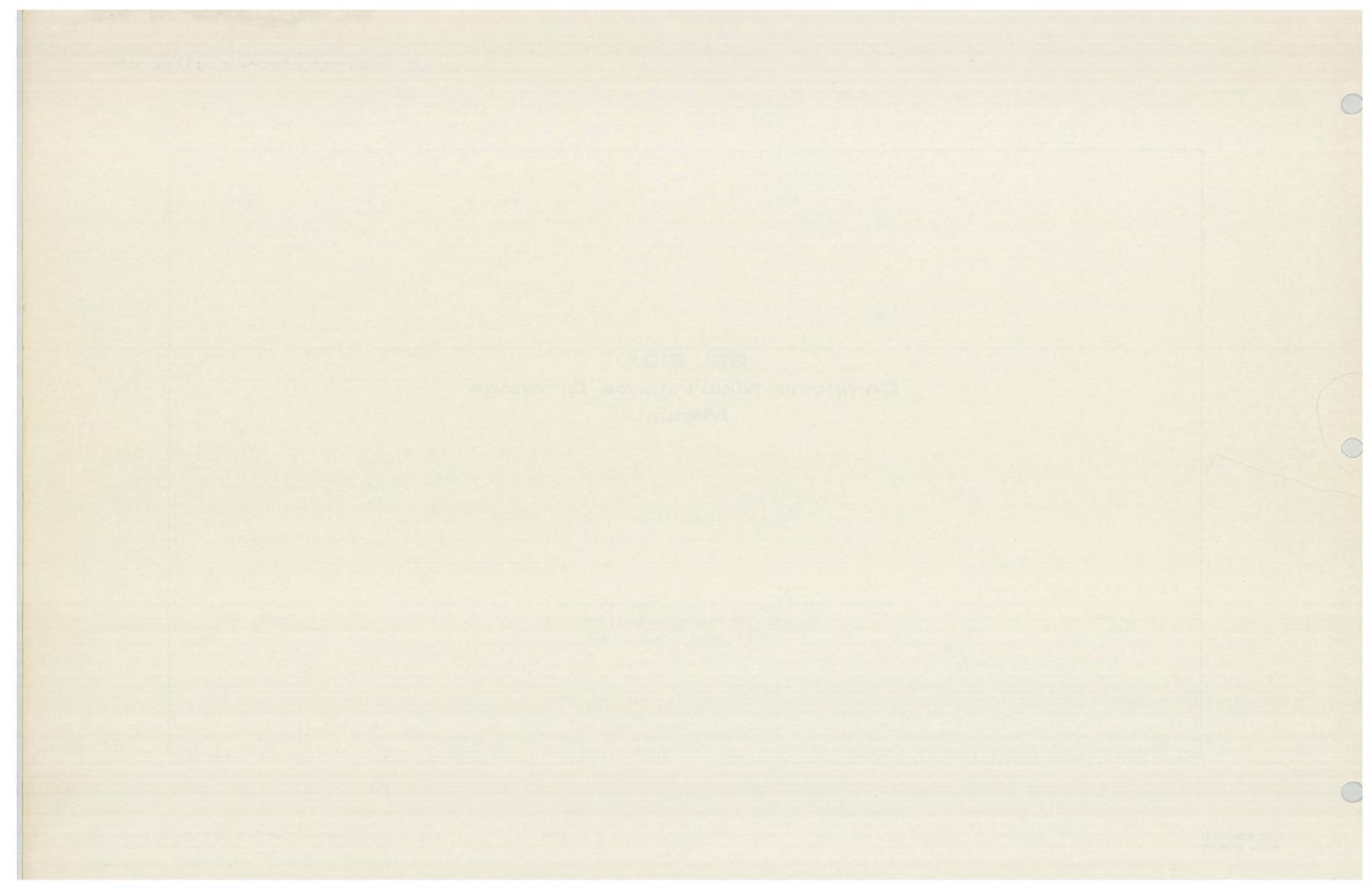
SEL 810A Computer Maintenance Drawings Manual

SEL 8IOA Computer Maintenance Drawings Manual

June, 1968

This publication supersedes SEL 810A Computer Maintenance Drawings Manual, 95001A, dated October, 1967



LIST OF DRAWINGS

Drawing Number	Title	Drawing Number	Title
CI	RCUIT CARDS - SCHEMATIC & ASSEMBLY DRAWINGS		
8201	Schematic & Assembly RTL Memory Interface	8217	Schematic & Assembly RTL Program Register Bits 1-5
8202	Schematic & Assembly RTL "T" Register & Instruction Register Bit Slice	8218	Schematic & Assembly RTL Program Register Bits 6-11
8203	Schematic & Assembly RTL A&B Accumulators Bit Slice (3 sheets)	8219	Schematic & Assembly RTL Program Register Bits 12-15
8204	Schematic & Assembly RTL 8 Bit Full Adder	8220	Schematic & Assembly RTL - Shift Control
	·	8221	Schematic & Assembly RTL Sense Switch Decode
8205	Schematic & Assembly RTL Operation Decoder	8222	Schematic & Assembly RTL Overflow & A Sign Strobe & Normalize
8206	Schematic & Assembly RTL Augmented Operation Decoder #1	8223	Schematic & Assembly RTL Indirect - 2
8207	Schematic & Assembly RTL Augmented Operation Decoder #2	8224	Schematic & Assembly RTL "B" Full Code Detector & Sign Strobe
8208	Schematic & Assembly RTL Control Cycle Generator	8225	Schematic & Assembly I - O Instruction Timing
8209	Schematic & Assembly RTL Shift Counter		
8210	Schematic & Assembly RTL "T" Output Gate Control	8226	Schematic & Assembly RTL Memory Data Register Load & & Address Clear
8211	Schematic & Assembly RTL "B" Output & "T" Comp Output Gate Control	8227	Schematic & Assembly RTL - Adder to Memory Control & Address
8212	Schematic & Assembly RTL "A" Output Gates		Register Strobe
8213	Schematic & Assembly RTL "B" Register and "T" Register Strobe	8228	Schematic & Assembly RTL Program Register Advance Logic I
8214	Schematic & Assembly RTL "A" Register Strobe	8229	Schematic & Assembly RTL Program Register Advance Logic II
8215	Schematic & Assembly RTL Carry Logic	8230	Schematic & Assembly RTL - Program Register Advance Logic III
		8231	Schematic & Assembly RTL Adder Zero Det.
8216	Schematic & Assembly RTL Input/Output Instruction Decode	8232	Schematic & Assembly RTL "A" Zero Det.

Drawing Number CIRCU	Title JIT CARDS - SCHEMATIC & ASSEMBLY DRAWINGS (Cont'd)	Drawing Number	Title
8234	Schematic & Assembly RTL Clock Control	8253	Schematic & Assembly RTL Program Protect Logic 1
8235	Schematic & Assembly RTL Clock Control #2	8254	Schematic & Assembly RTL Program Protect Logic 2
8236	Schematic & Assembly RTL Clock Control #3	8255	Stall Alarm Control
8237	Schematic & Assembly RTL Load/Display Memory Step Contro	1 8256	Stall Alarm Counter
8238	Schematic & Assembly RTL Priority Interrupt Latch 1R1	8257	Schematic & Assembly RTL-Initialize Control (2 sheets)
8239	Schematic & Assembly RTL Priority Interrupt TOI & Disable Logic 1R1	8258	Schematic & Assembly RTL Cycle Control (2 sheets)
8240	Schematic & Assembly RTL PI Group Matrix 1R1	8259	Schematic & Assembly RTL Address Control (2 sheets)
8241	Schematic & Assembly RTL Priority Interrupt Address "and"	8260	Schematic & Assembly RTL Load Address Control (2 sheets)
ok a salah 1	Gates 1R1	8261	Schematic & Assembly RTL BTC Data Transfer Control 1 (2 sheets)
8242	Schematic & Assembly RTL Priority Interrupt 1R1 (7 sheets)	8262	Schematic & Assembly RTL-BTC-Data Transfer Control 2 (2 sheets)
8243	Schematic & Assembly RTL PI Unitary/Binary CONV(MSB'S)	8263	Schematic & Assembly RTL 5 Bit Dual Counter (3 sheets)
8244	Schematic & Assembly RTL Unitary/Binary CONV(LSB'S)	8264	Schematic & Assembly RTL Clock Buffers
8245.	Schematic & Assembly RTL Power Failure Control	8265	Schematic & Assembly RTL BPC Cycle Control
8246.	Schematic & Assembly RTL Memory Module Select Driver	8266	Schematic & Assembly RTL Protect Control
8247	Schematic & Assembly RTL 16 Bit Parity Gen/Check	8267	Schematic & Assembly RTL-Initial Address Control
8248	Schematic & Assembly RTL Parity Control	8268	Schematic & Assembly RTL-BTC Priority
8249	Schematic & Assembly RTL Instruction Trap	8269	Schematic & Assembly RTL Line Driver, Current Driver and Inhibit Drive Regulator
8250	Schematic & Assembly RTL Variable Base Register Control	8270	Schematic & Assembly RTL Memory Timing #I
8251	Schematic & Assembly RTL Variable Base Register-Register	8271	Schematic & Assembly RTL Memory Timing #II
8252	Schematic & Assembly RTL-VBR Display Gates	8272	Schematic & Assembly RTL Quad Data Register & Inhibit Driver

Drawing Number		Drawing Number	Title
CIRCU	JIT CARDS - SCHEMATIC & ASSEMBLY DRAWINGS (Cont'd)		
8273	Schematic & Assembly RTL Read/Write Matrix and Line Drivers	8292	Schematic & Assembly RTL - MPY/DIV Control 8
8274	Schematic & Assembly RTL & Bit Memory Address Register	8293	Schematic & Assembly RTL Mask Priority Interrupt
8275	Schematic & Assembly RTL 5-Bit Memory Address Register & 8 K Control	8294	Schematic & Assembly RTL Extender Adder and TX 2 Gates
027/		8295	Schematic & Assembly RTL Memory Access Control
8276	Schematic & Assembly RTL Memory Parity & Protect Bit Data Registers & Inhibit Drivers	8296	Schematic & Assembly RTL Power Fail Safe
8277	Schematic & Assembly RTL + 12V and Threshold Regulator	8297	Schematic & Assembly TI 8-Bit Ripple Adder
8278	Schematic & Assembly Inhibit Resistors	8298	Schematic & Assembly RTL - SPB Map Store Gates
8279	Schematic & Assembly Memory Power Supply (2 sheets)	8299	Program Protect Lamp Drivers
- 8280	Schematic & Assembly 3.6V & 5V Power Supply (2 sheets)	8543	Schematic & Assembly 2 NOR(M)
8281	Schematic & Assembly Memory Rectifiers	8544	Schematic & Assembly 3 NOR(M)
8282	Schematic & Assembly RTL - Control Panel (2 sheets)	8545	Schematic & Assembly Inverter (M)
8283	Schematic & Assembly RTL MPY/DIV Control 1	8547	Schematic & Assembly 4 NOR(M)
8284	Schematic & Assembly RTL MPY/DIV Control #2	8557	Schematic & Assembly 4 NOR Expander (M)
8285	Schematic & Assembly RTL - MPY/DIV Control 3	8614	Schematic & Assembly Cable Driver
8286	Schematic & Assembly RTL - MPY/DIV Control #4	8615	Schematic & Assembly Cable Terminator
8287	Schematic & Assembly RTL - MPY/DIV Control 5	8620	Schematic & Assembly Oscillator
8288	Schematic & Assembly RTL - MPY/DIV Control 6	8639	Schematic & Assembly Data Saver
8289	Schematic & Assembly RTL - MPY/DIV Control 7	8705	Schematic & Assembly Gated Cable Driver
8290	Schematic & Assembly RTL - MPY/DIV "T" X2 Gates	8749	Schematic & Assembly Digital Buffer (M)
8291	Schematic & Assembly RTL - MPY/DIV Shifted Adder Gates	8757	Schematic & Assembly Computer Clock

SEL 95001A

Drawing Title Number Title			
DS - SCHEMATIC & ASSEMBLY DRAWINGS (Cont'd)	(0.7)		
tic & Assembly Dual Current Driver 53072 Wiring Diagram BTC to Unit 1, 2, 3, 4 50 Pin Con	,		
tic & Assembly μ A Sense Amplifier #3 53092 Wiring Diagram Basis Input/Output Cable 104 P	'in 1F3-P1 Amp		
53175 Wiring Diagram Control Panel (3 sheets) AMS			
POWER SUPPLY SCHEMATICS	POWER SUPPLY SCHEMATICS		
Diagram BPC Address to Memory 53140 Schematic Memory Power Supply Model 222			
Diagram BPC Buffer-Drivers 53141 Schematic 3.6V & 5V Power Supply Model 221			
Diagram BTC - BTC 53142 Schematic 3.6V Power Supply Model 223			
Diagram 8K x 18 Memory a Data Flow Timing and Scheets) MECHANICAL ASSEMBLY DRAWINGS			
iagram $4K \times 18$ Memory a Data Flow Timing and 15049 Control Panel Assembly	~		
sheets) 15513 Power Supply 3.6V Assembly			
nt Control 15514 Power Supply 3.6V and 5.0V Assembly			
laneous Buffer 15515 Power Supply Memory			
RAMS Power Supply Panel Assembly			
Diagram Interconnecting Cables Memory Module #1 15595 Primary Plane Assembly (2 sheets)			
Diagram Interconnecting Cables Memory Module #2 Secondary Plane Assembly			
Diagram Interconnecting Cables BPC-Memory Interface			
15598 Memory Module Assembly			
15599 Meter Panel Assembly			
Diagram Power Supply Model 222 Memory 15600 Final Assembly (4 sheets)			
Diagram Power Supply Model 223 15601 Assembly I/O Connector Panel			
Diagram DC Power Dist. 810A 15602 Table Top Assembly			
Diagram AC Power Distribution 810A 53065 Elco 6501 Assembly Connectors			
15513 Power Supply 3.6V Assembly 15514 Power Supply 3.6V and 5.0V Assembly 15515 Power Supply Memory 15515 Power Supply Memory 15517 Power Supply Panel Assembly 15517 Power Supply Panel Assembly 15595 Primary Plane Assembly (2 sheets) 15596 Secondary Plane Assembly 15597 Primary Module Assembly 15598 Memory Module Assembly 15598 Memory Module Assembly 15599 Meter Panel Assembly 15599 Meter Panel Assembly 15600 Final Assembly (4 sheets) 15601 Assembly I/O Connector Panel 15602 Table Top Assembly			

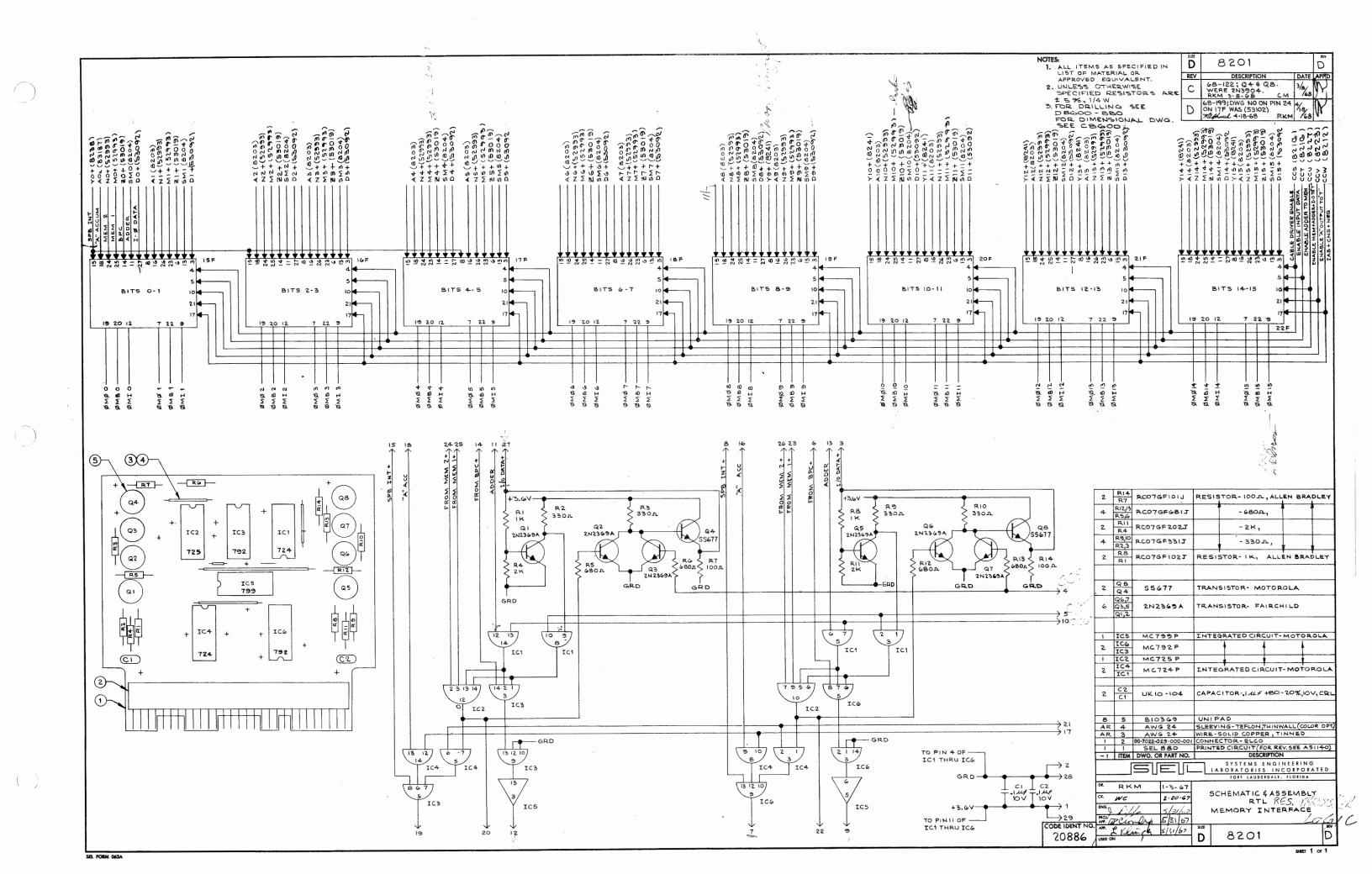
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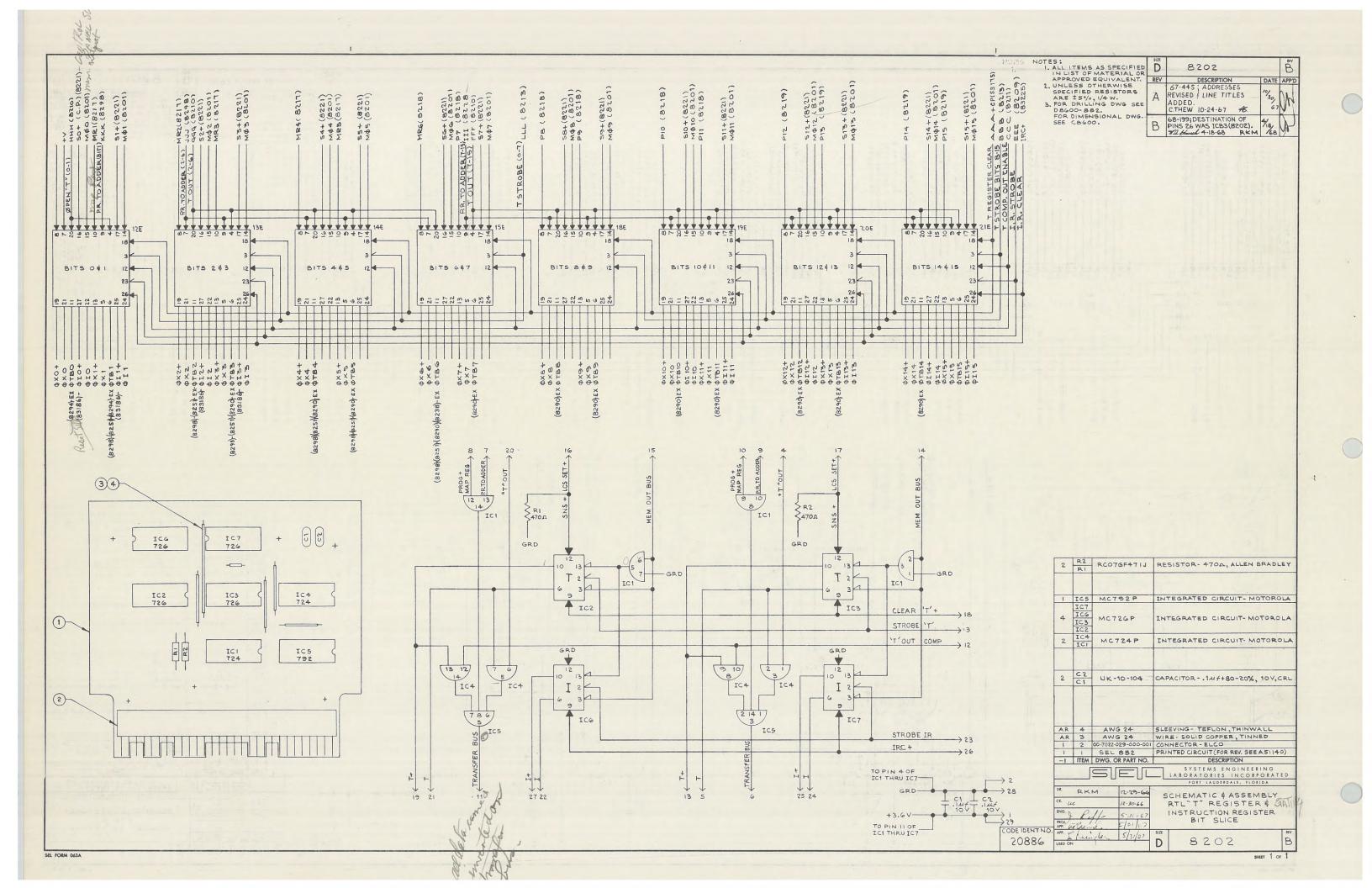
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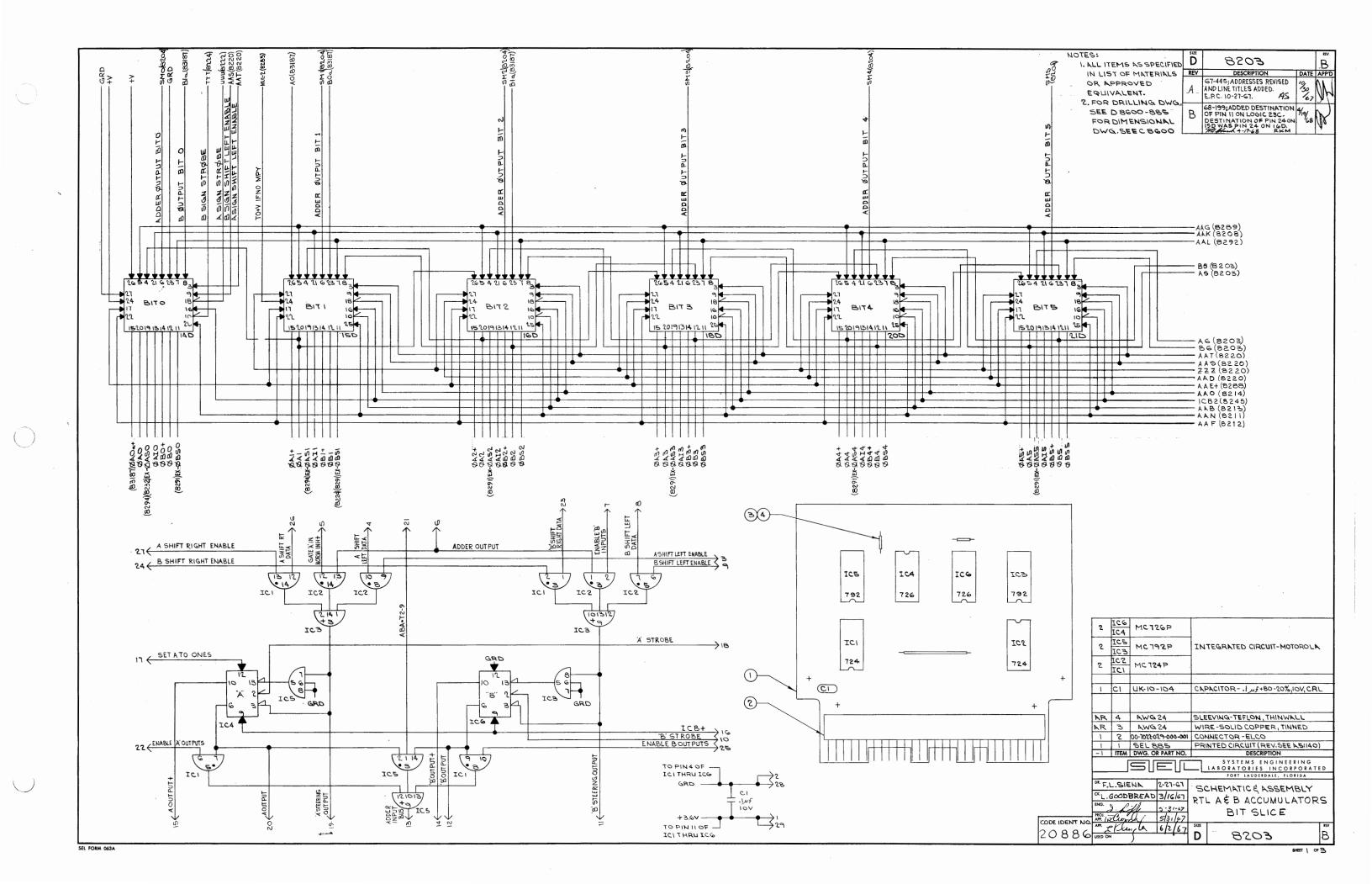
CARD LOCATION DRAWINGS

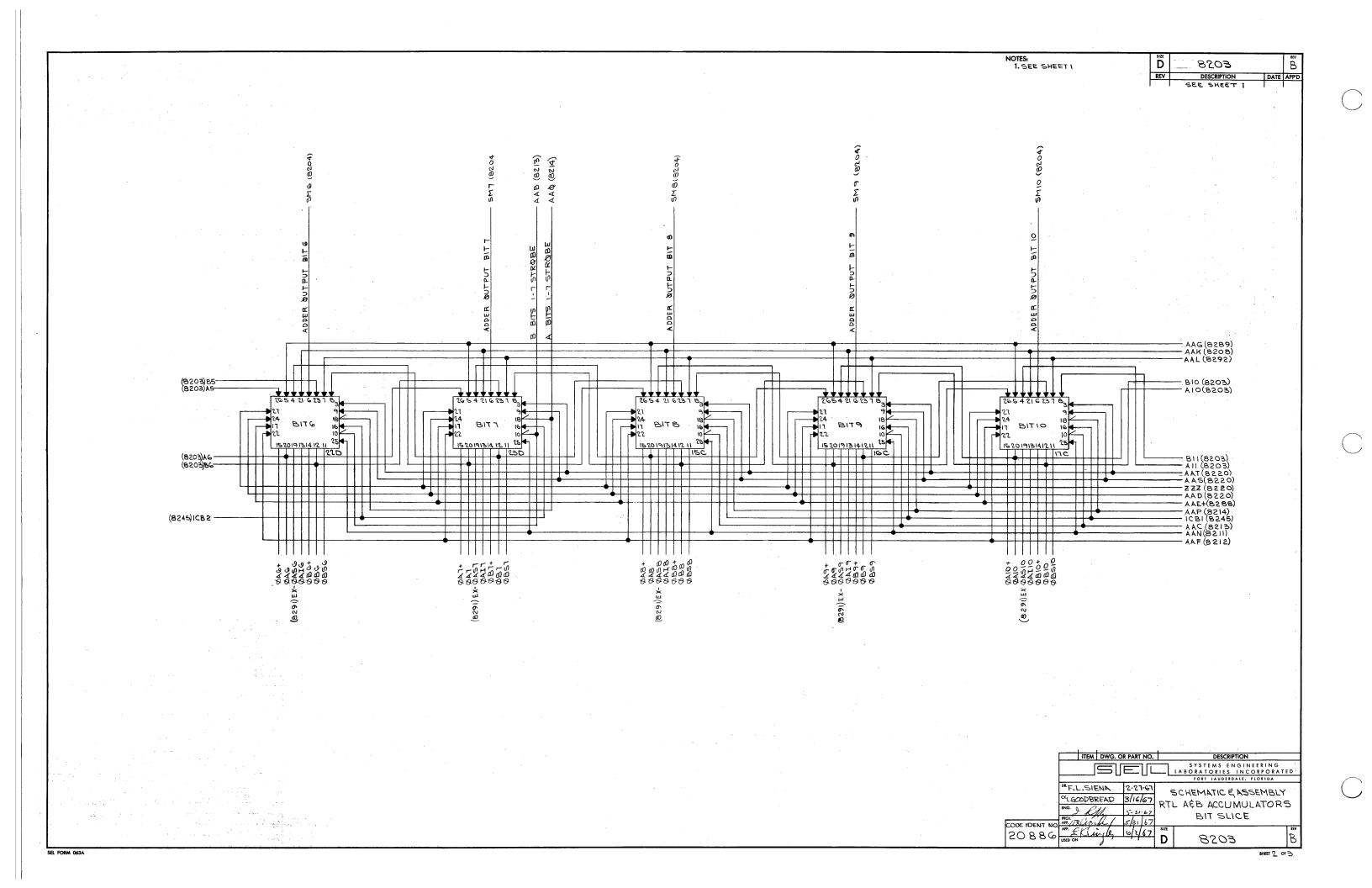
Card Layout Plane 1R1 52992**-**1

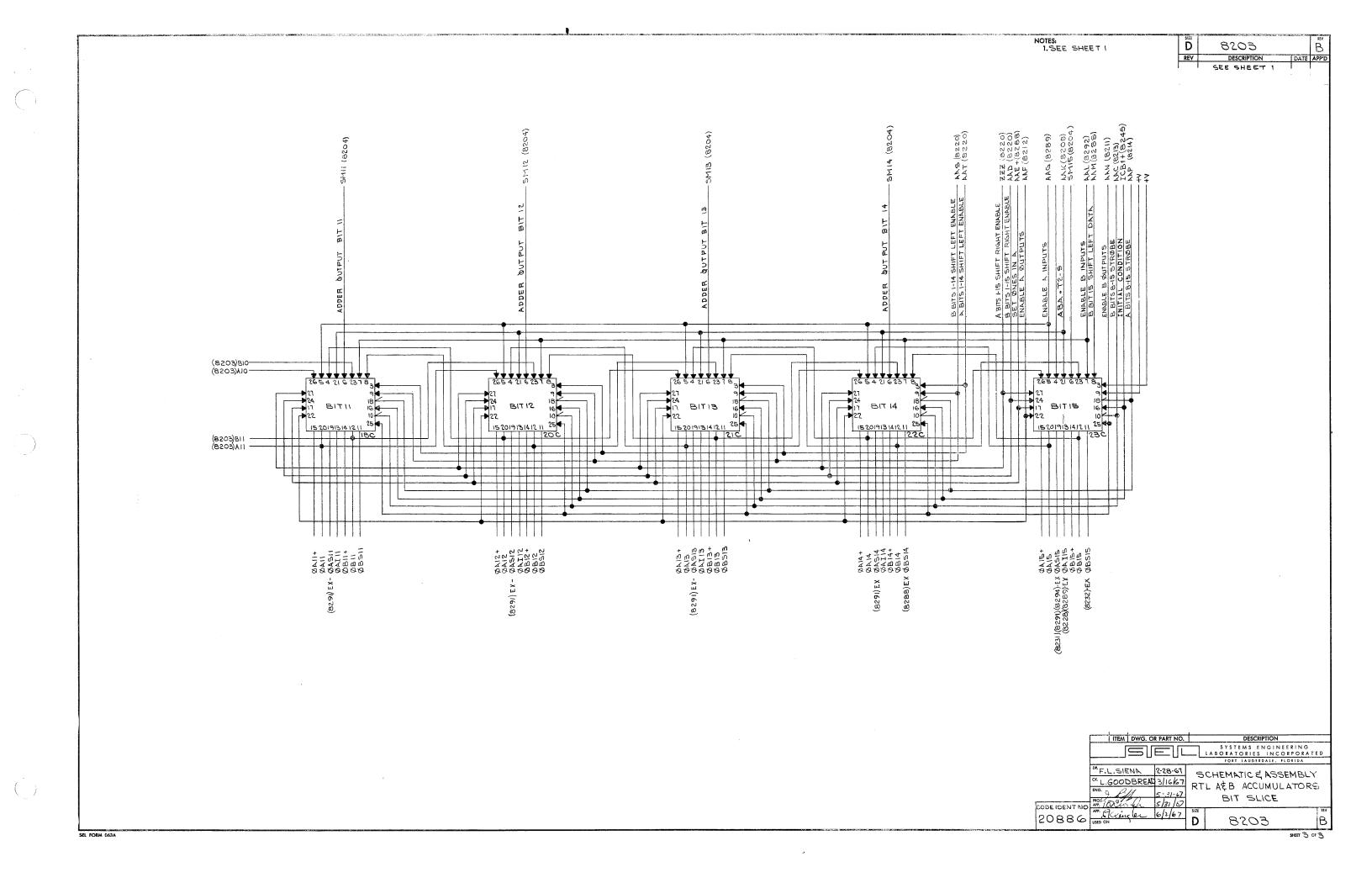
52992-2 Card Layout Plane 1R2





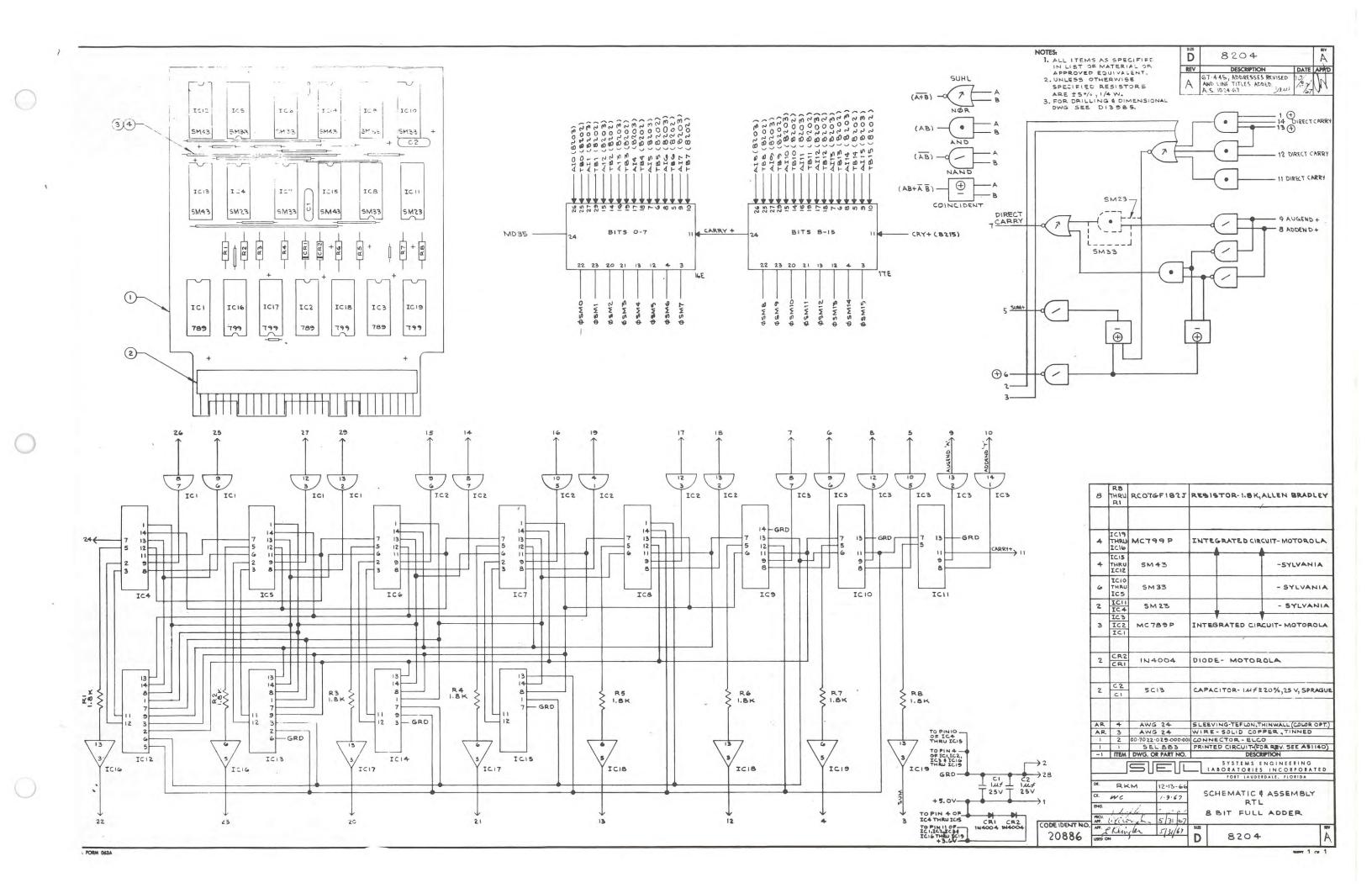


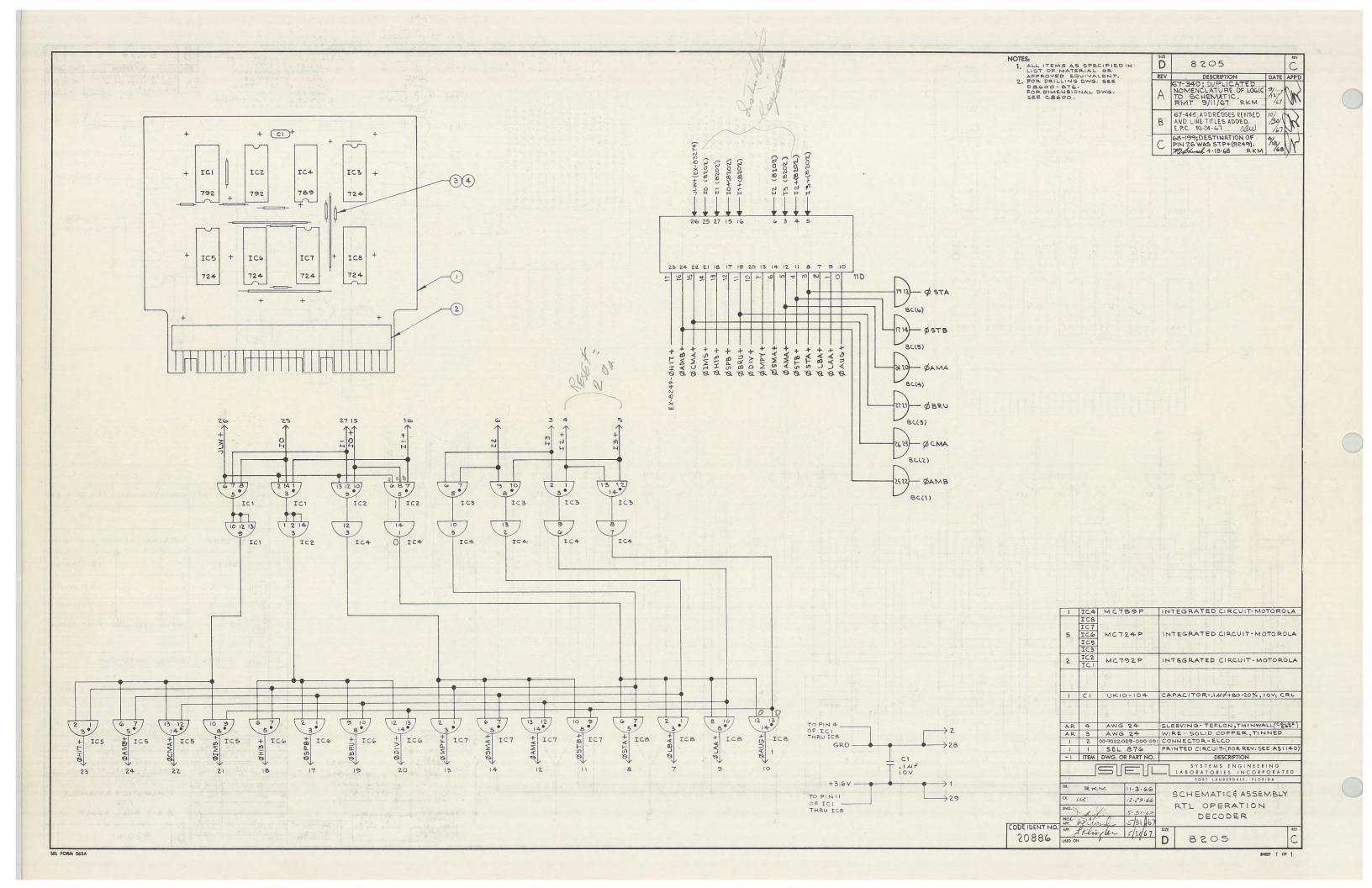


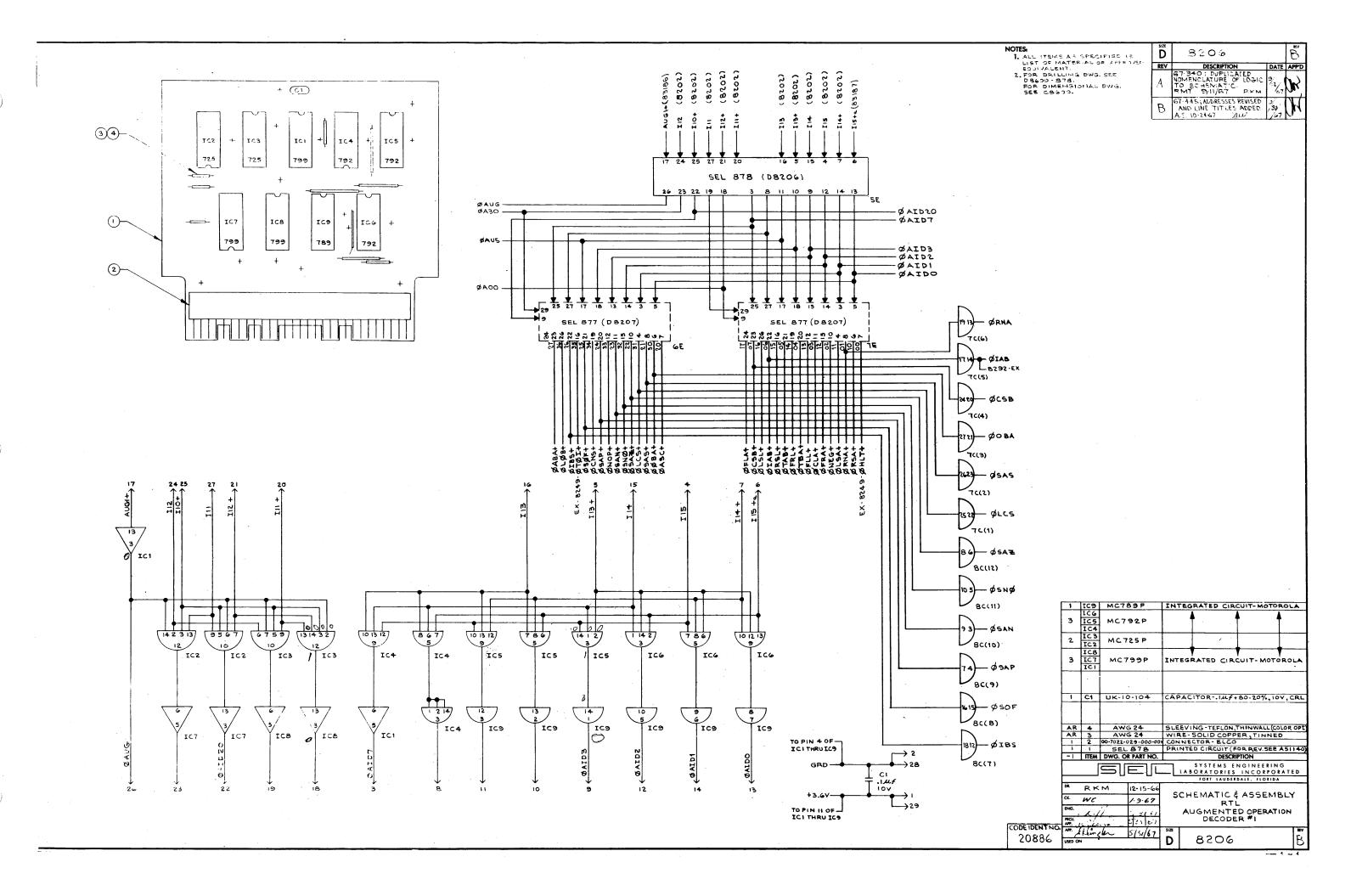


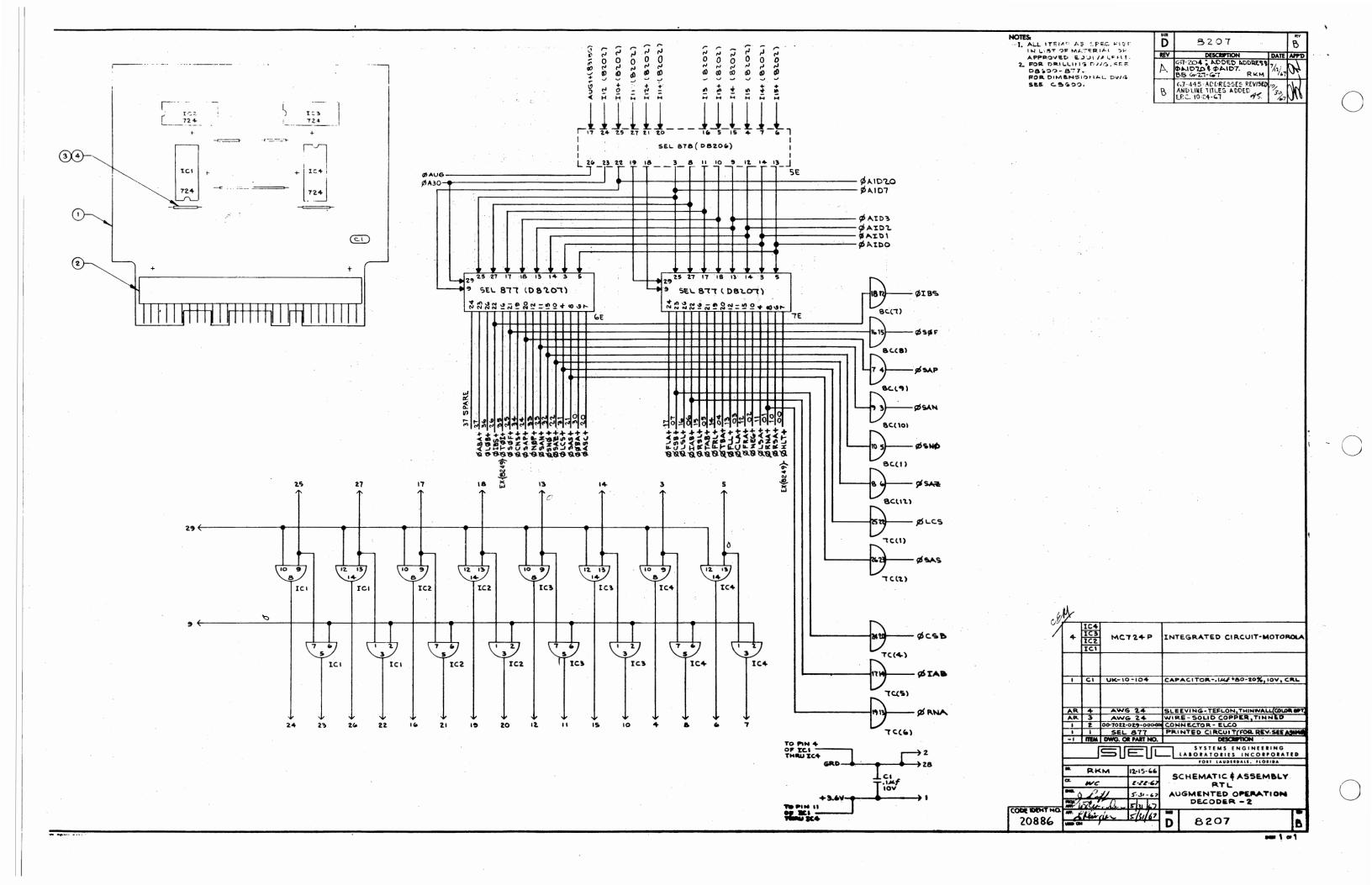
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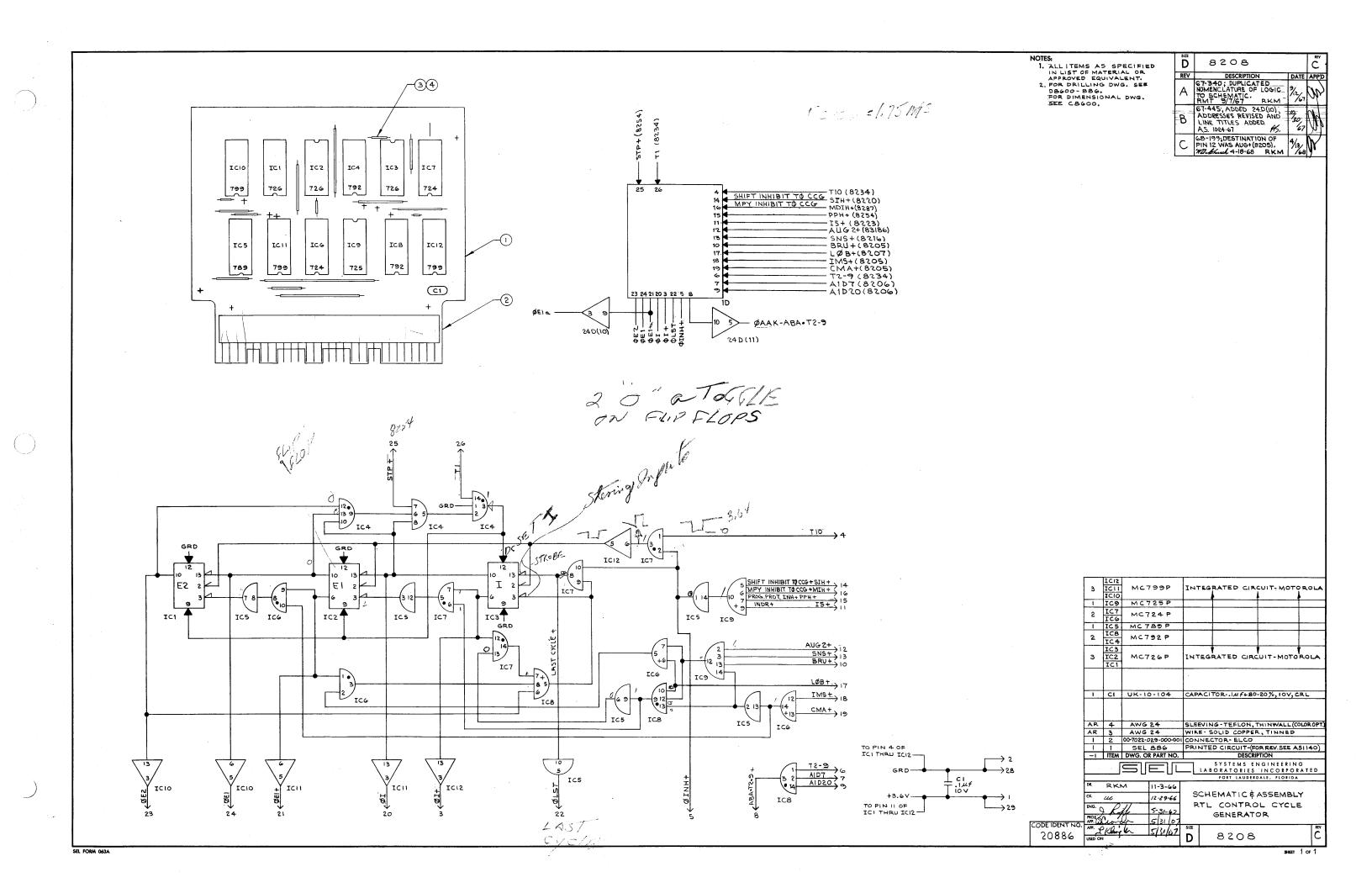
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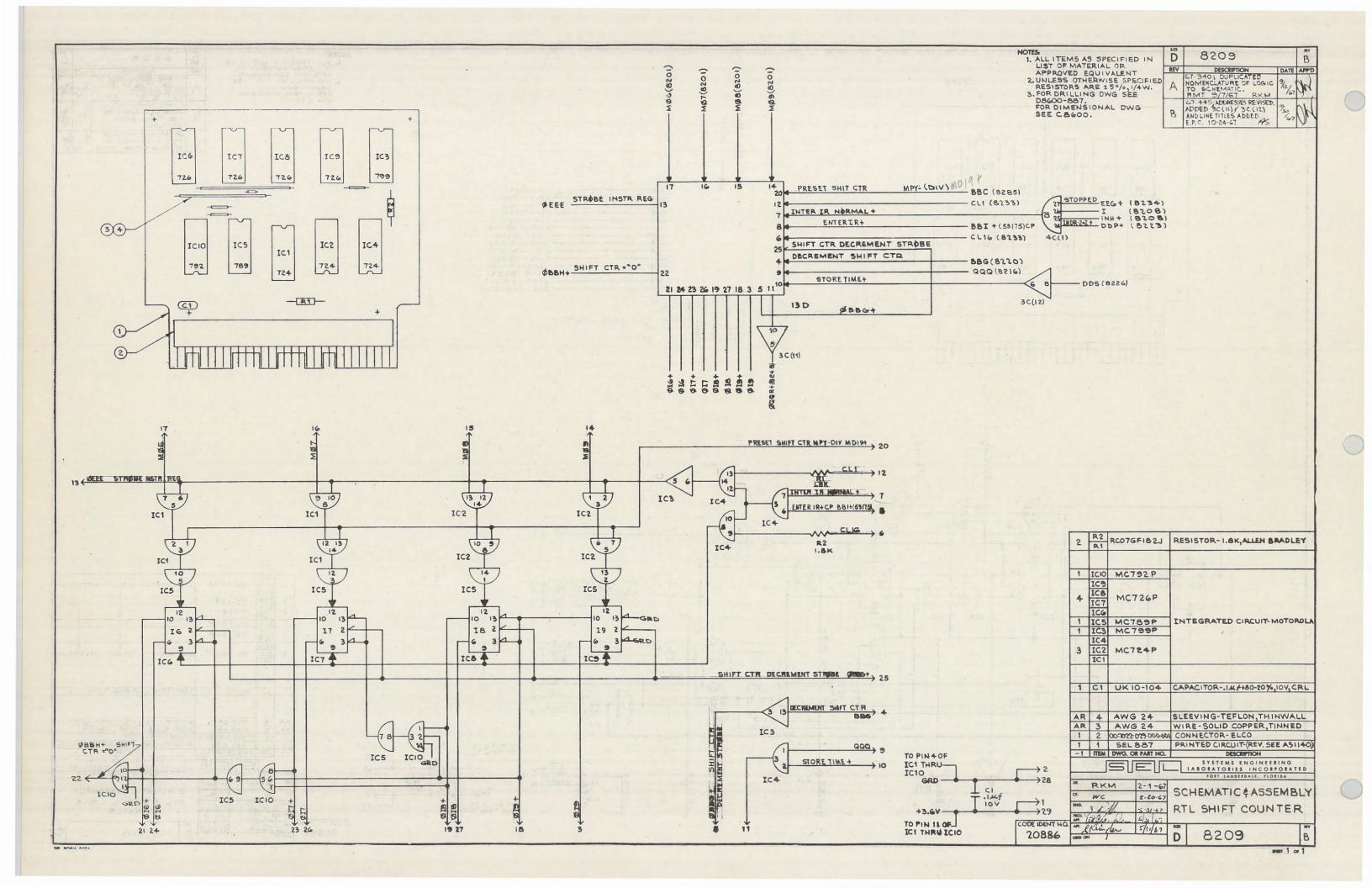


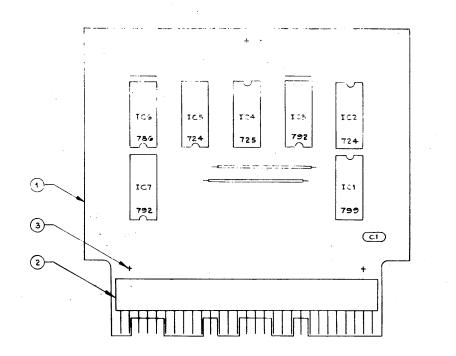


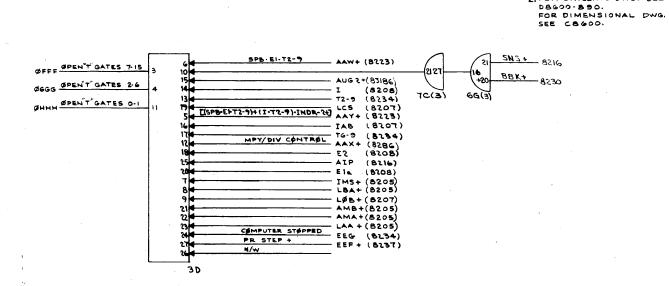


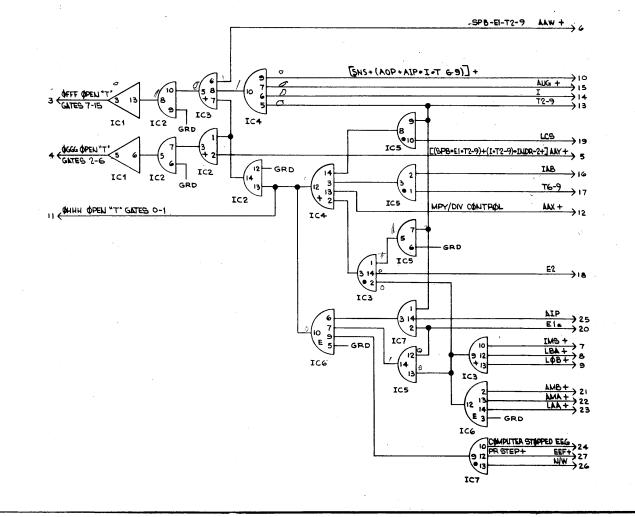


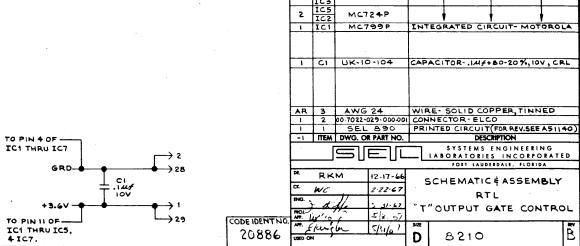












NOTES.

1. ALL ITEMS AS SPECIFIED IN LIST OF MATERIAL OR APPROVED EQUIVALENT,
2. FOR DRILLING DWG. SEE

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REV

8210

67-340; DUPLICATED NOMENCLATURE OF LOGIC

A TO SCHEMATIC.

TO SCHEMATIC.

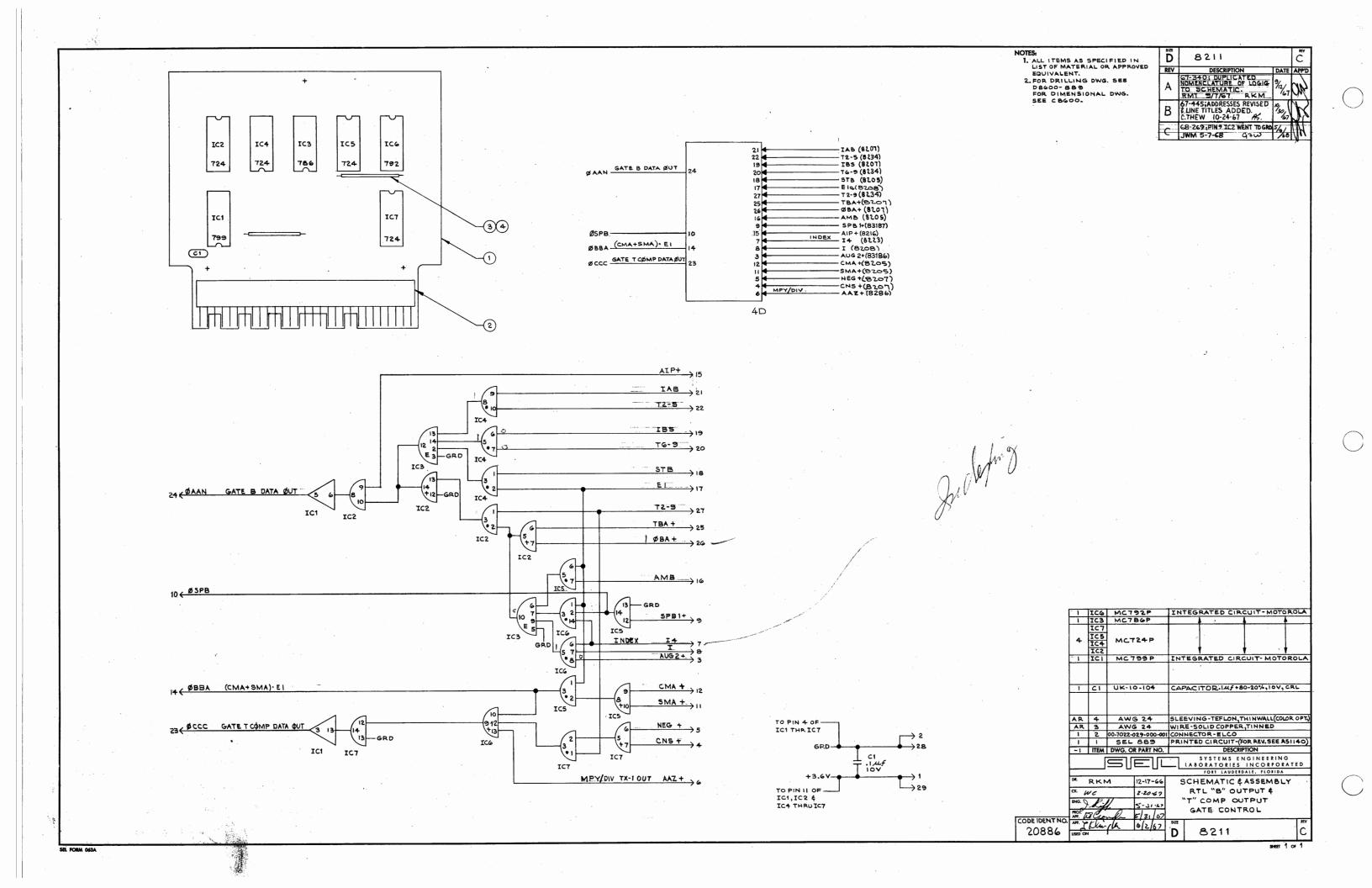
THE 9/7/67 RKM

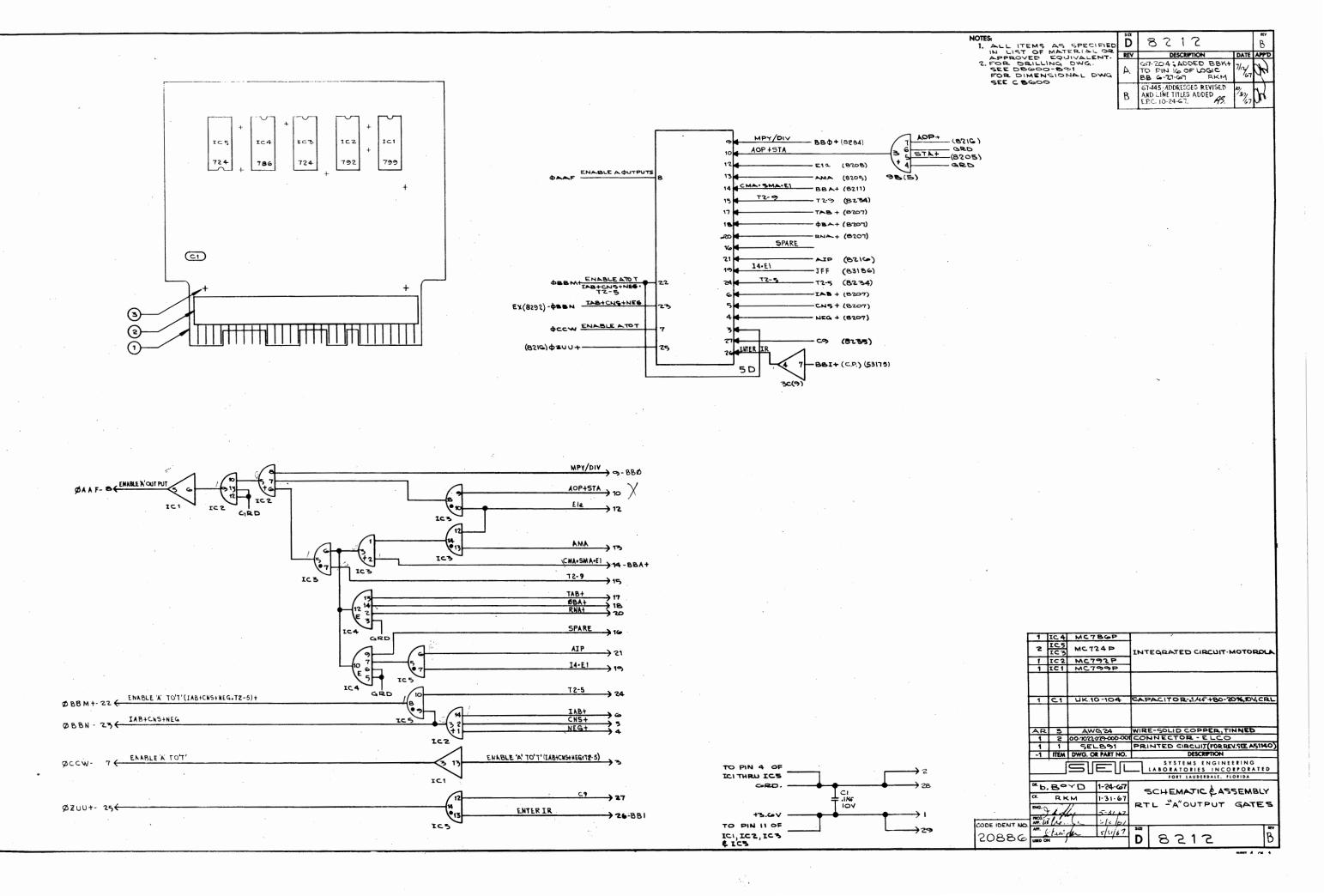
G7-445; ADDED GG(3),
ADDRESSES REVISED AND
LINE TITLES ADDED.

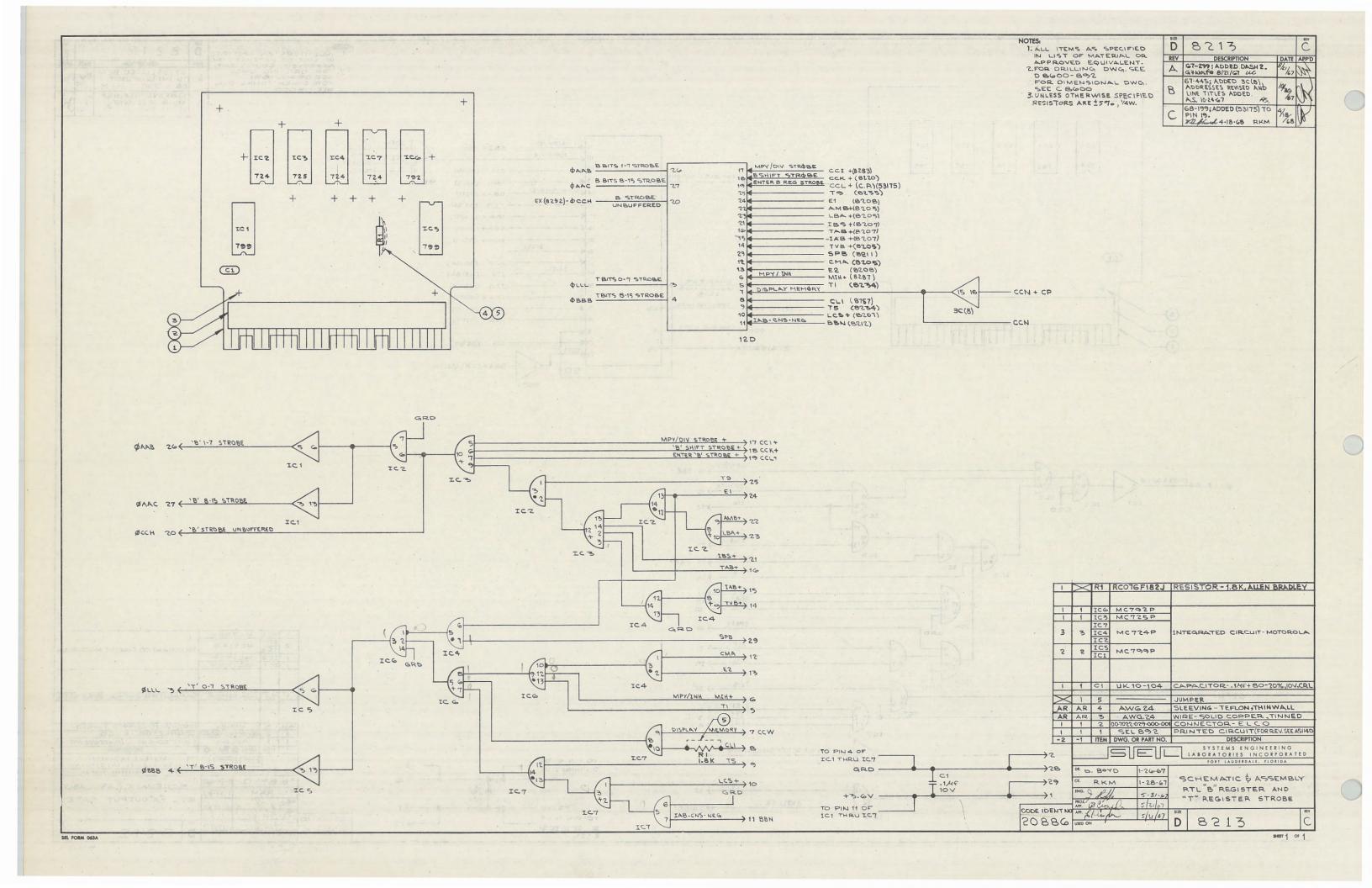
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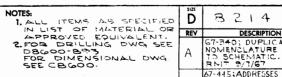
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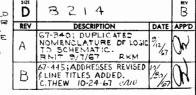
SHEET 1 OF 1

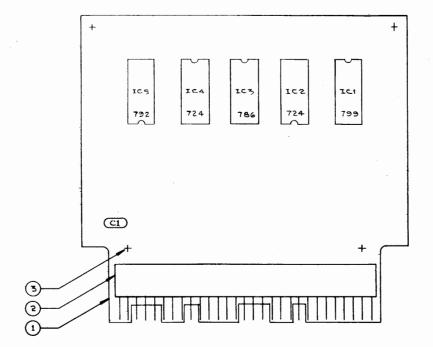


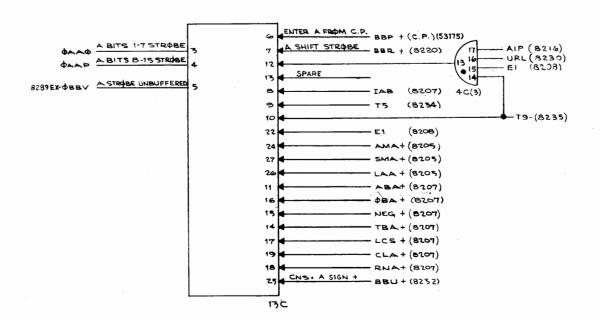


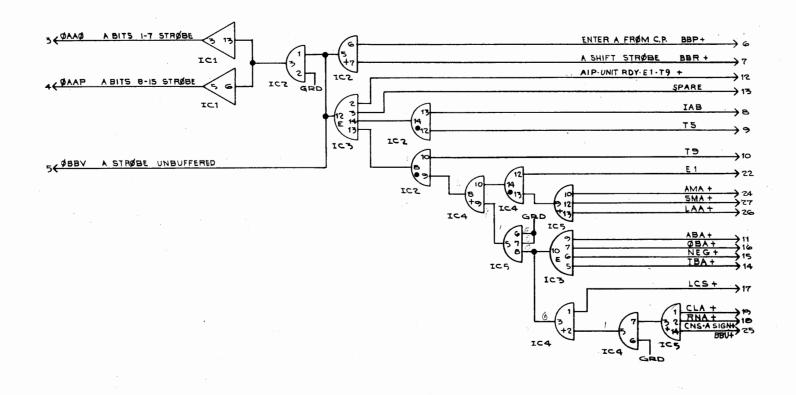


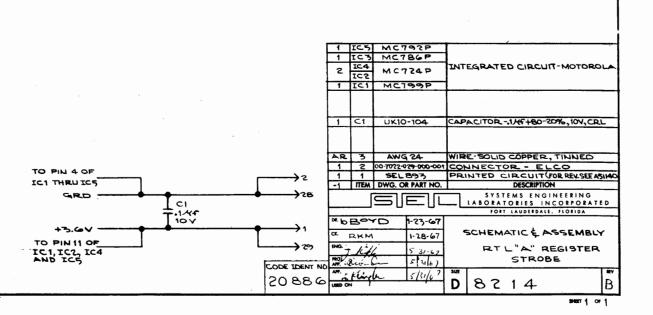


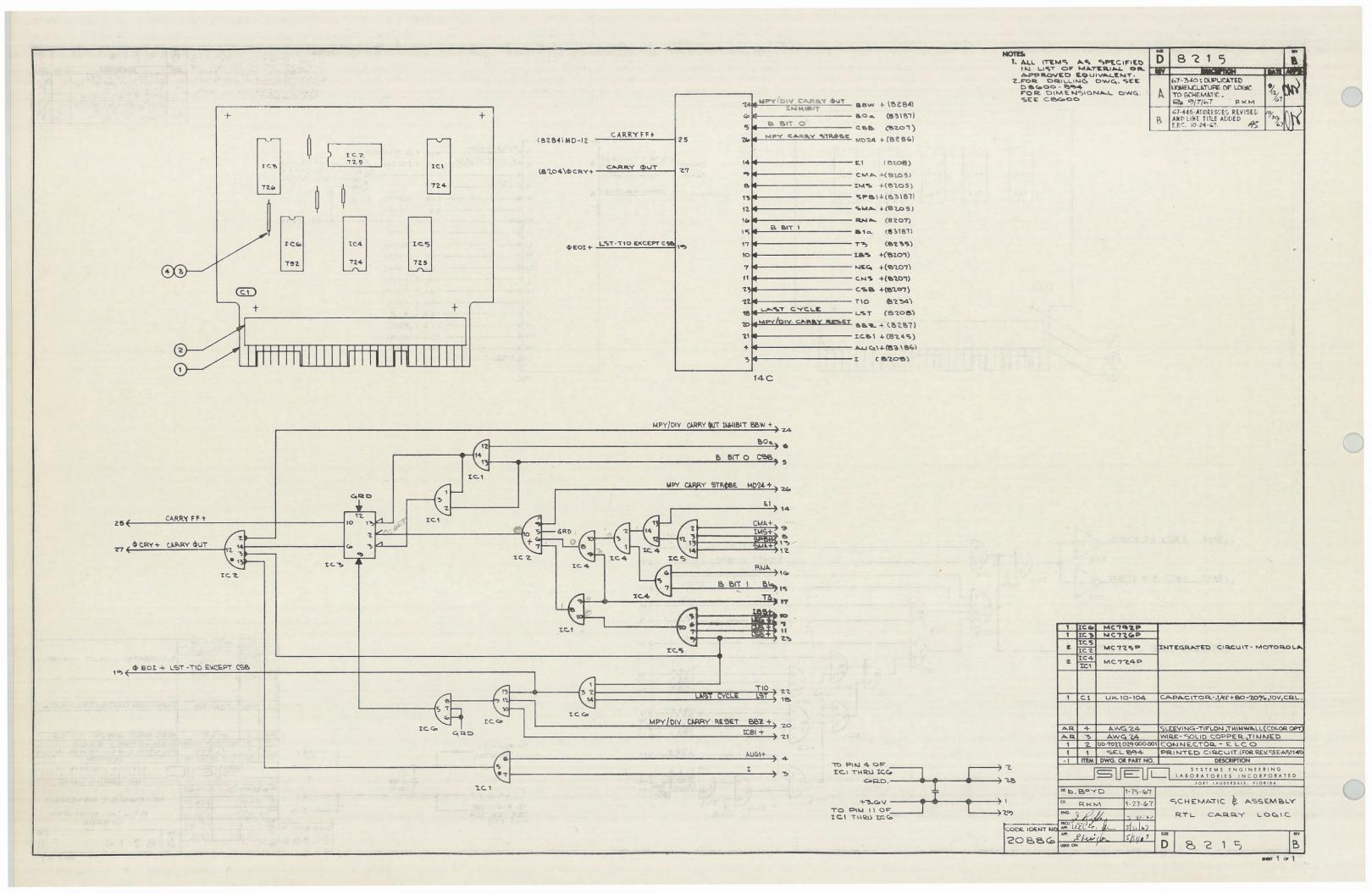


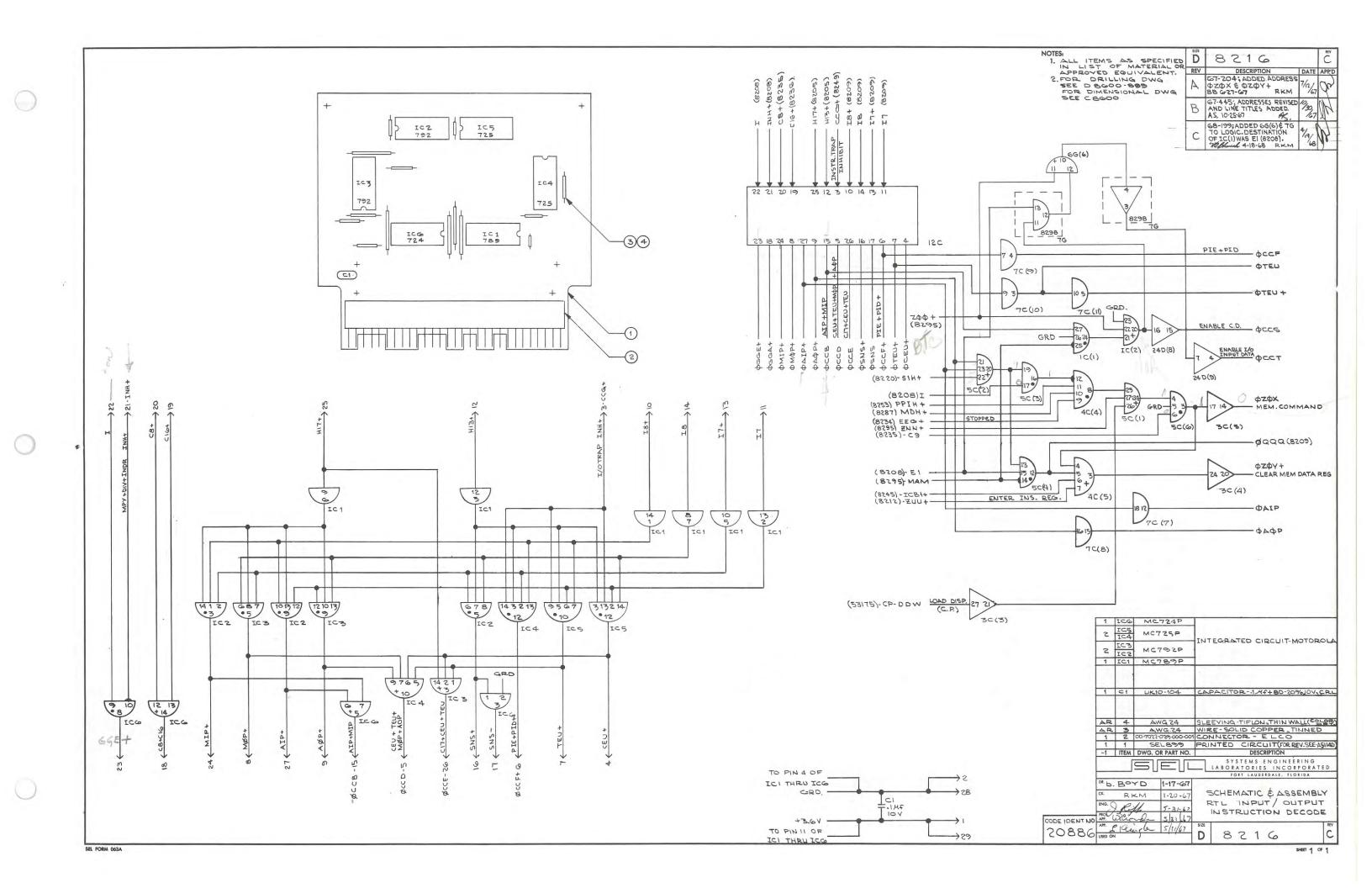


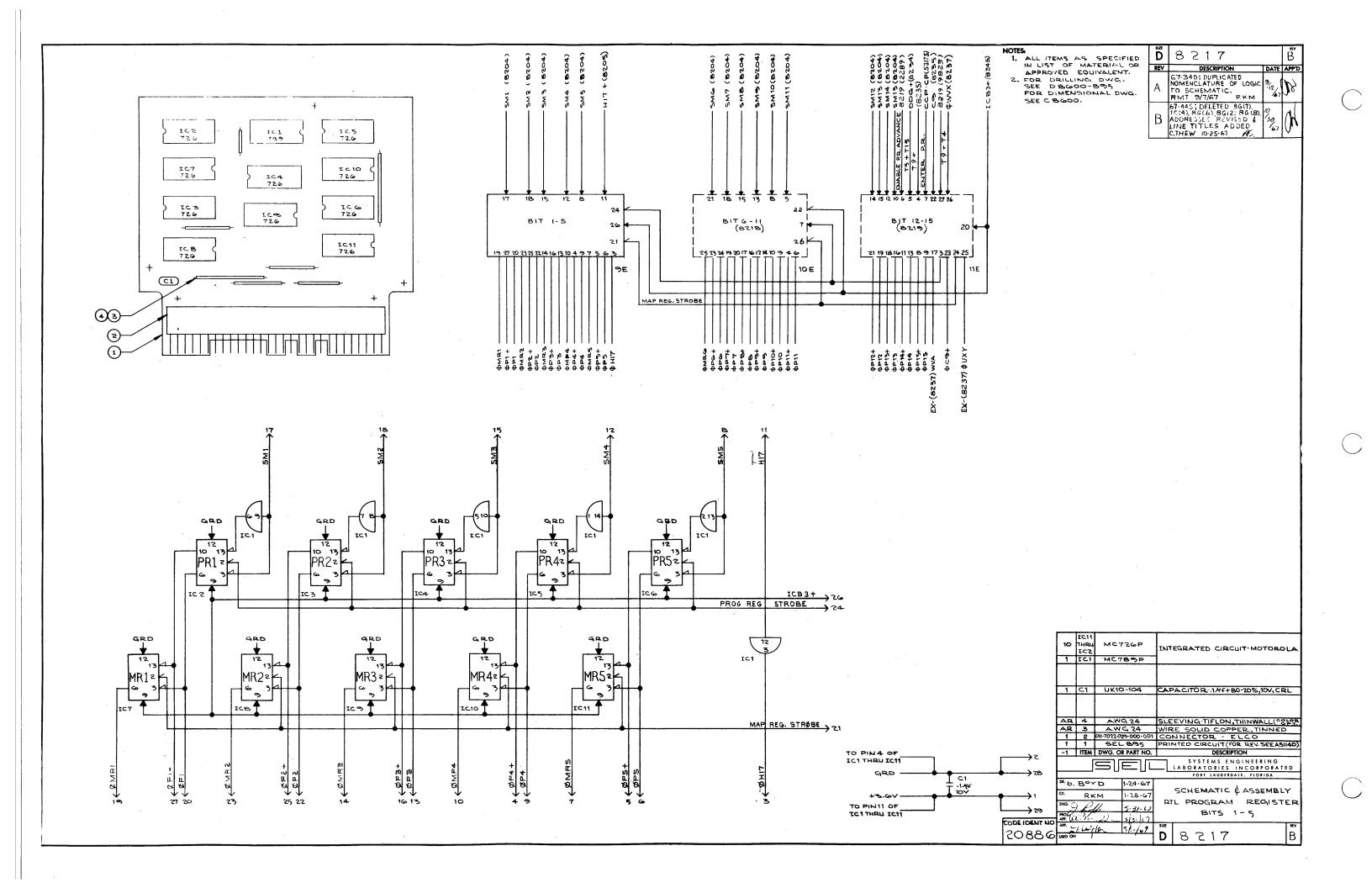


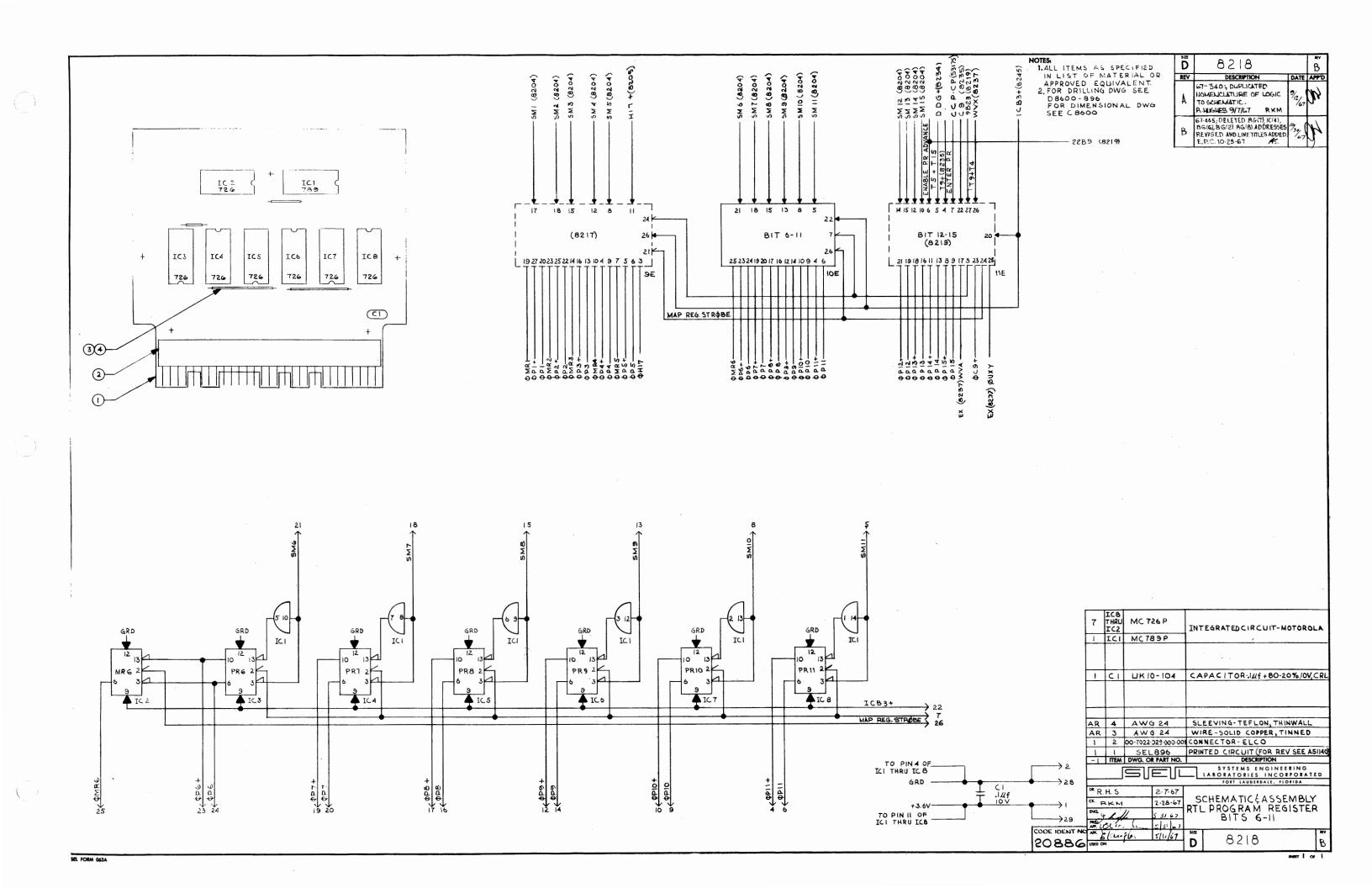


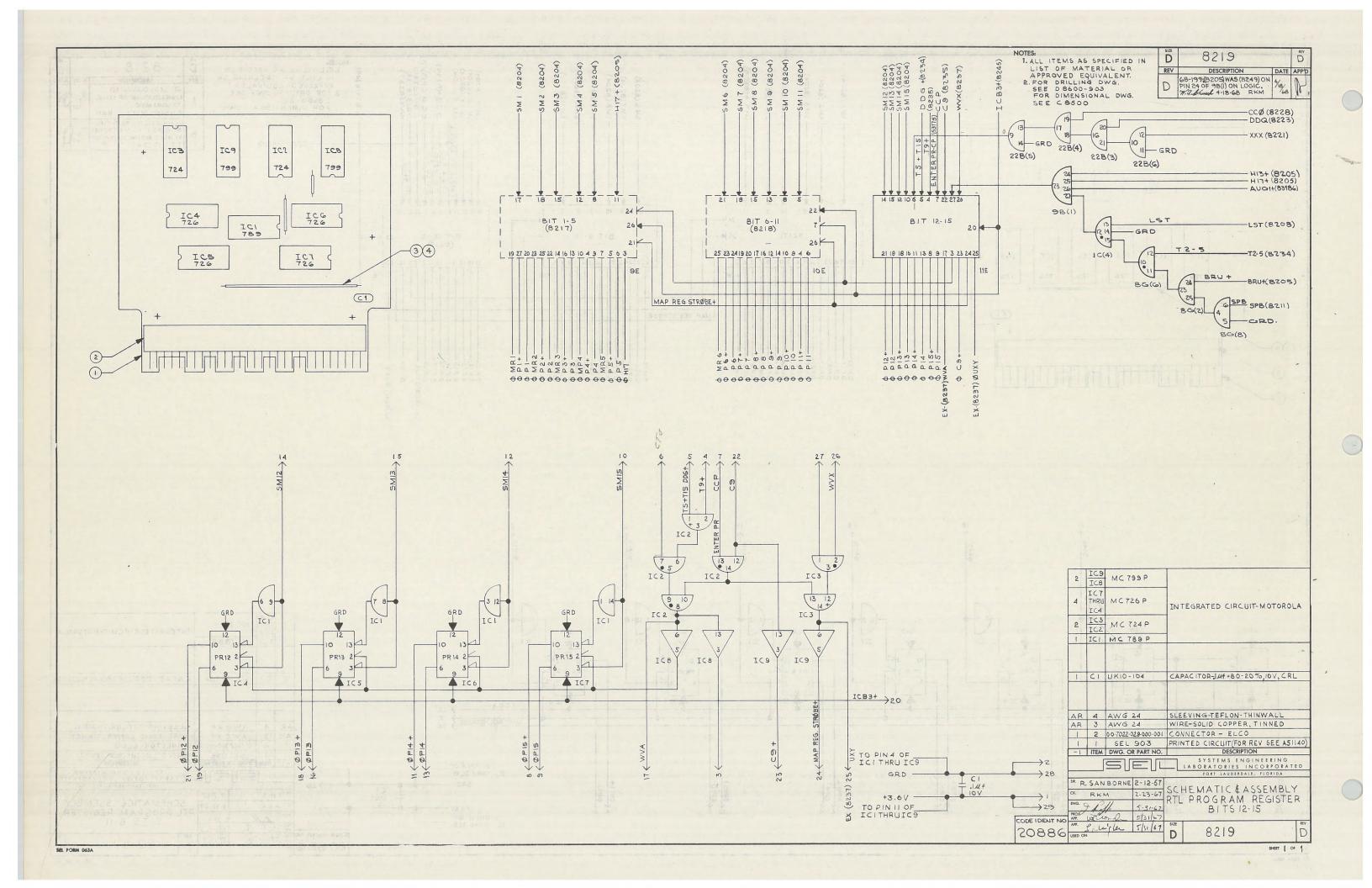


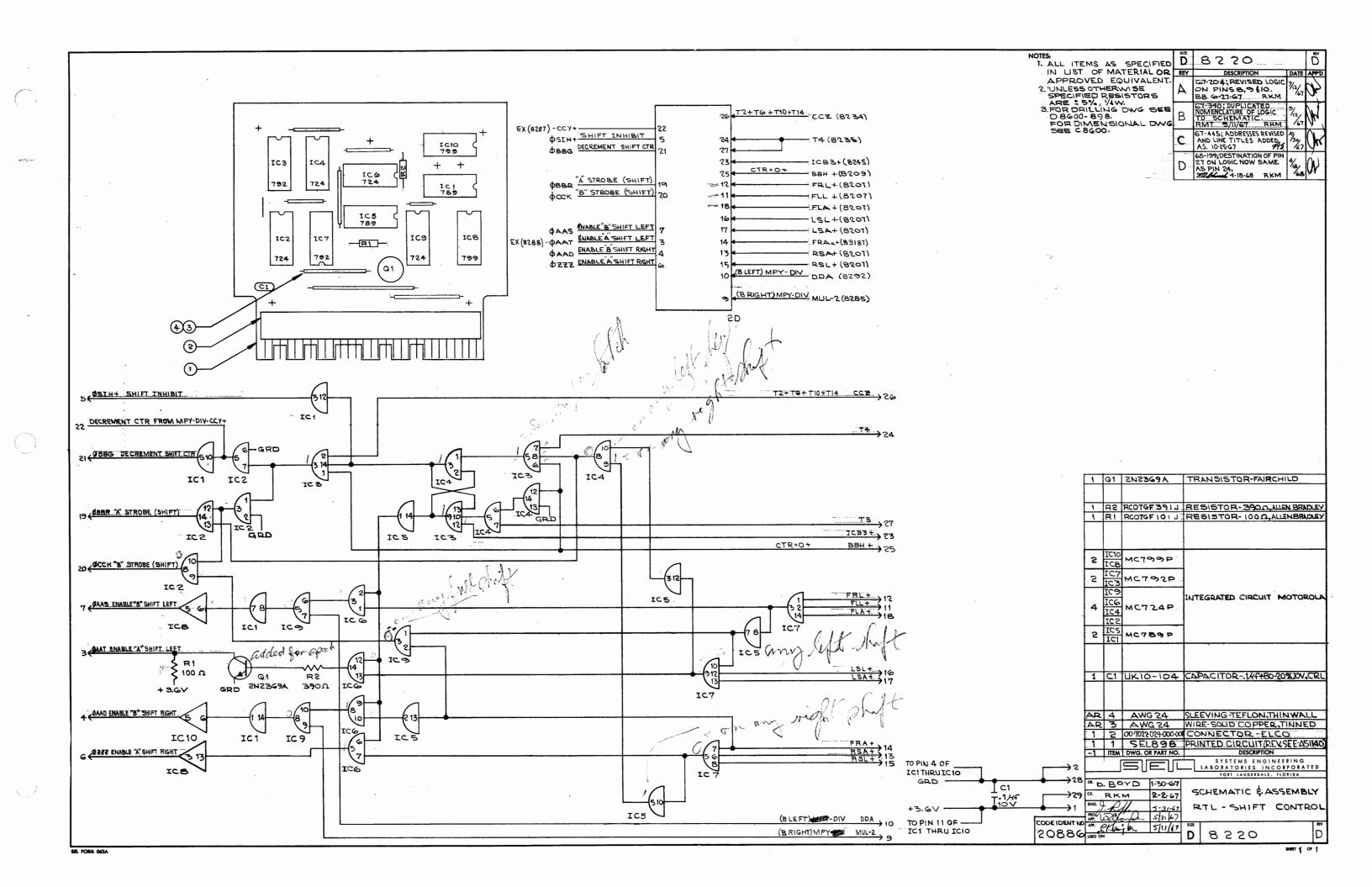


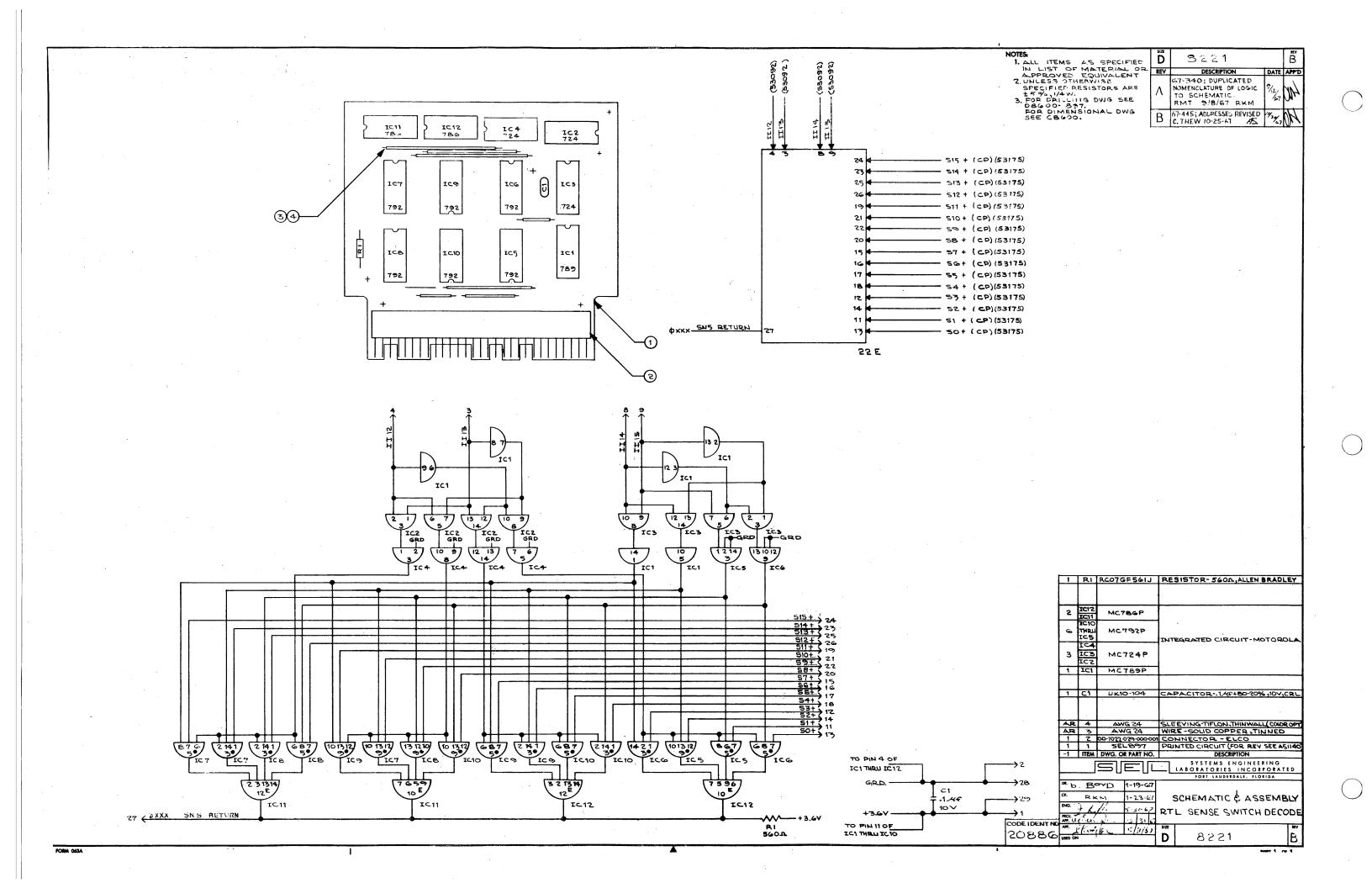


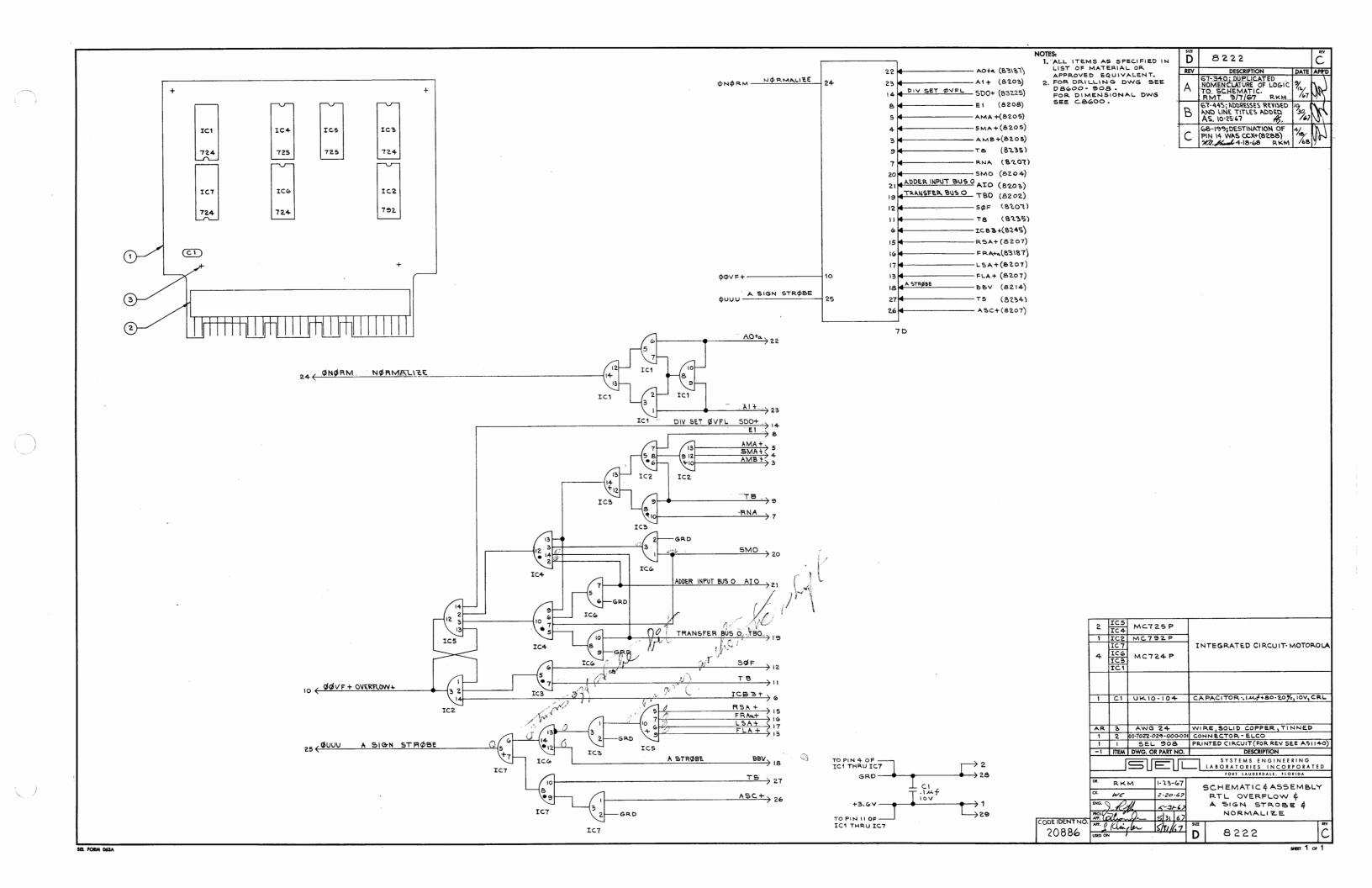


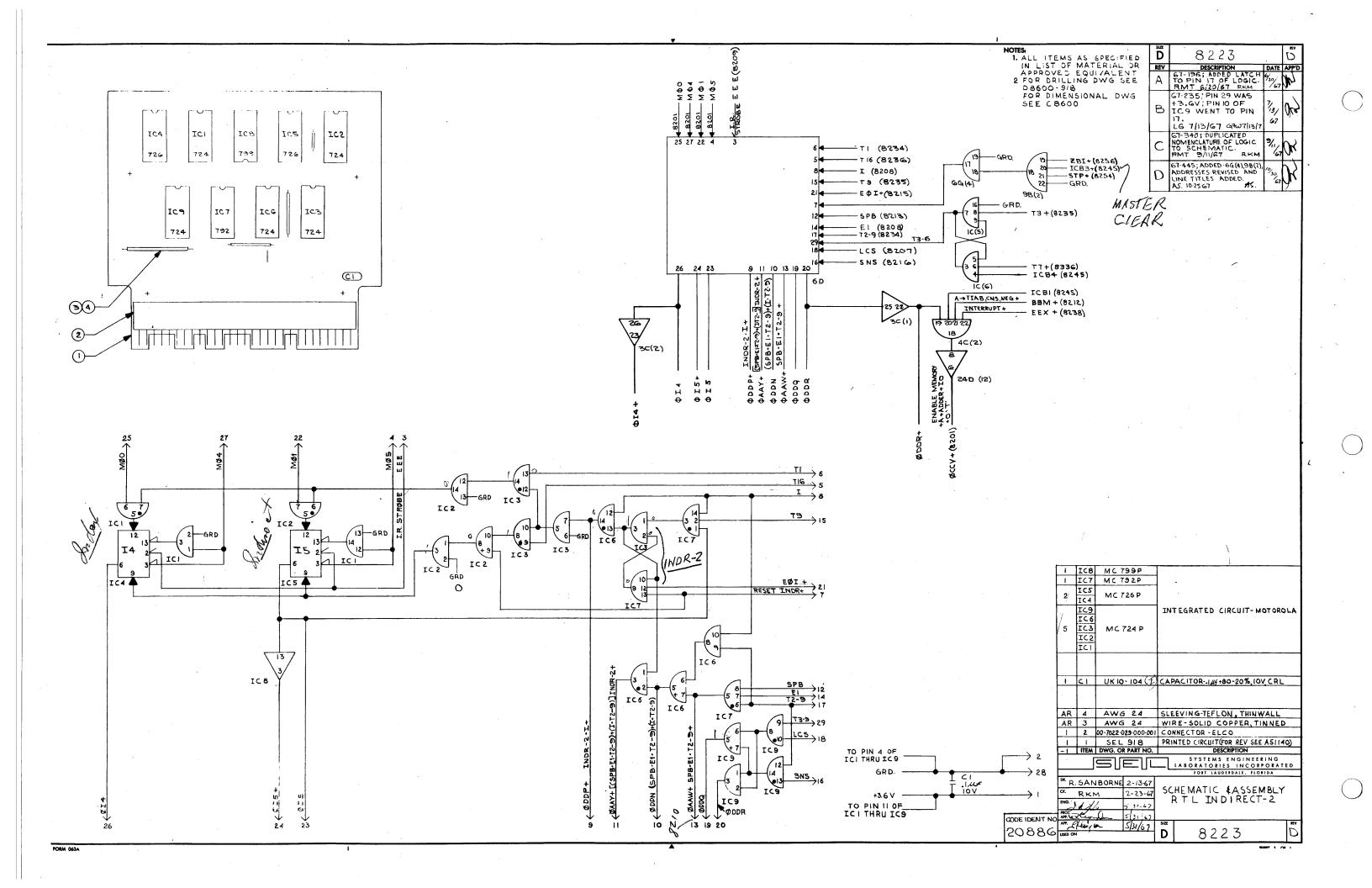


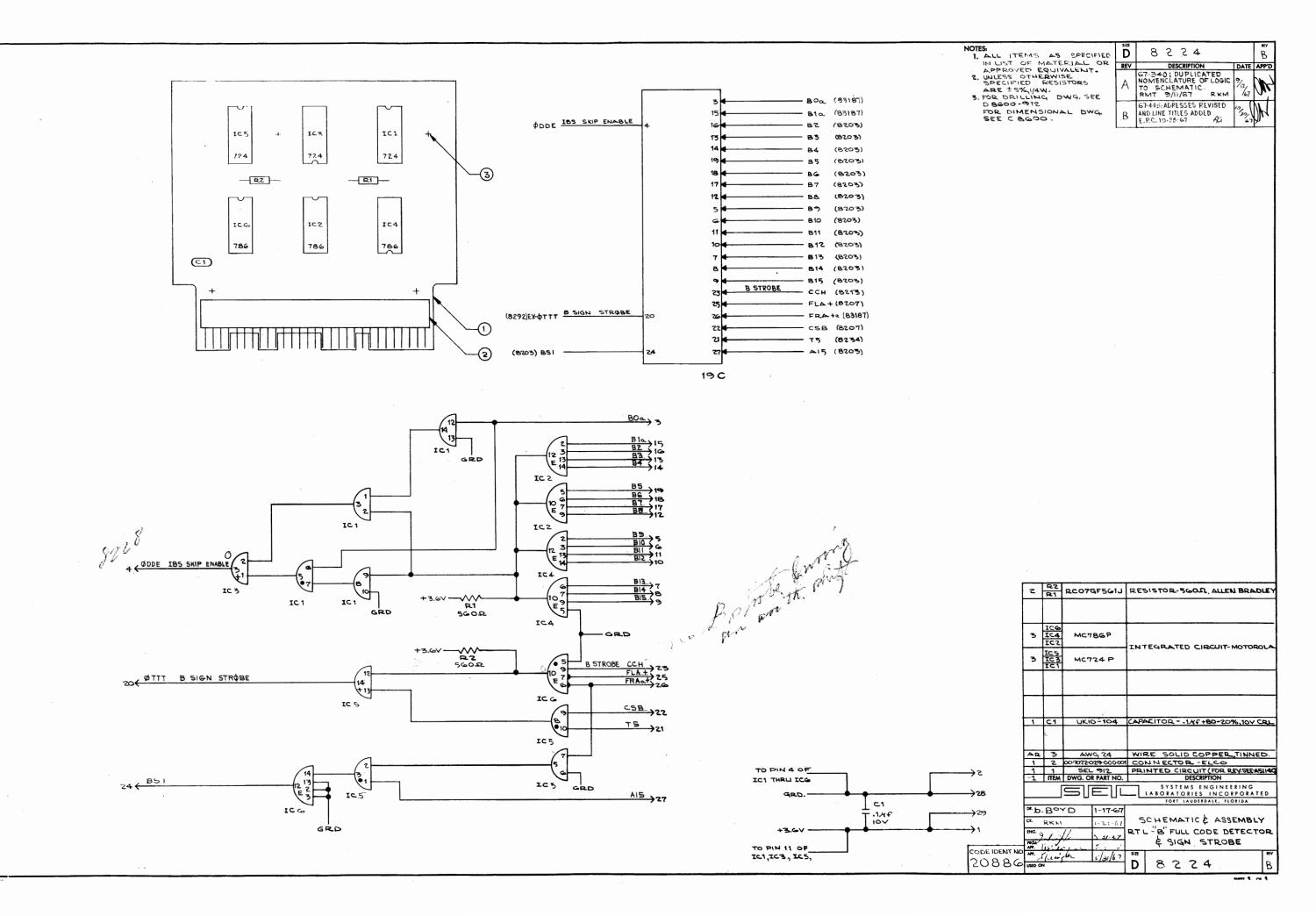


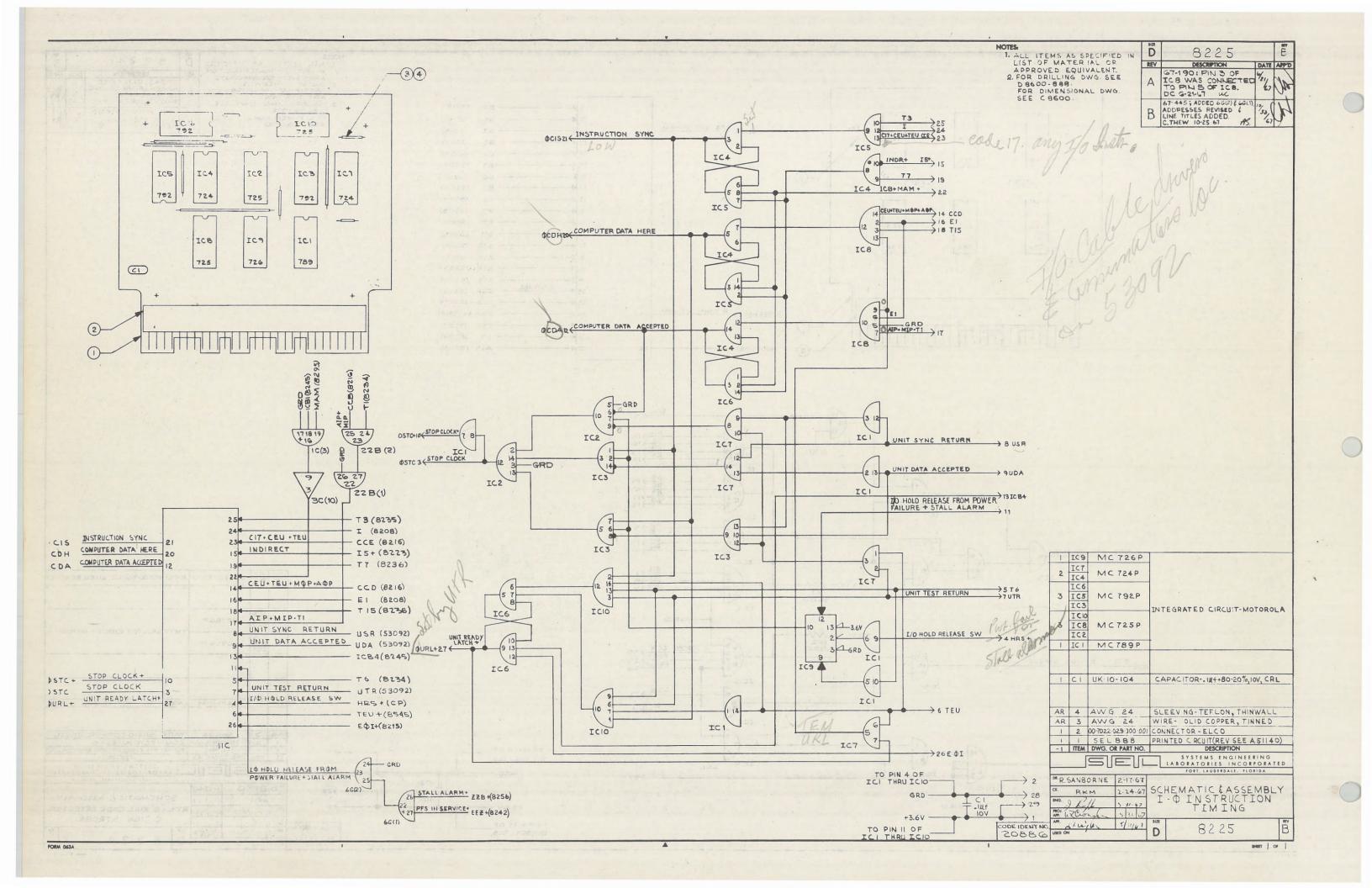


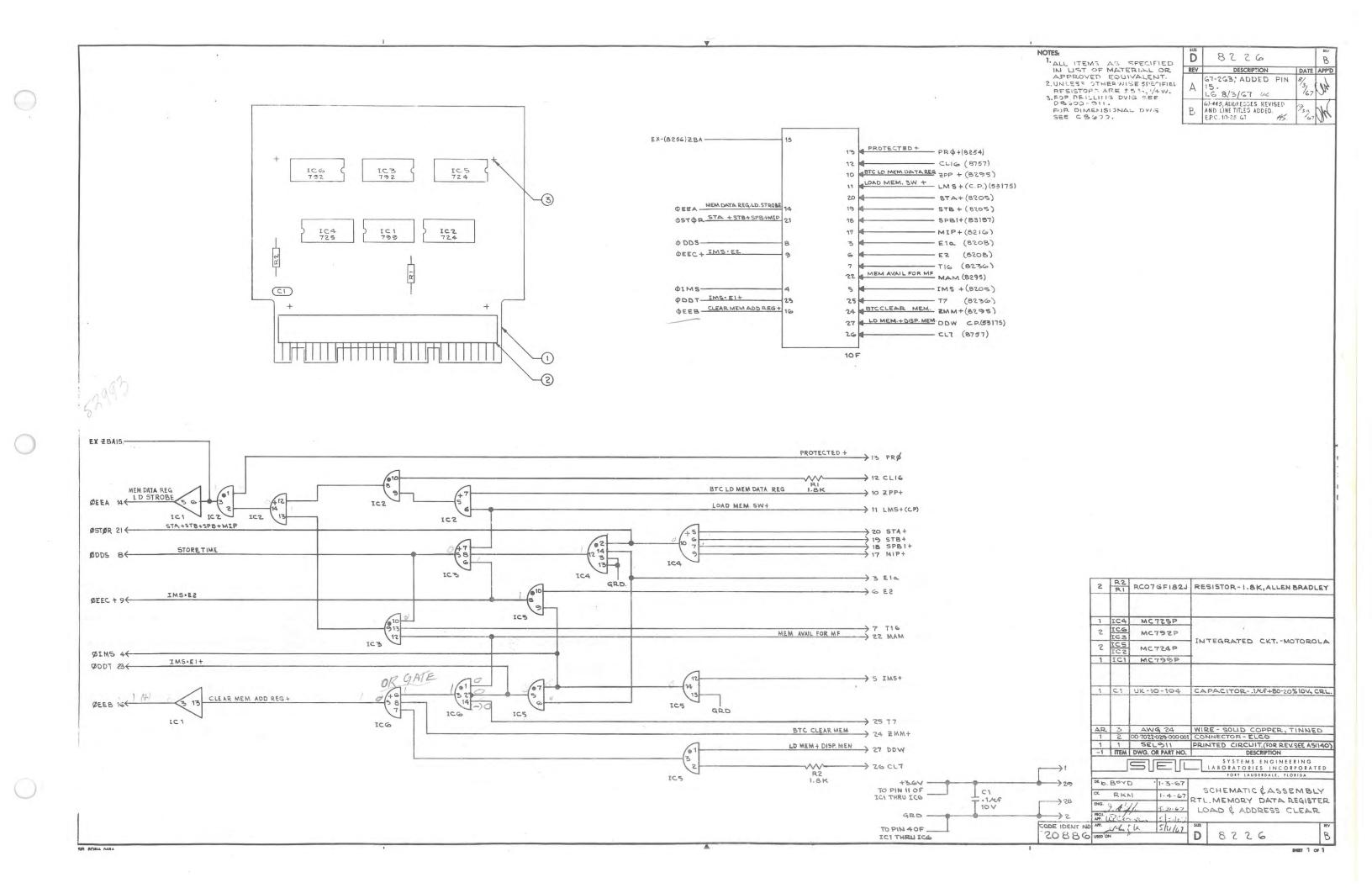


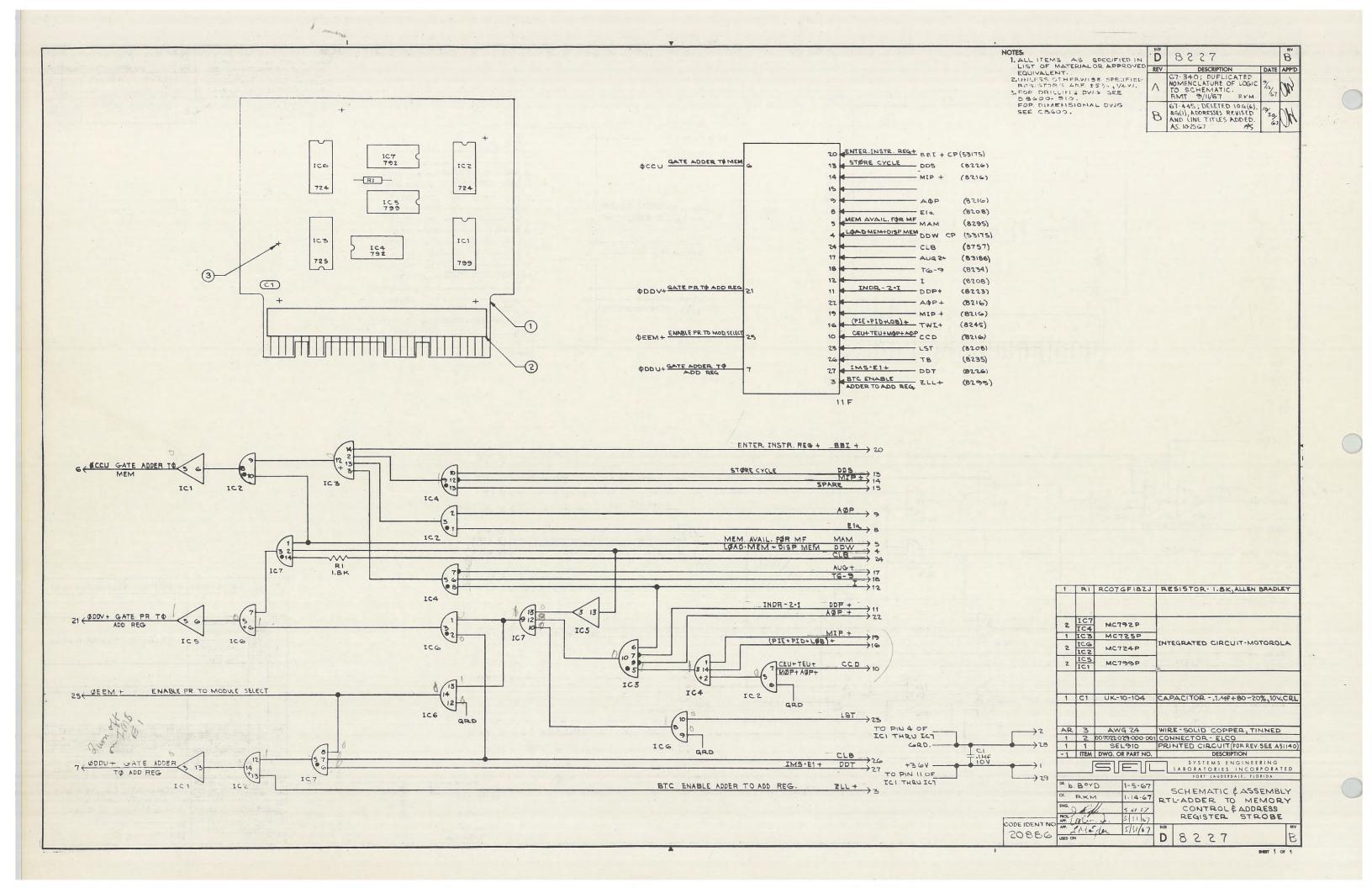


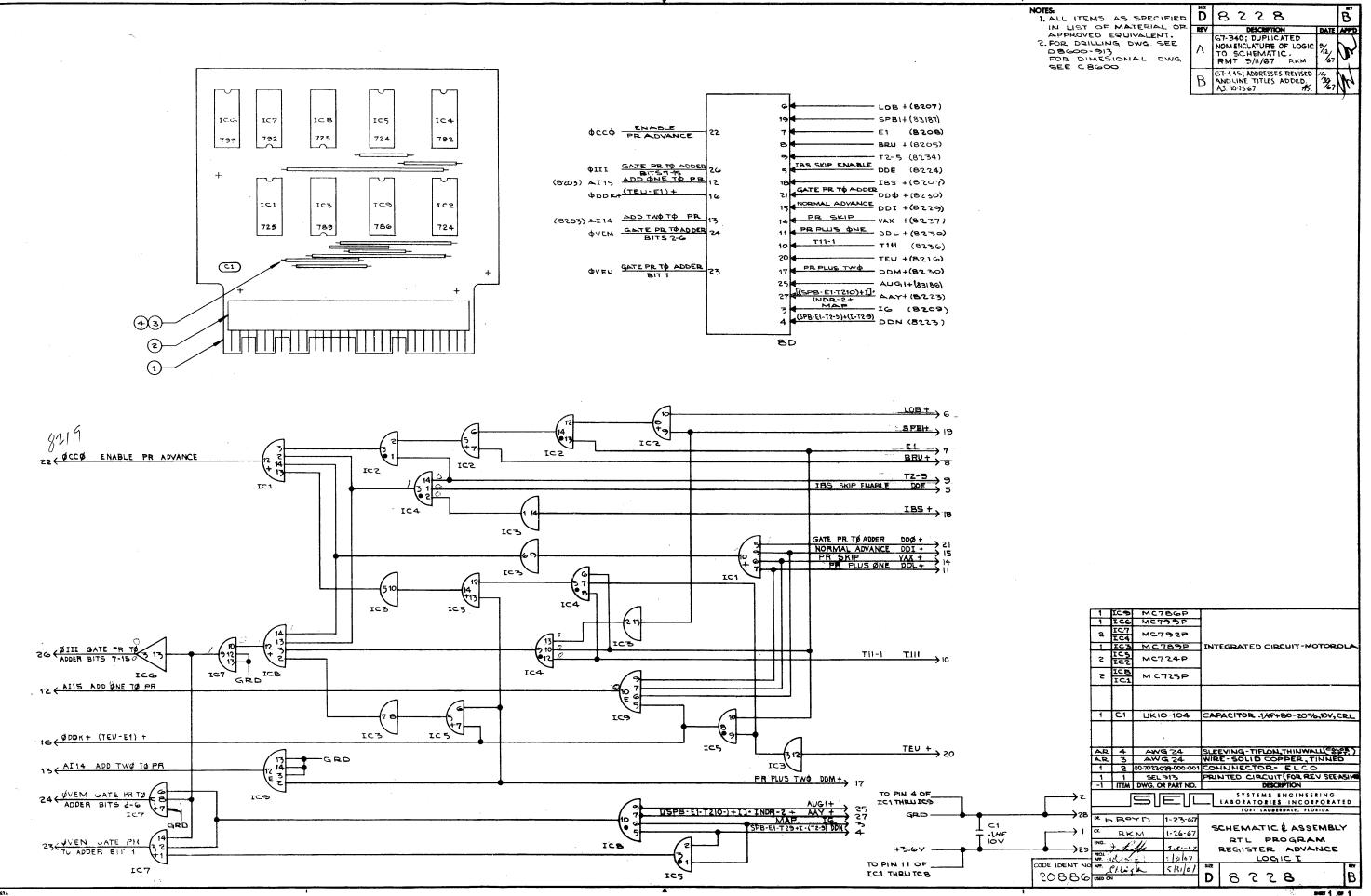


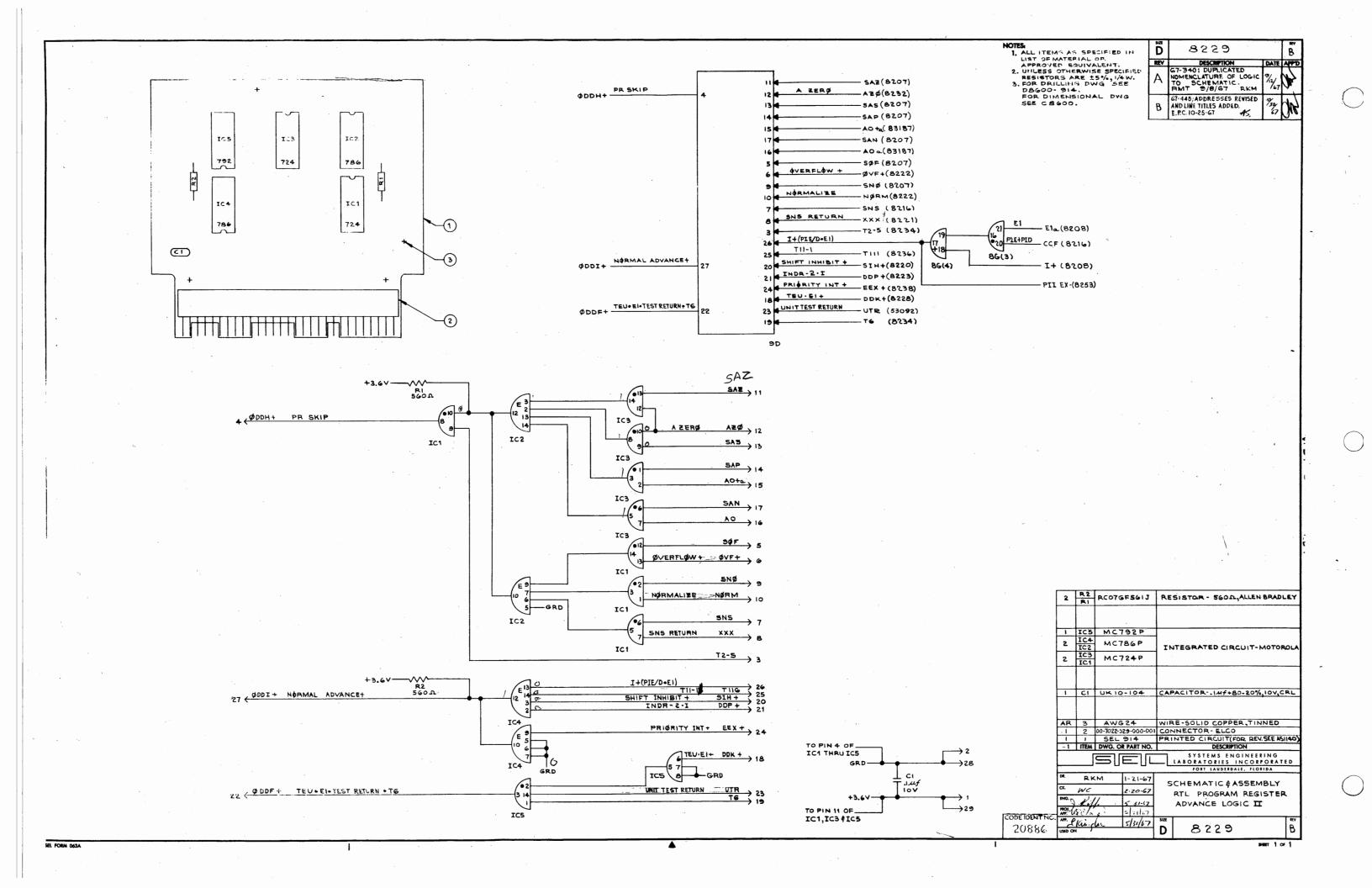


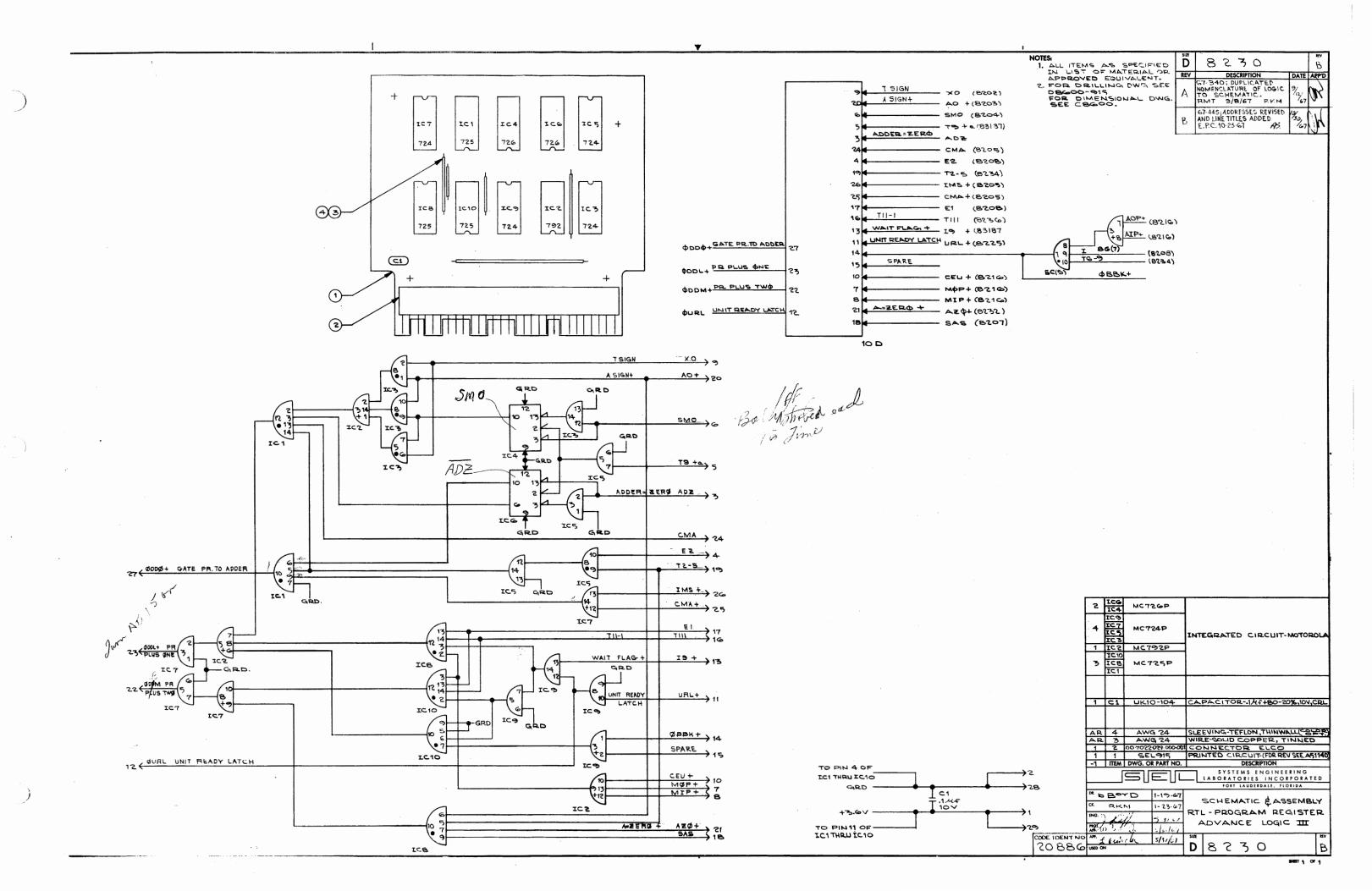


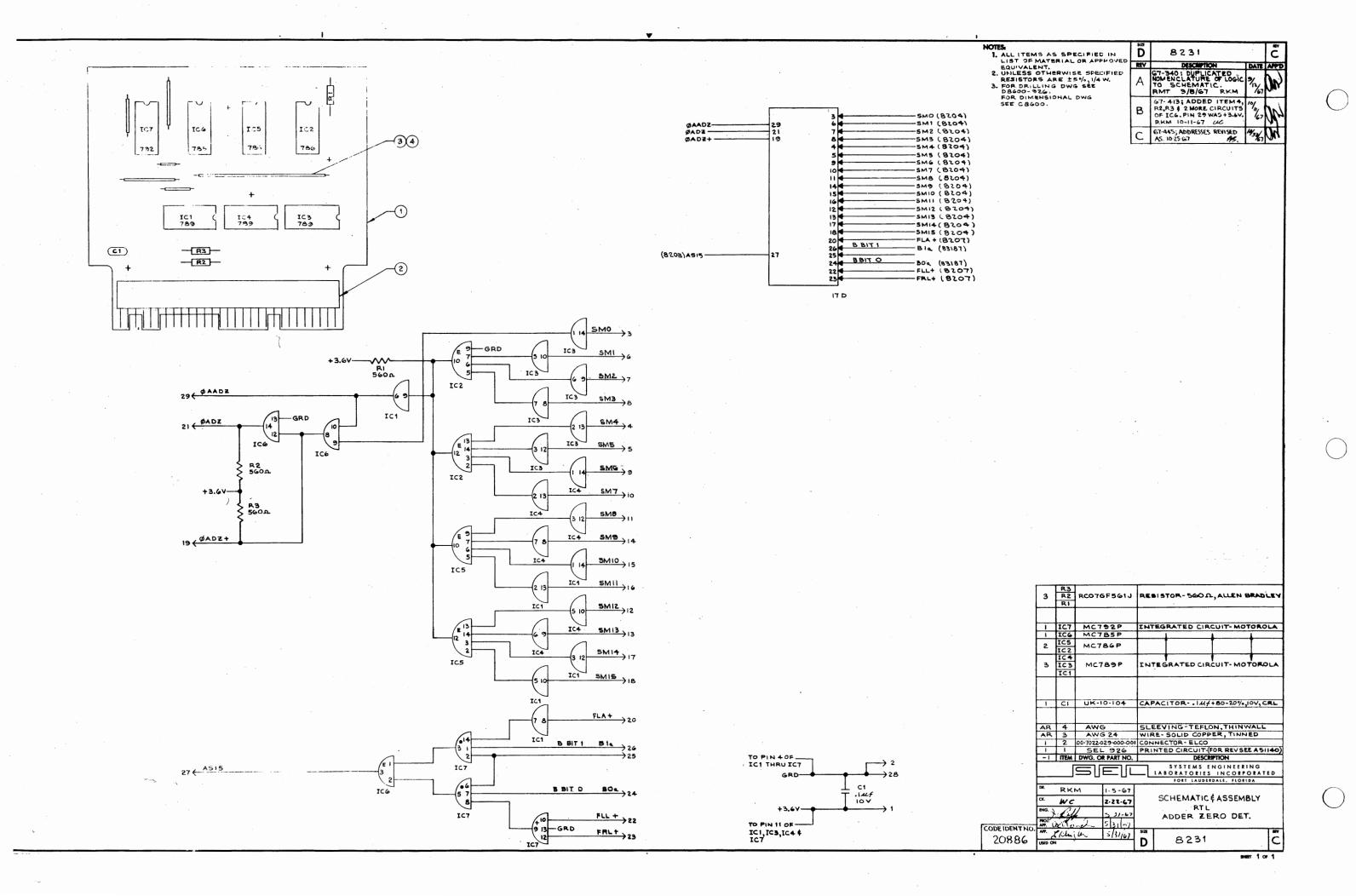


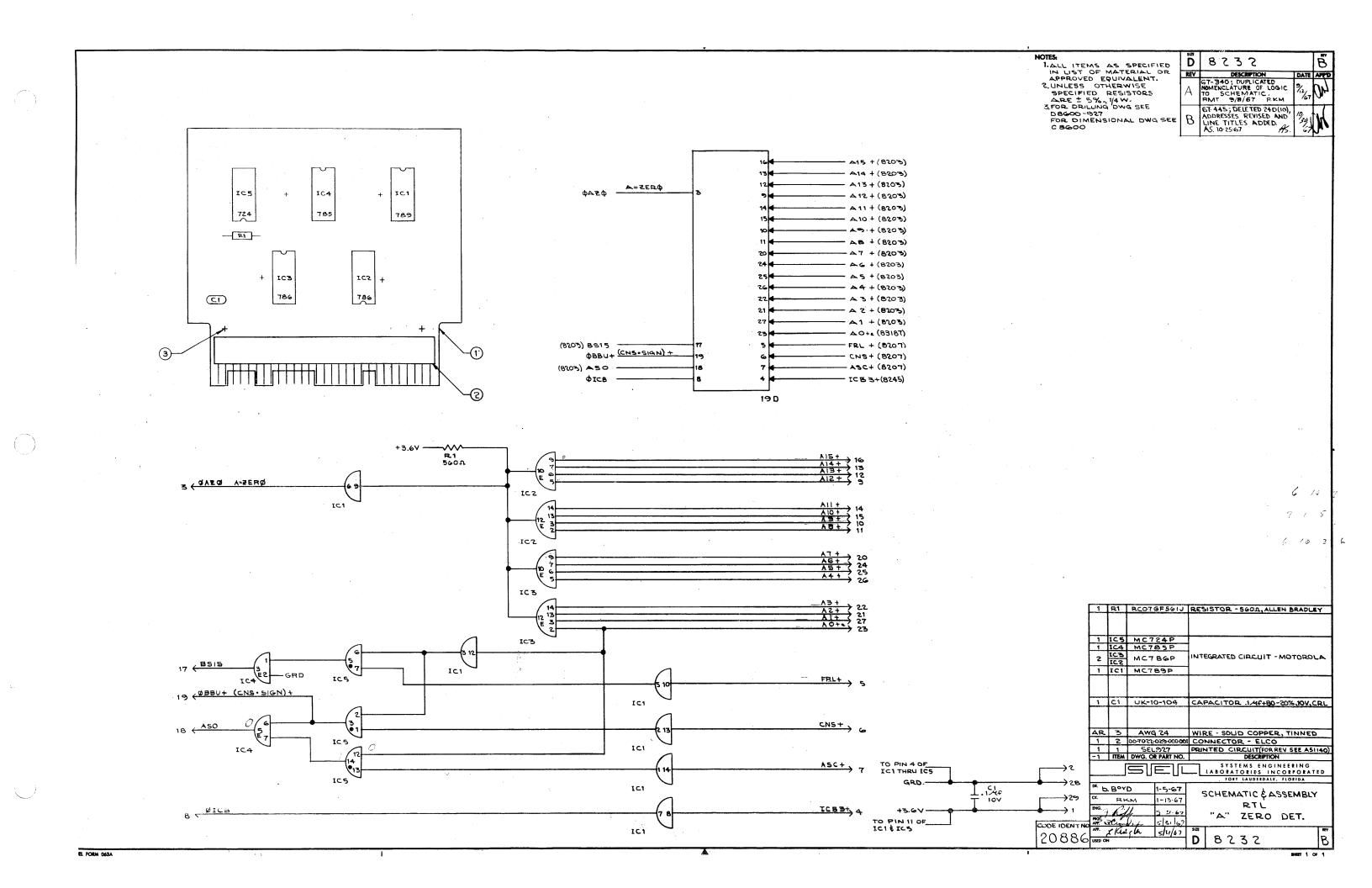


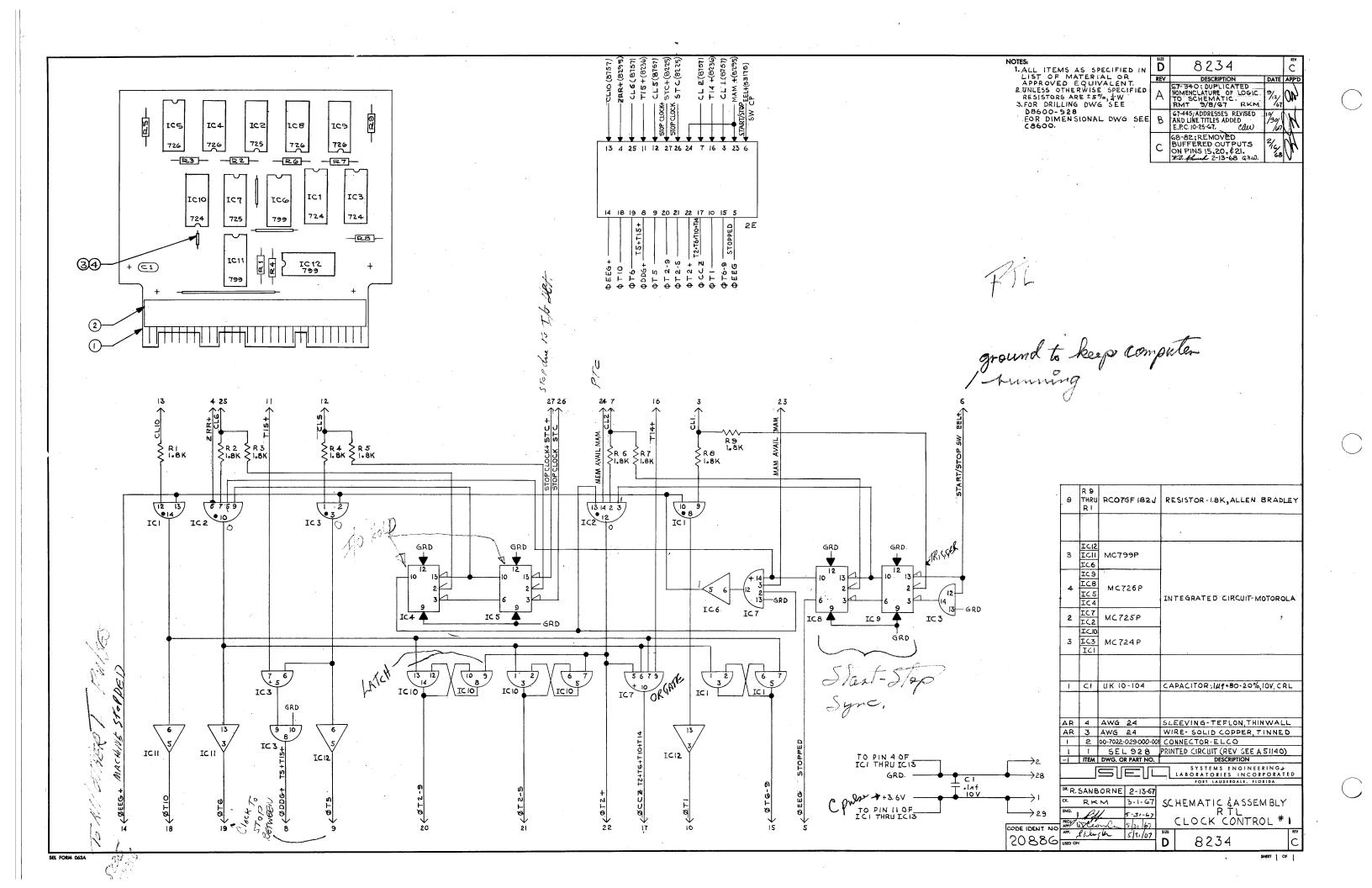


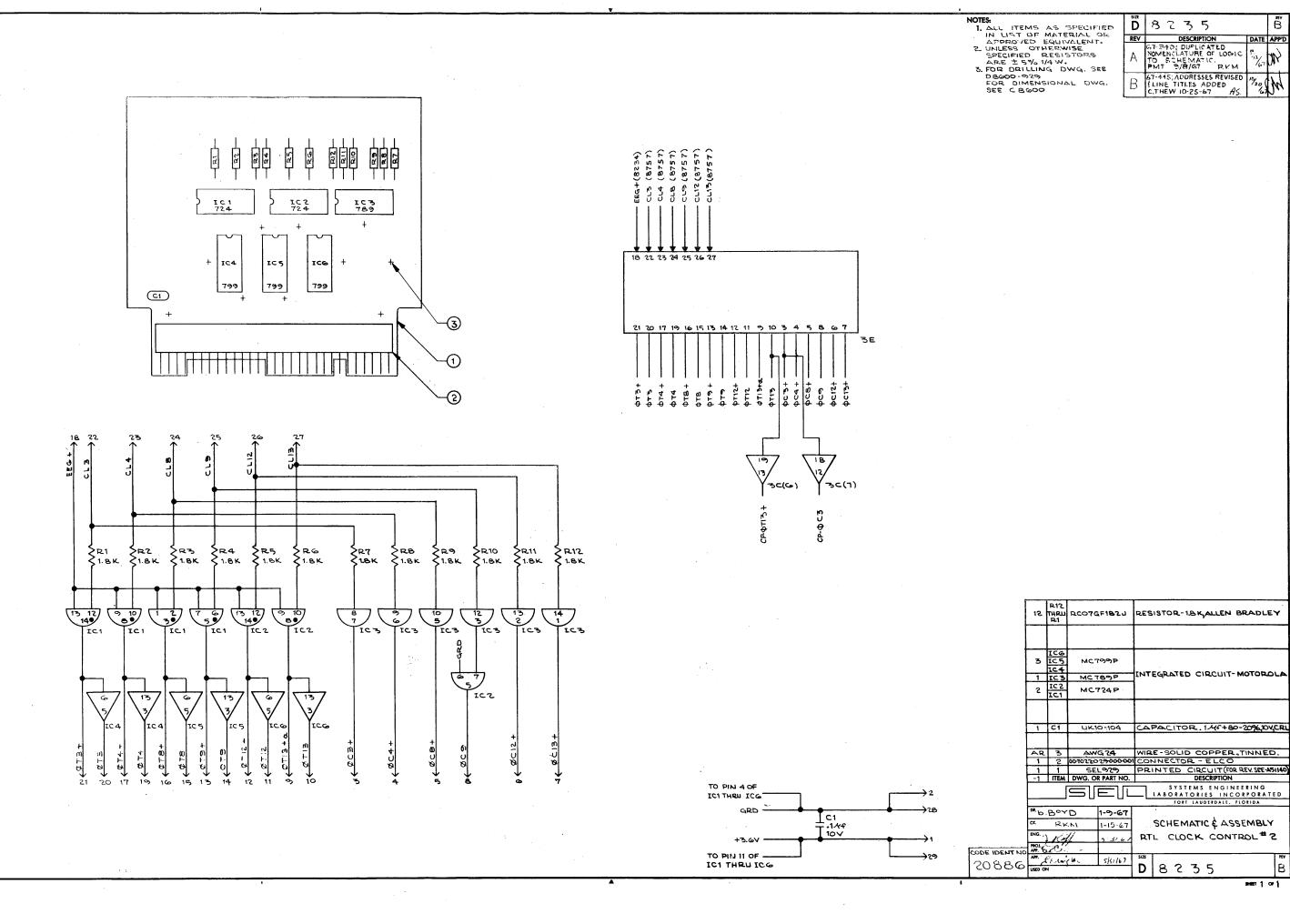






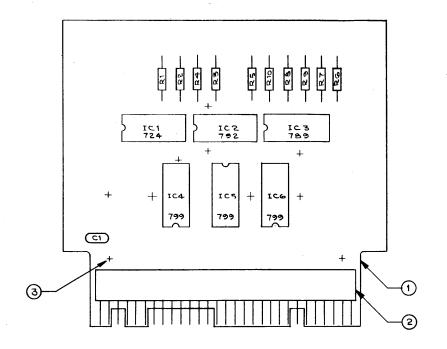


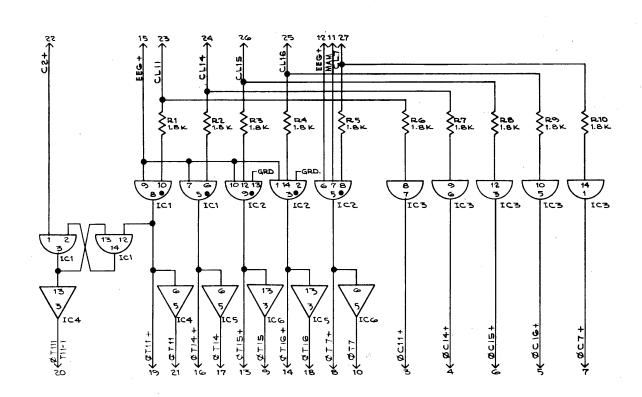


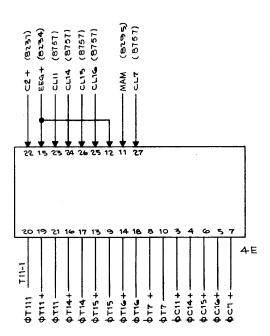


DATE APP'D 12/67 M

SHEET 1 OF 1







NOTES:

1. ALL ITEMS AS SPECIFIED IN LIST OF MATERIAL OR APPROVED EQUIVALENT.

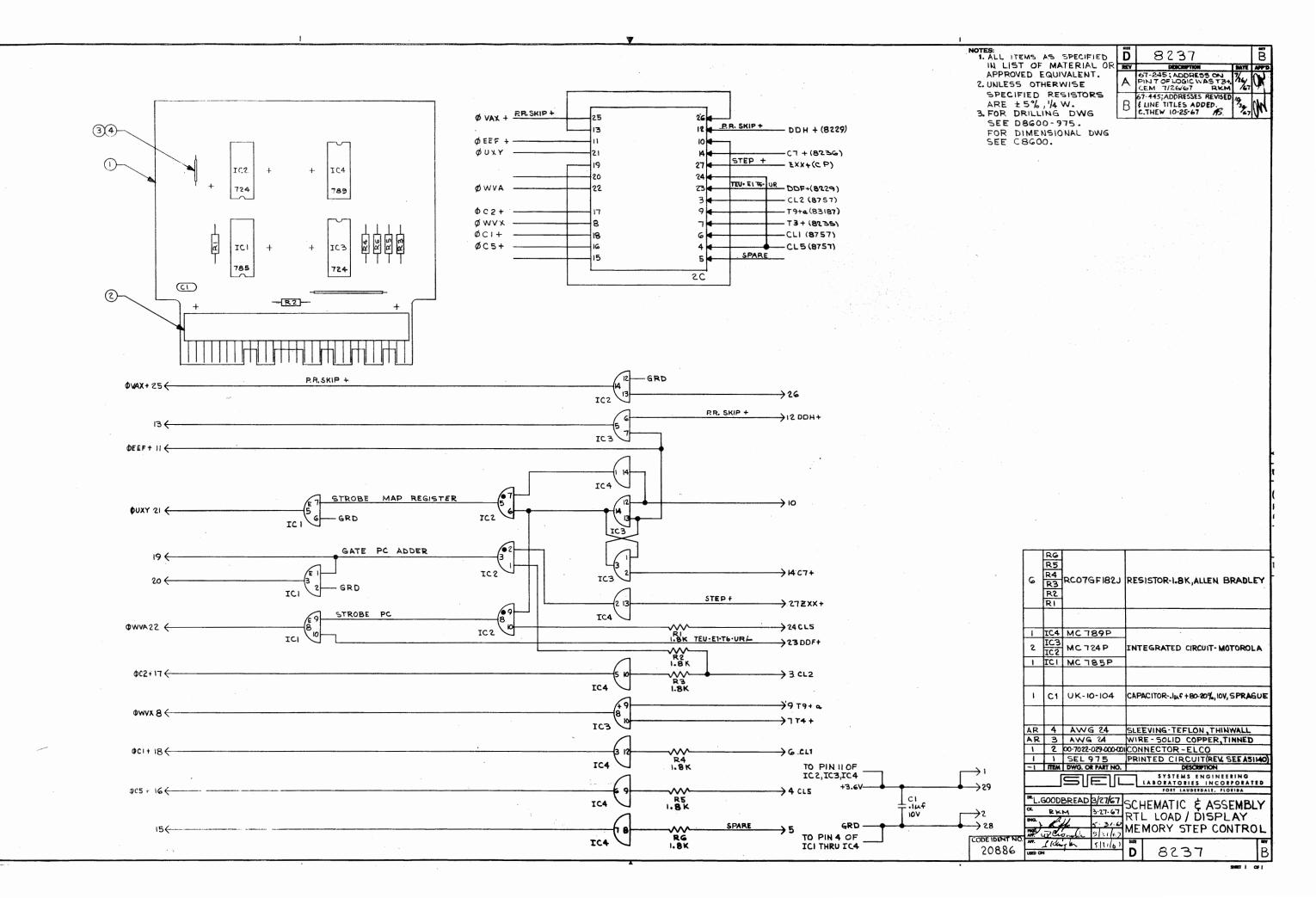
2. UNLESS OTHERWISE SPECIFIED RESISTORS ARE ±5%, 1/4W

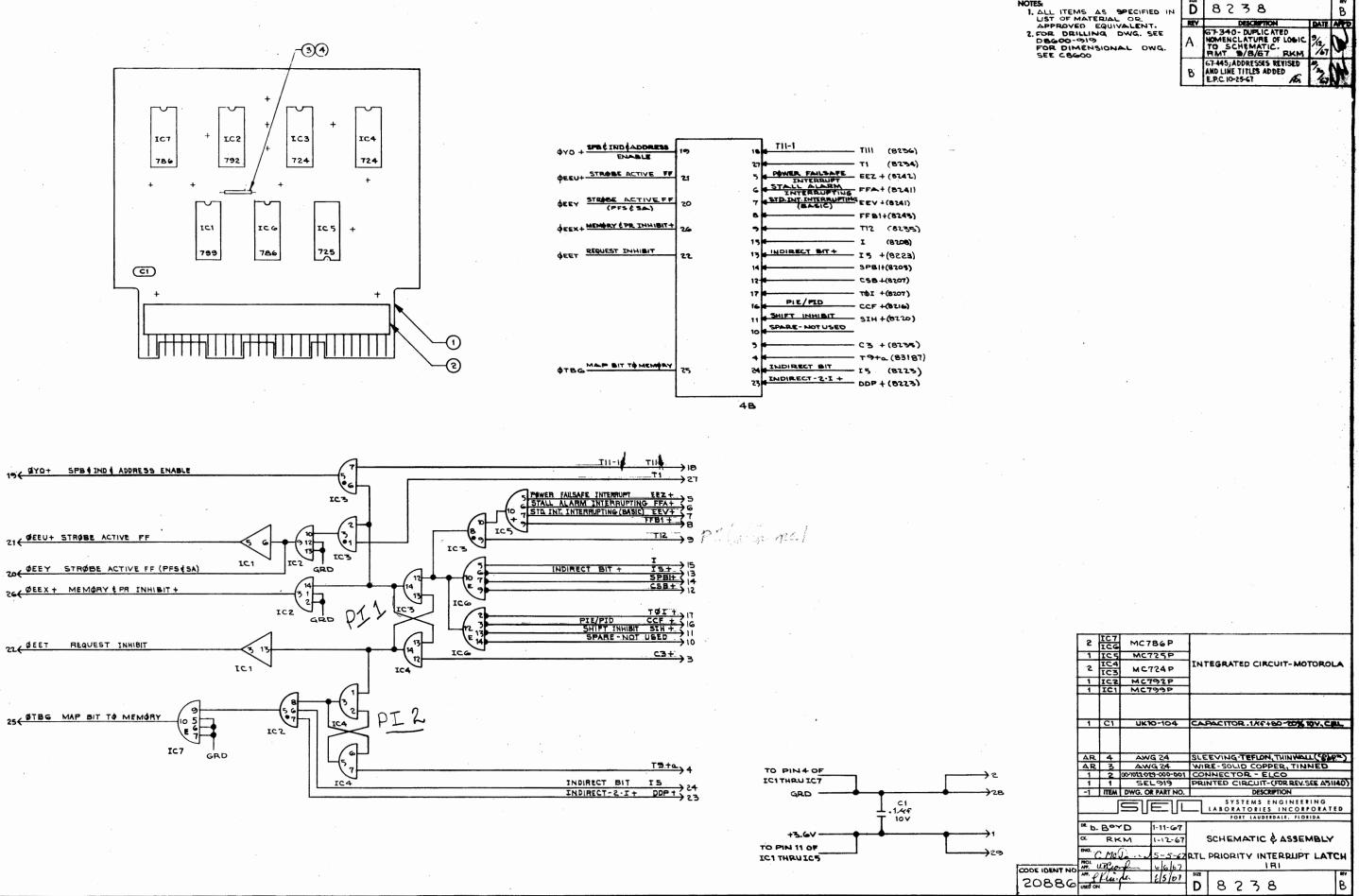
5. FOR DRILLING DWG. SEE D8600-930.
FOR DIMENSIONAL DWG. SEE C8600

D	8236		
REV	DESCRIPTION	DATE	APP'D
Α	GT-340; DUPLICATED NOMENCLATURE OF LOGIC TO SCHEMATIC, RMT 5/8/G7 RKM	%2/ 12/ 107	M
В	67-445; ADDRESSES REVISED AND LINE TITLES ADDED E.P.C.10-25-67	10,30,	OH

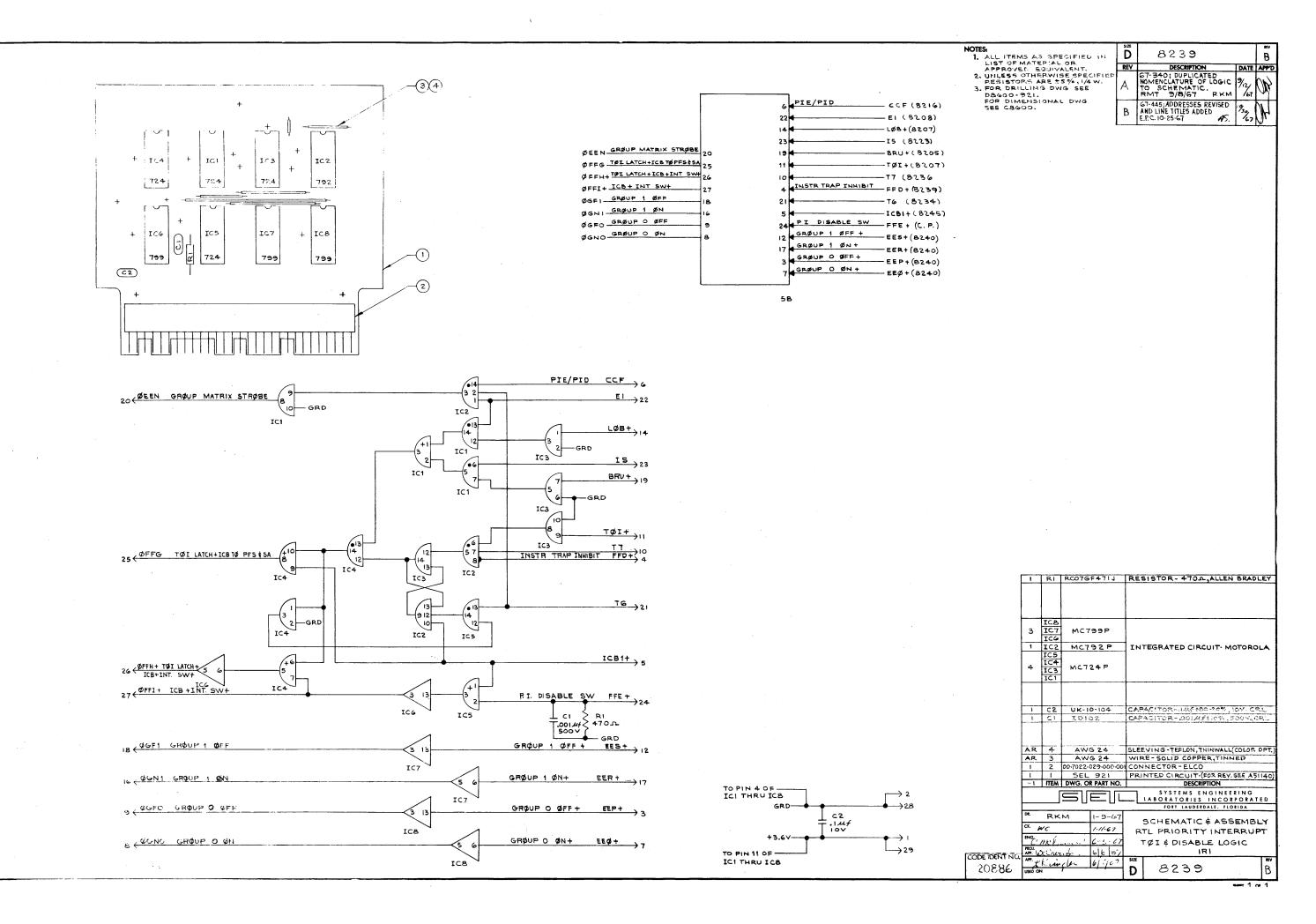
	10	R.10 THRU R.1	RCO7GF182J	RE?	SISTOR-1.8K, ALLEN BRADLEY			
	3	ICG ICS	MC799P					
	1			INT	NTEGRATED CIRCUIT-MOTOROLA			
	1	ICS	MC792P]				
٠.	1	IC1	MC724P	<u> </u>	·			
					,			
	1	C1	UK10-104	CA	PACITOR-1147+80-20%,10VCRL			
	l							
	AR	3	AWG 24		E - SOLID COPPER, TINNED			
	1	5			INECTOR-ELCO			
	1	1	SEL930	PRII	NTED CIRCUIT (FOR REV. SEE-AS1140)			
	-1	ITEM	DWG. OR PART NO.	<u> </u>	DESCRIPTION			
					SYSTEMS ENGINEERING LABORATORIES INCORPORATED FORT LAUDERDALE, FLORIDA			
		Box			COURT A TIEND A ACCESSED			
	CK.	RK	M 1-12-67		SCHEMATIC & ASSEMBLY			
	PROL.	TO CO	1/2 5 31.67	R.	TL- CLOCK CONTROL#3			
ODE IDENT NO 2880S	APP.	Lich	,	SIZE	8236 B			

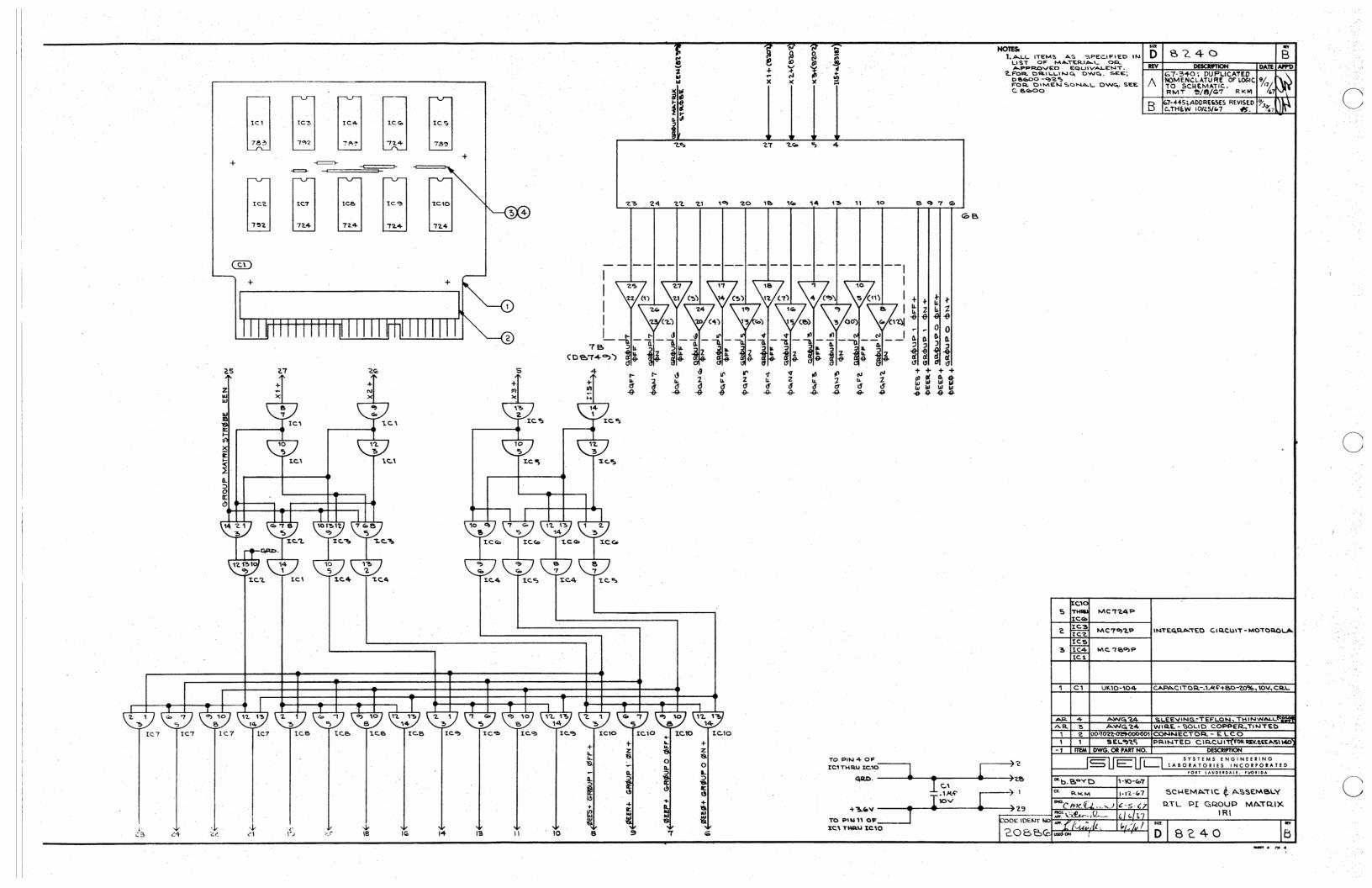
TO PIN 4 OF_ IC1 THRU ICG I c1 I .1.4.f I 10.V TO PIN 11 OF ICITHRU ICG

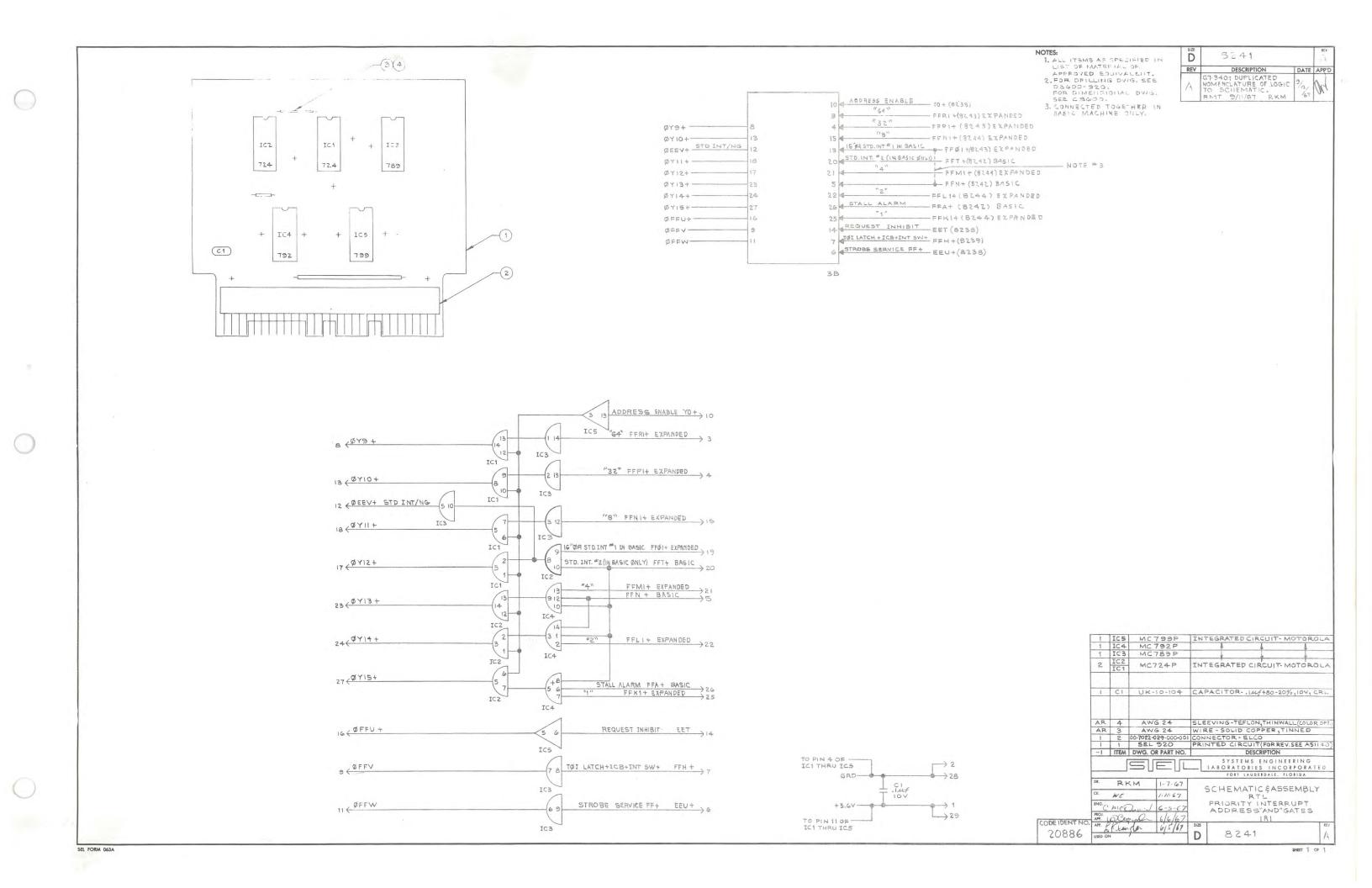




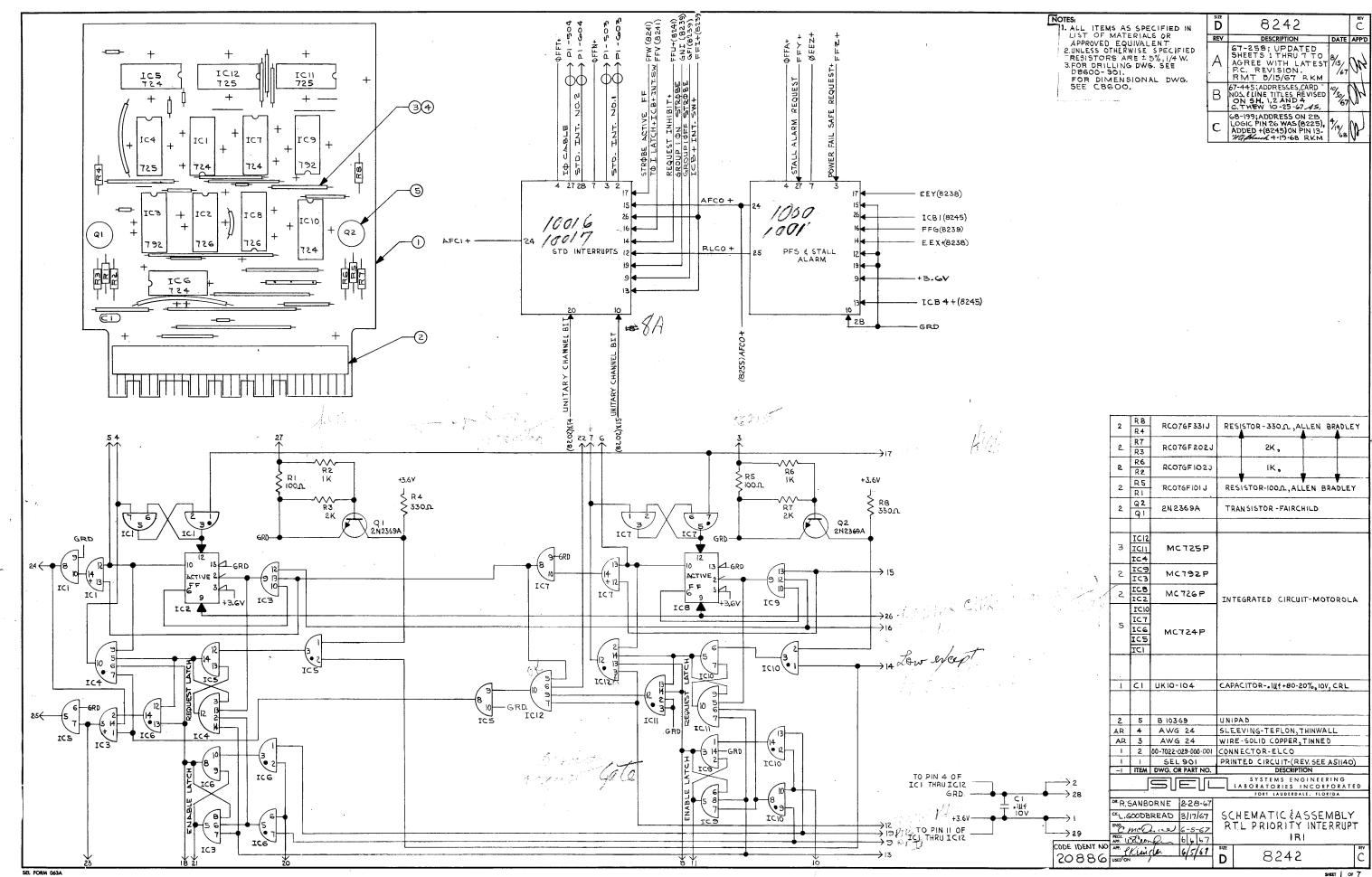
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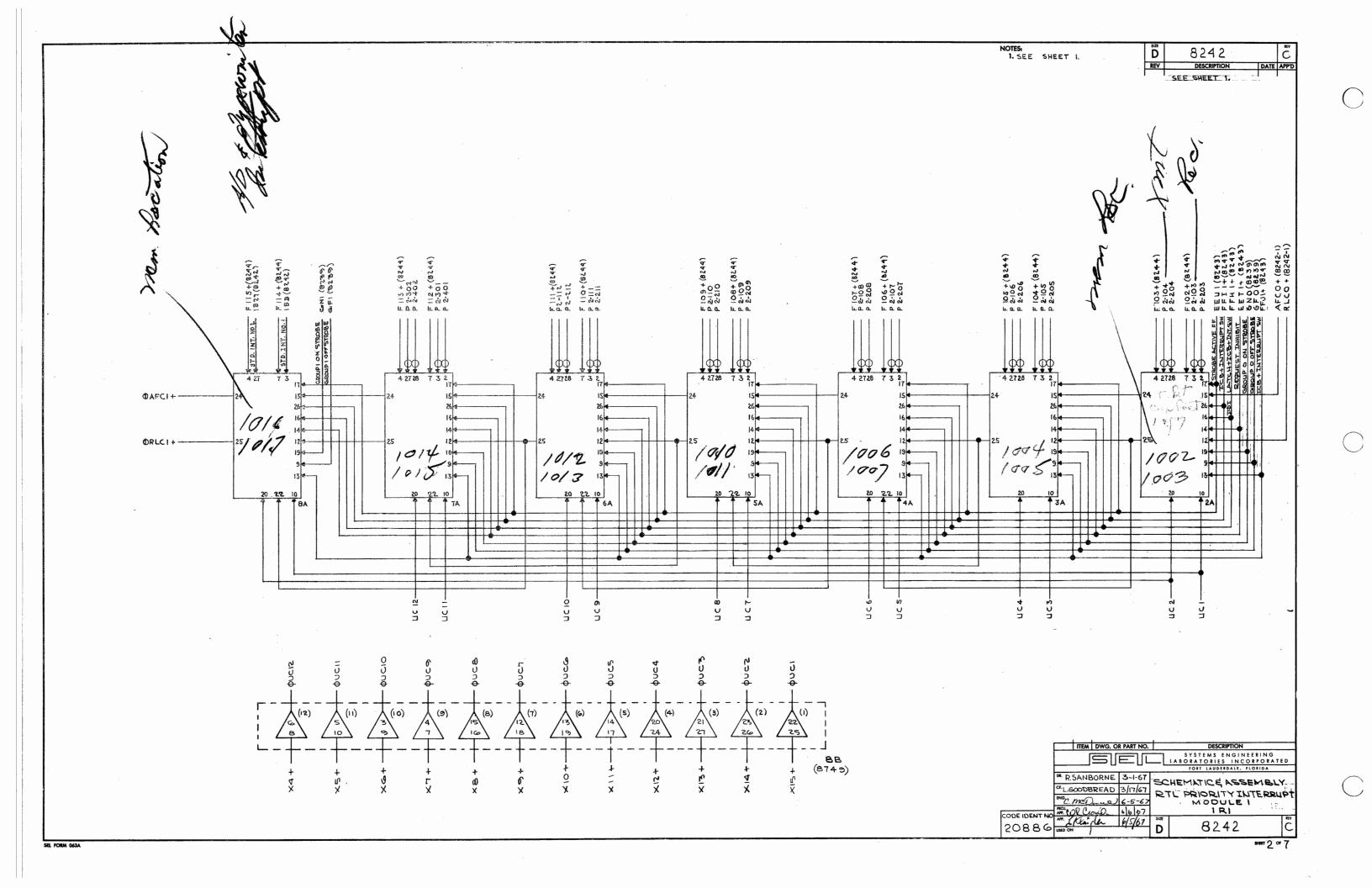


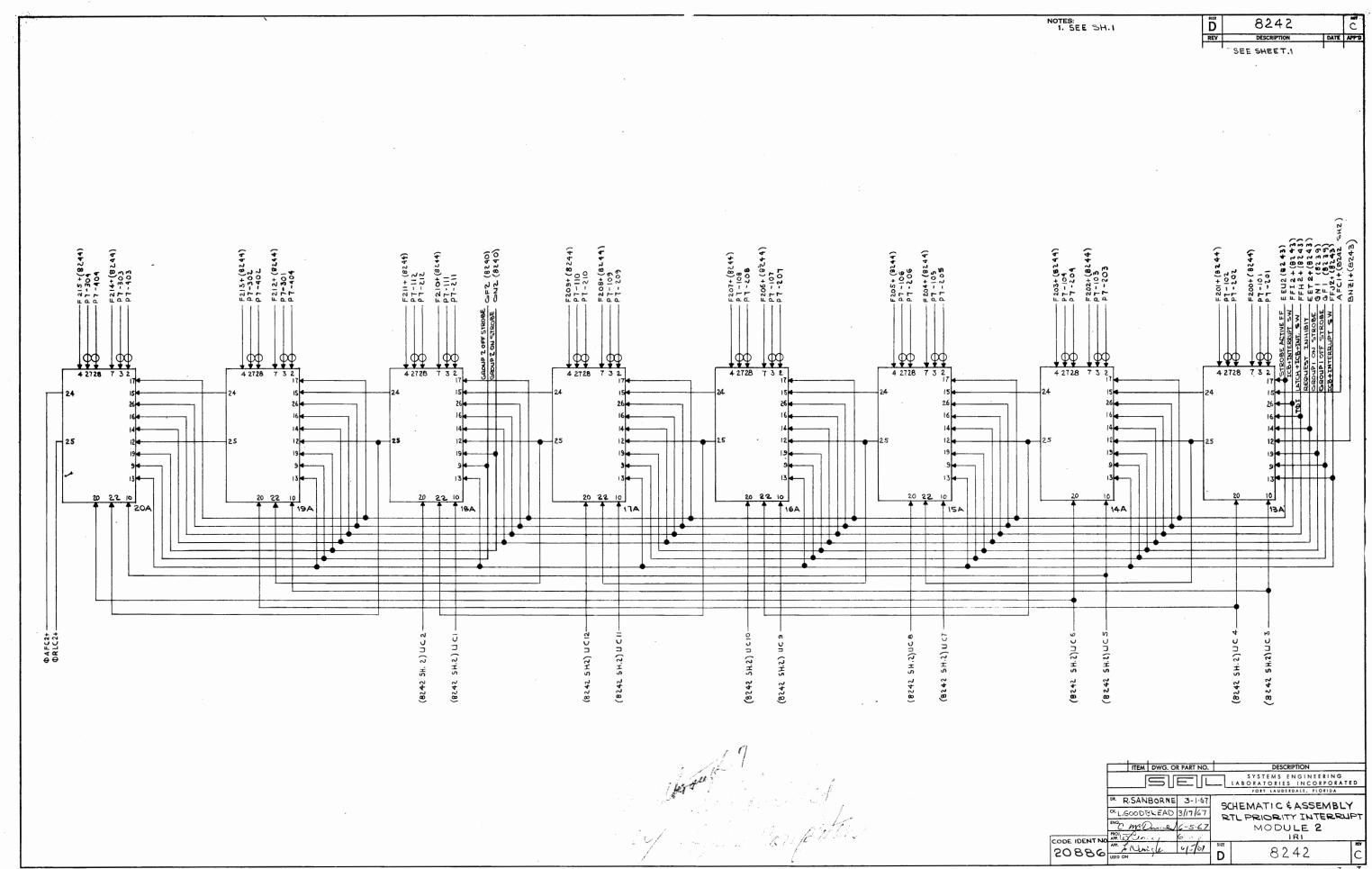


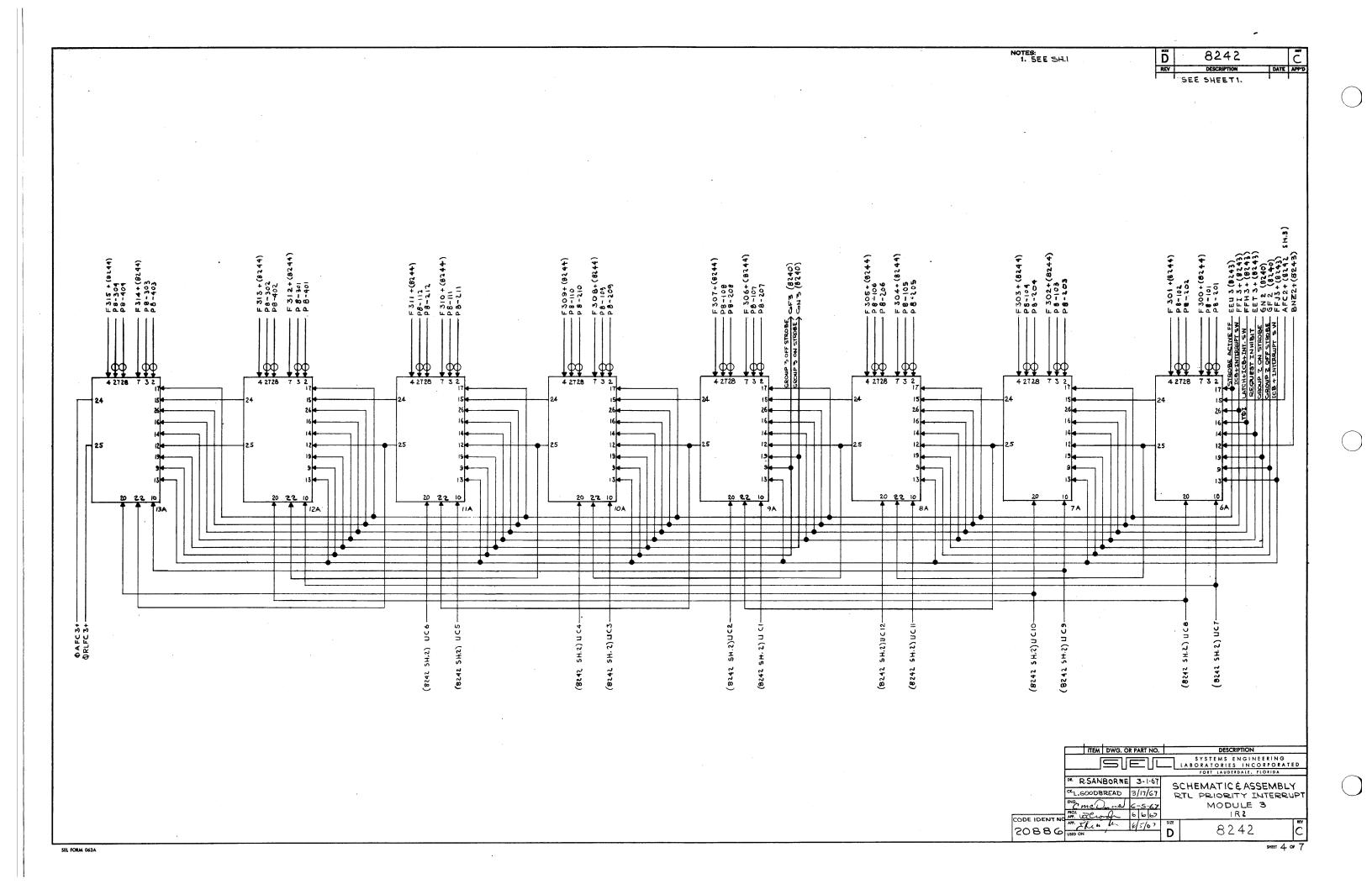


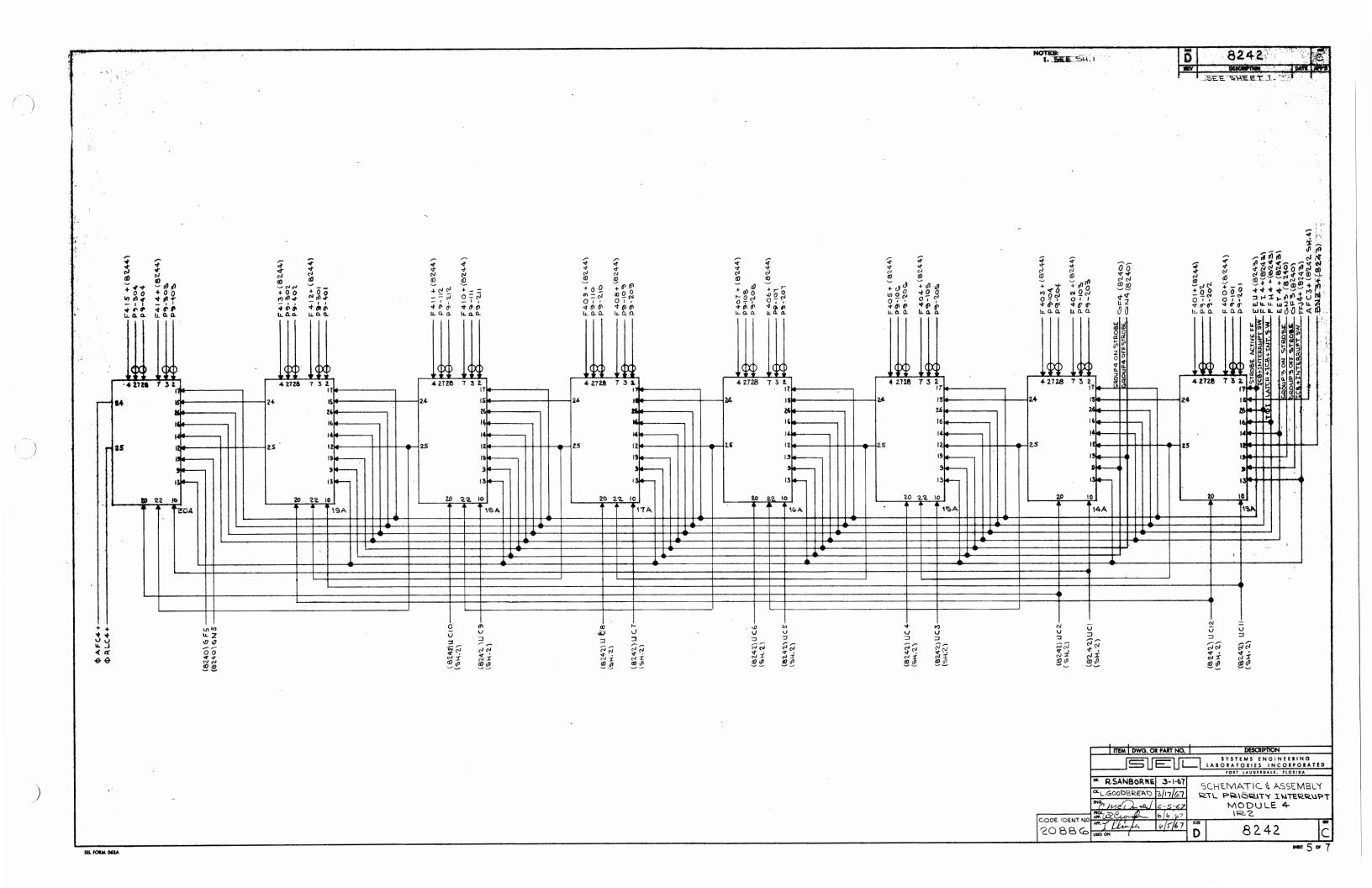
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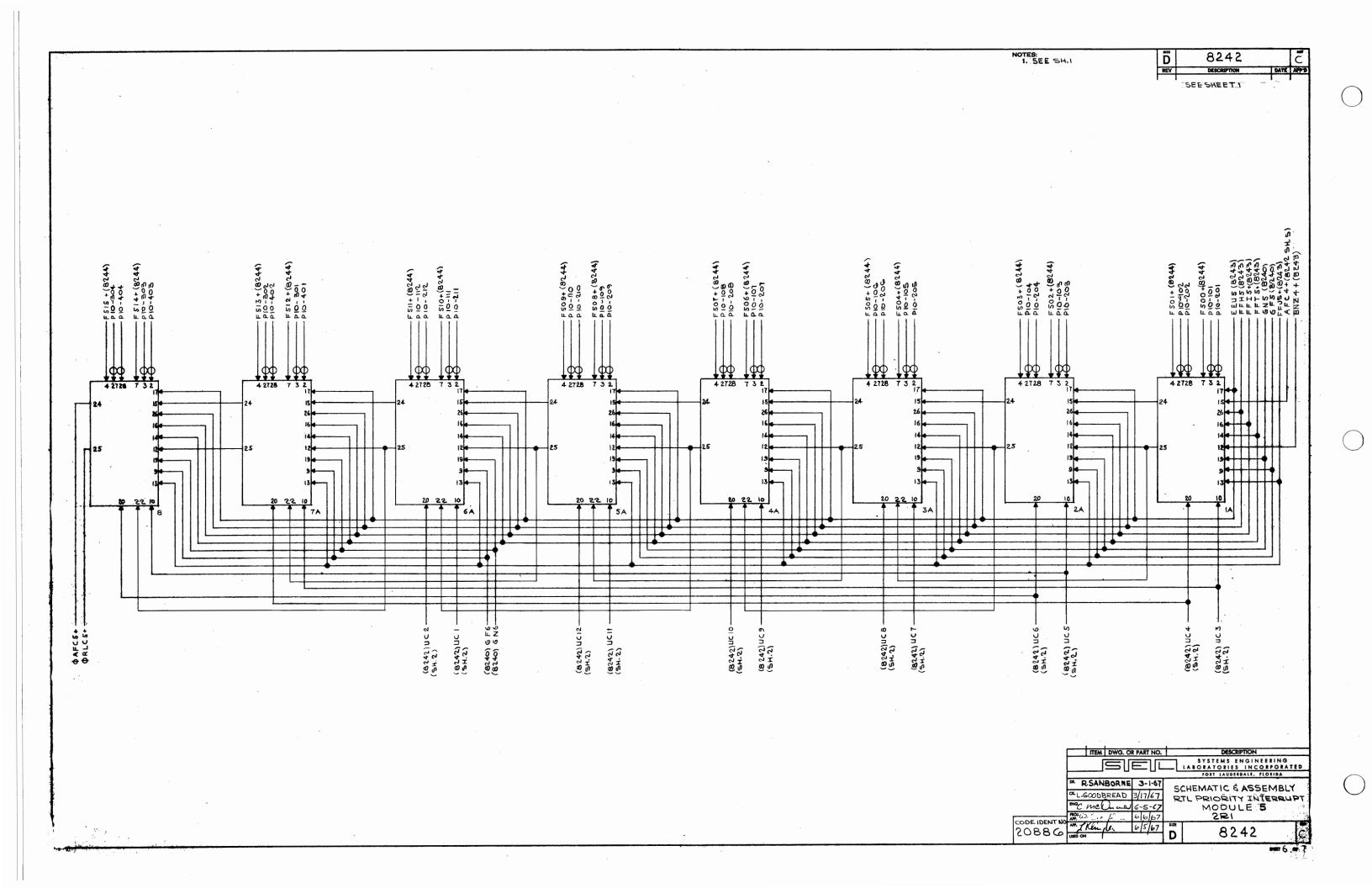






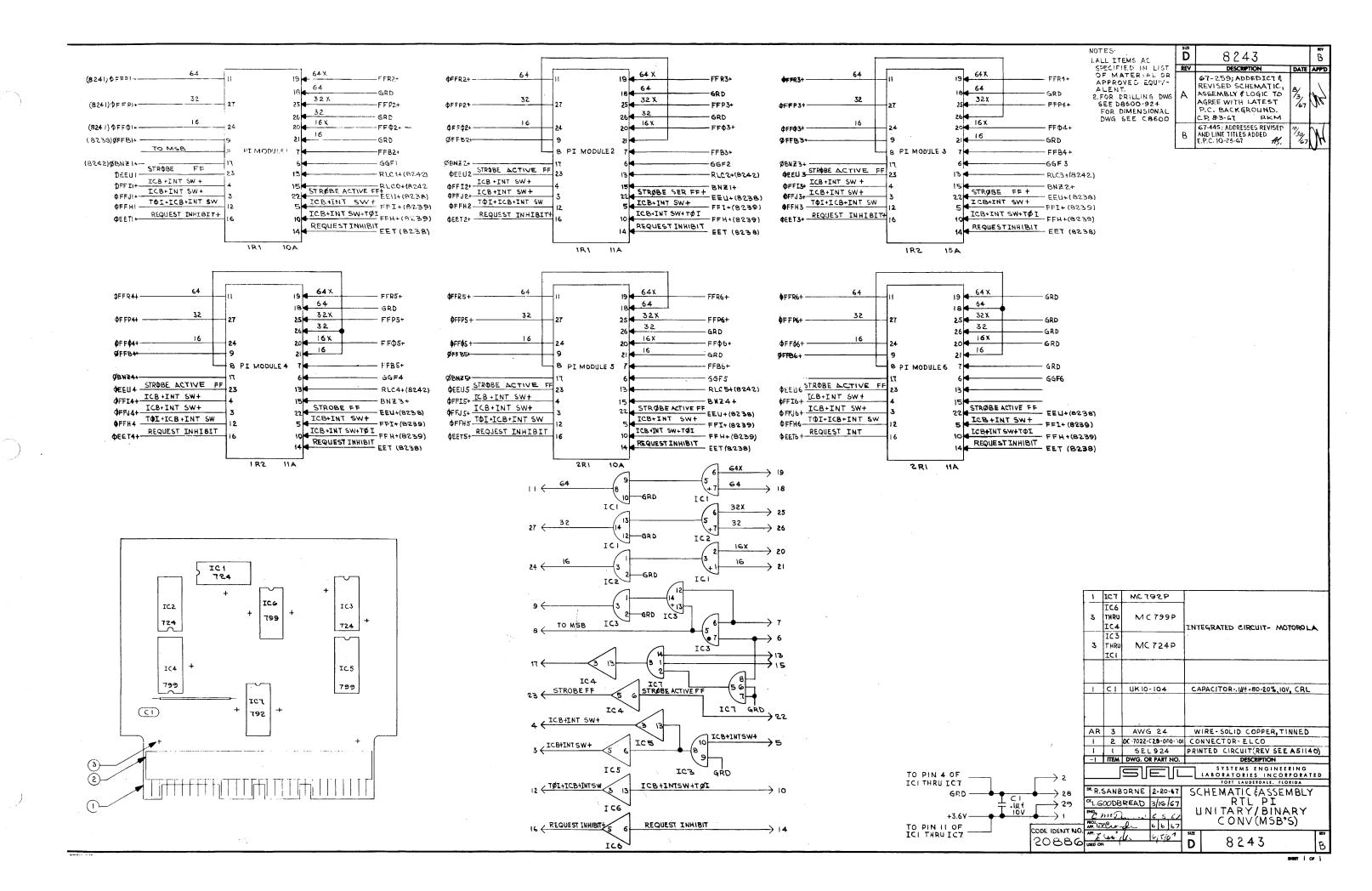


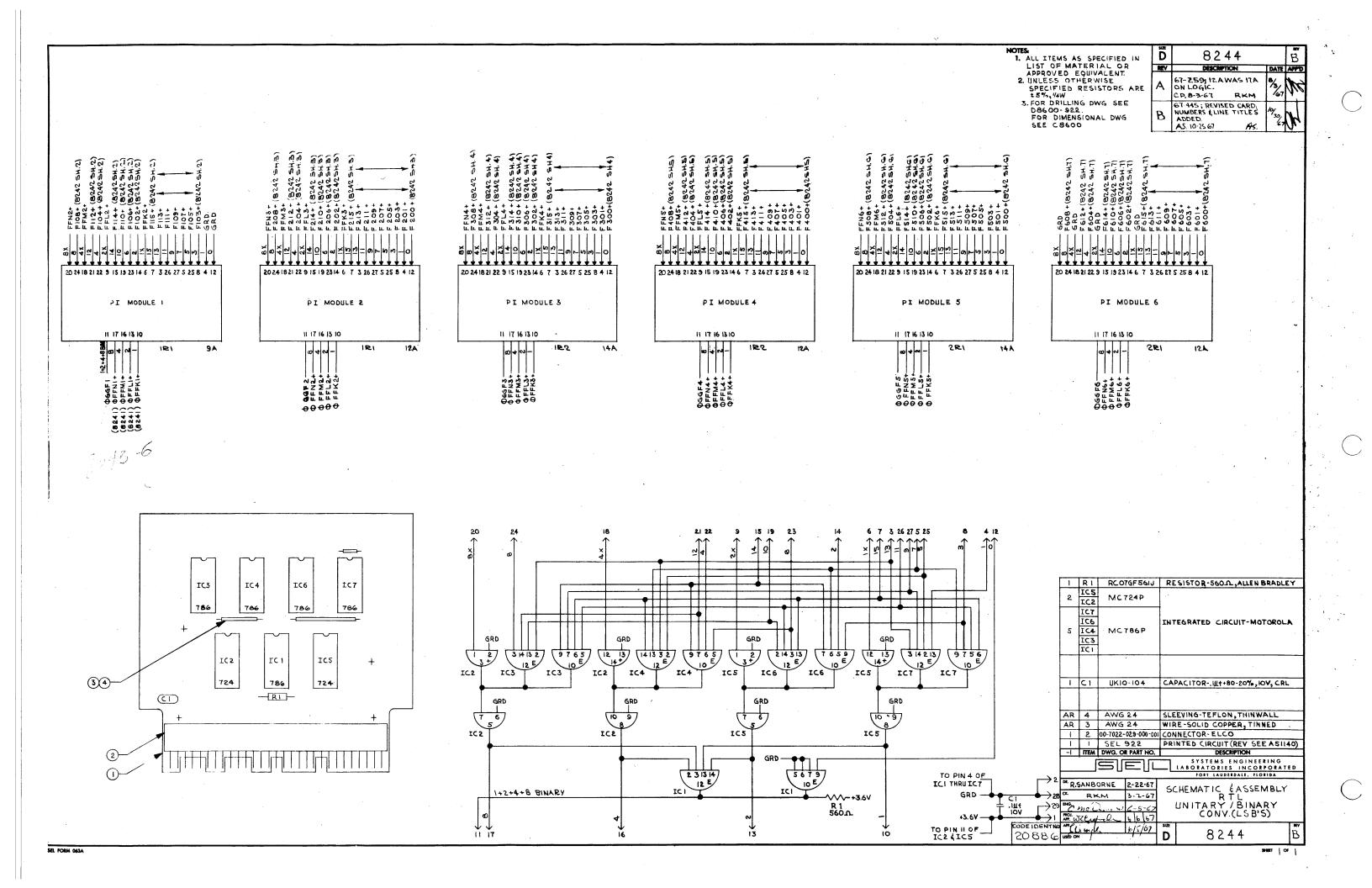


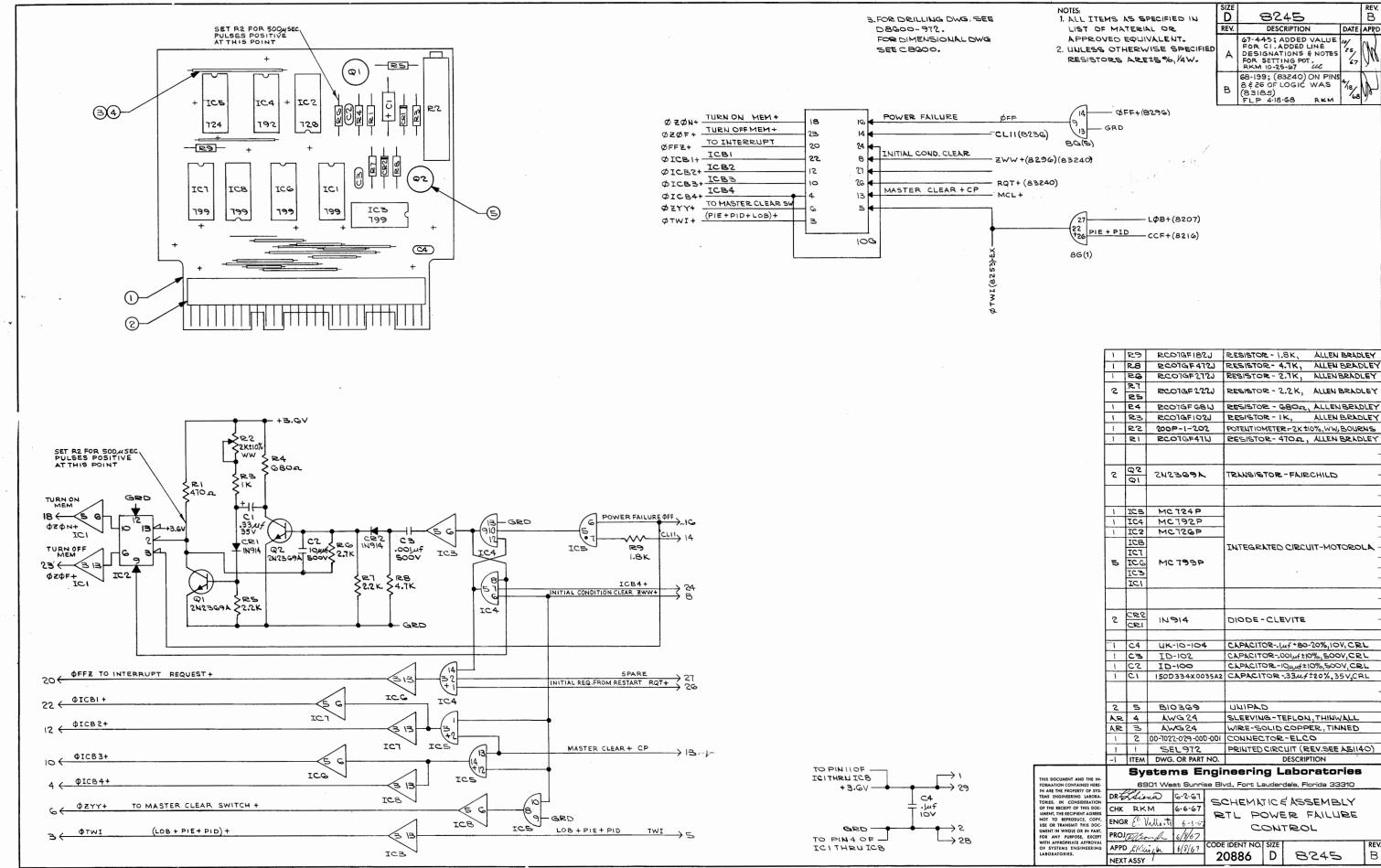


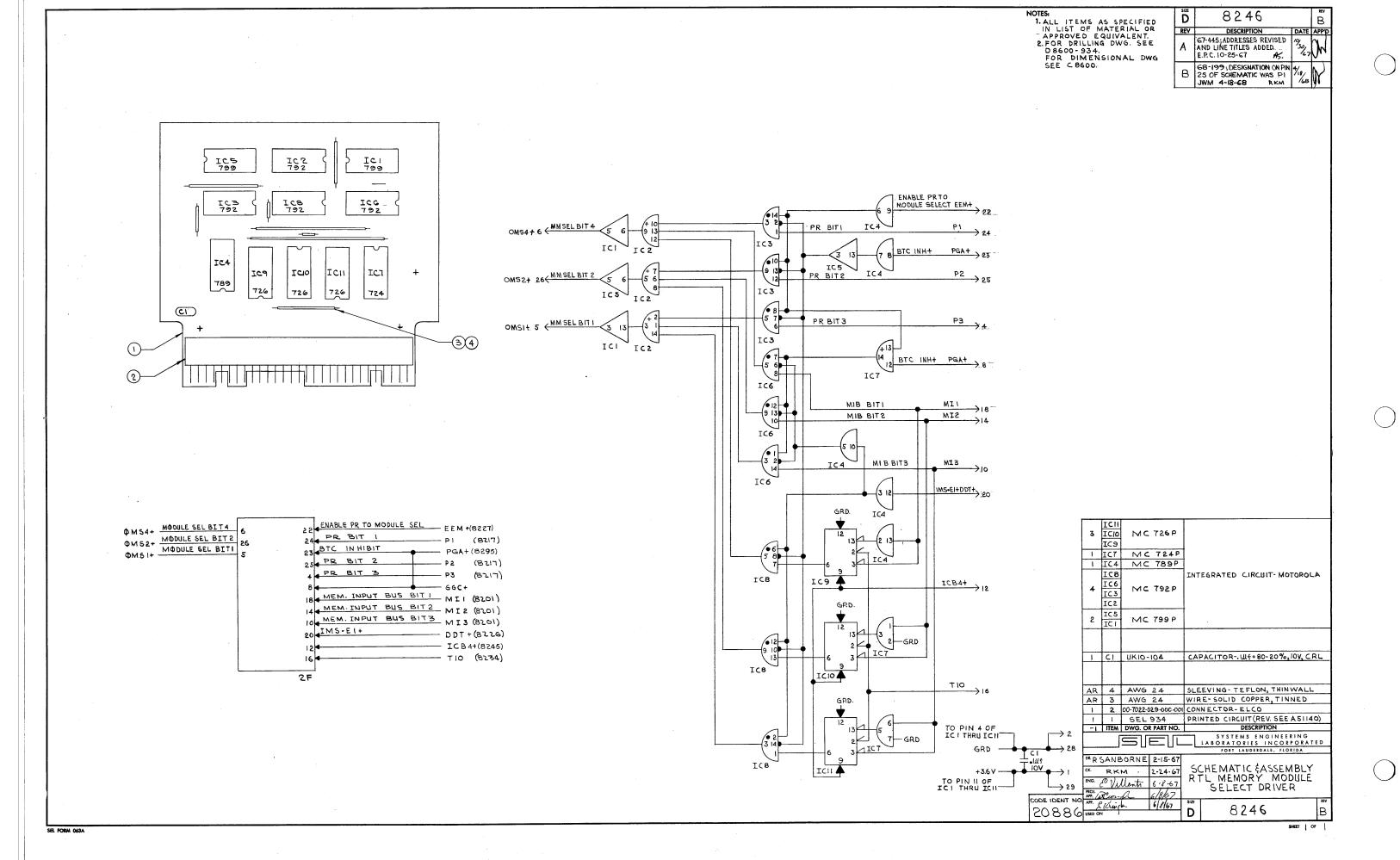
NOTES: 1. SEE SHJ D REV 8242 DESCRIPTION DATE APP'D SEE SHEET 1. . F 500 + (82.44) . P 11 - 101 . P 11-201 F 612+ (8244) P11-301 P11-401 - F613+(8244) - P11-302 - P11-402 F610+ (8244) P11-111 P11-211 F611+(8244) P11-112 P11-212 4 2728 7 3 2 4 2728 7 3 2 4 2728 7 3 2 13.A 14A GF71-60 n UC3 (8242 5H.2) UCIO SH.2) UCII 5H. 2) IIC6 5 H.2) UC 5 SH.2) SH.2) \mathcal{C} SH. 2) (8242 (8242 (8242 (8242 (8242 DESCRIPTION

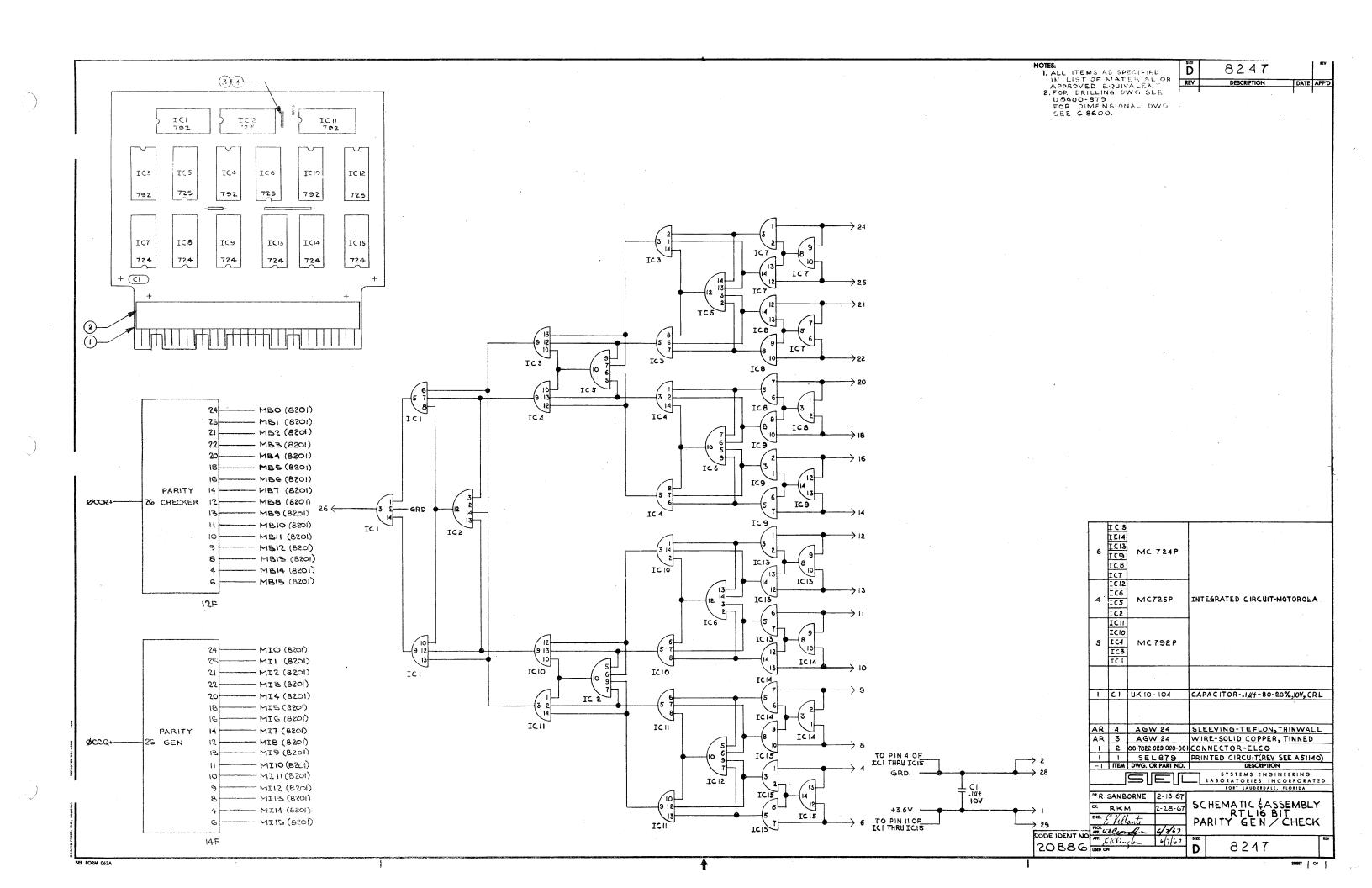
SYSTEMS ENGINEERING
LABORATORIES INCORPORATED
FORT LAUDERDALE, FLORIDA ITEM DWG. OR PART NO. DR. R.SANBORNE 3-1-67
CK.L.GOODBREAD 3/17/67 SCHEMATIC & ASSEMBLY
RTL PRIORITY INTERBUPT
MODULE 6
2RI CODE IDENT NO MAY | 1/4 | 6/5/07 8242 SEL FORM 063A

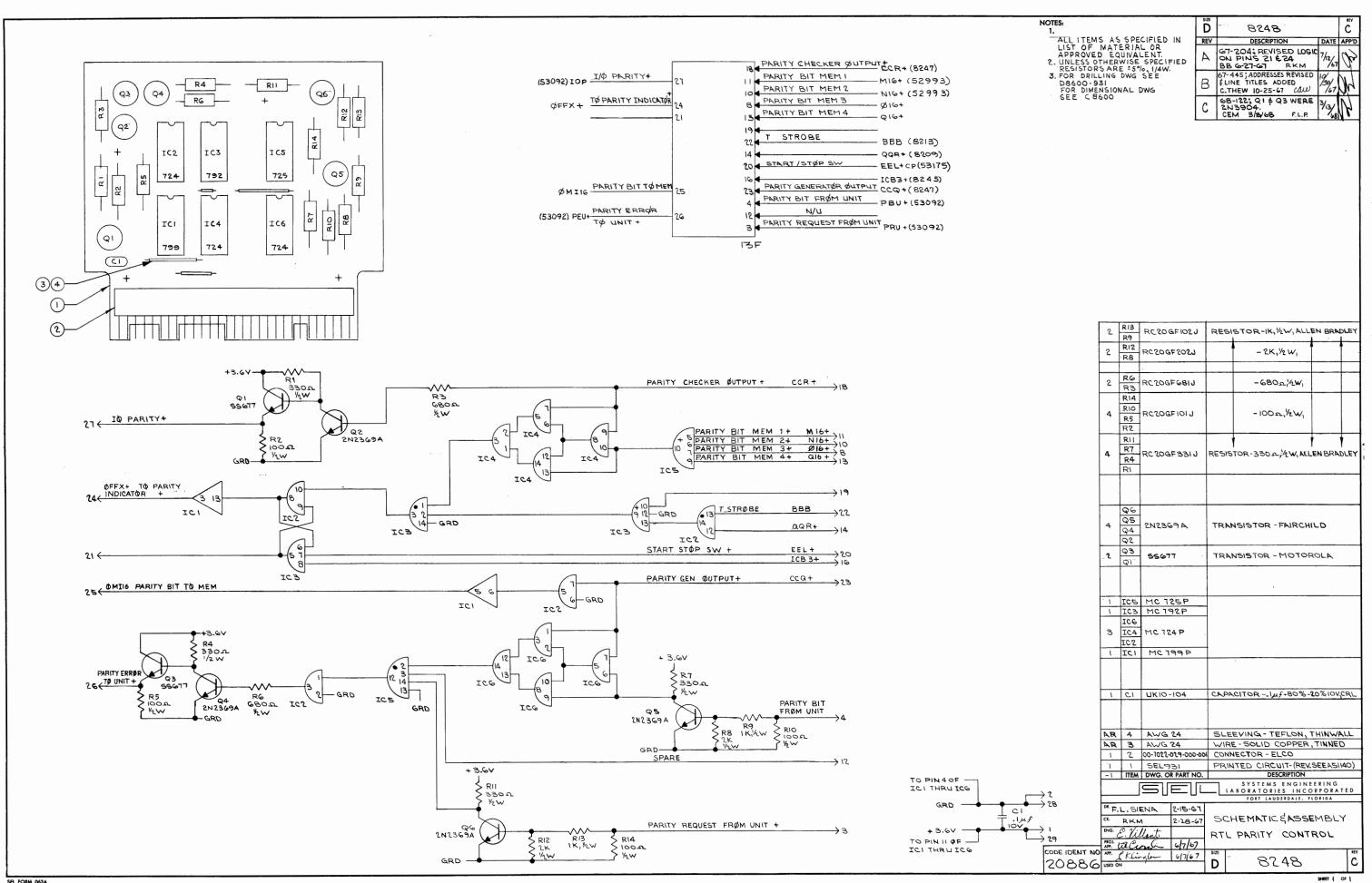


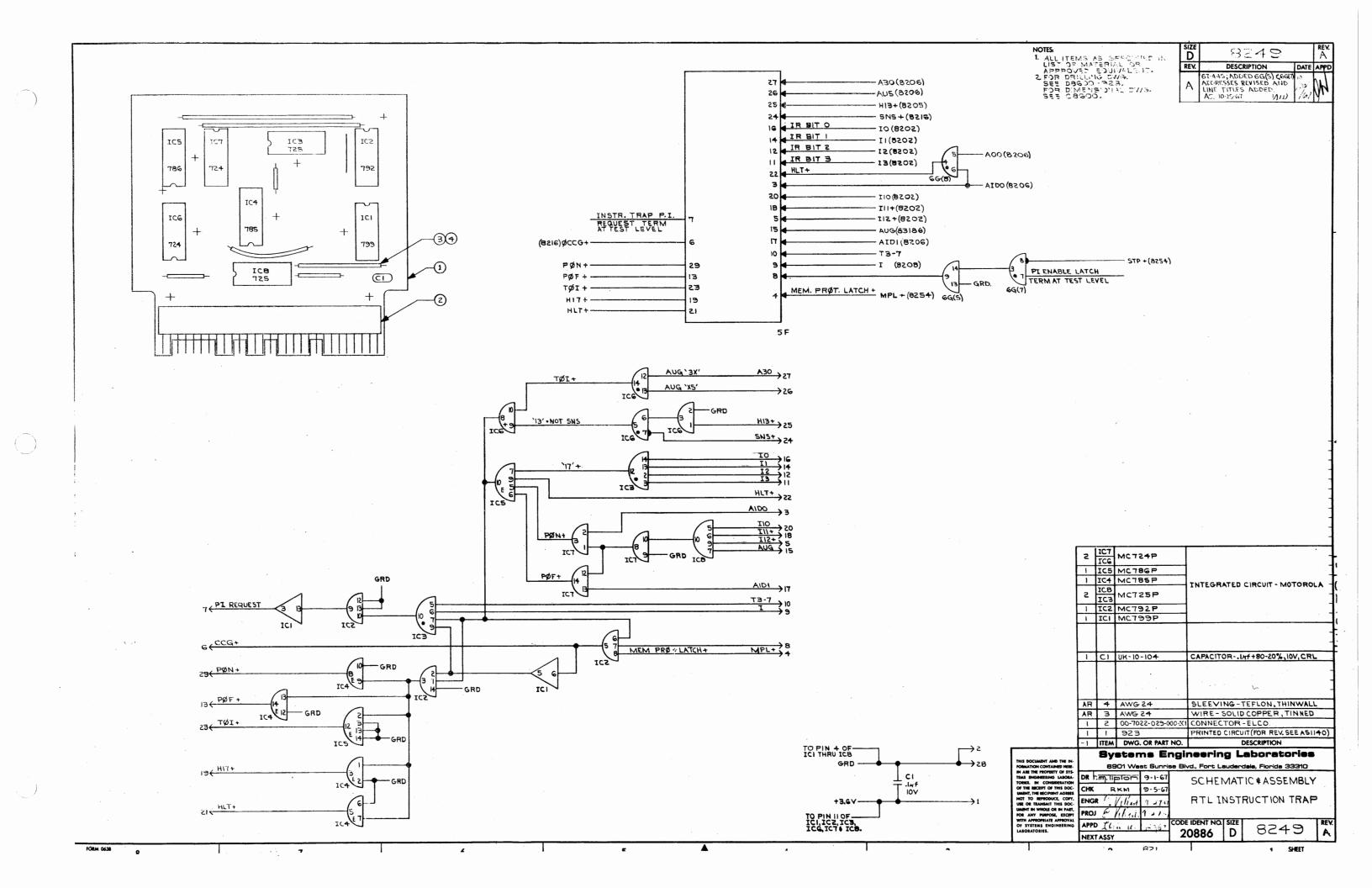


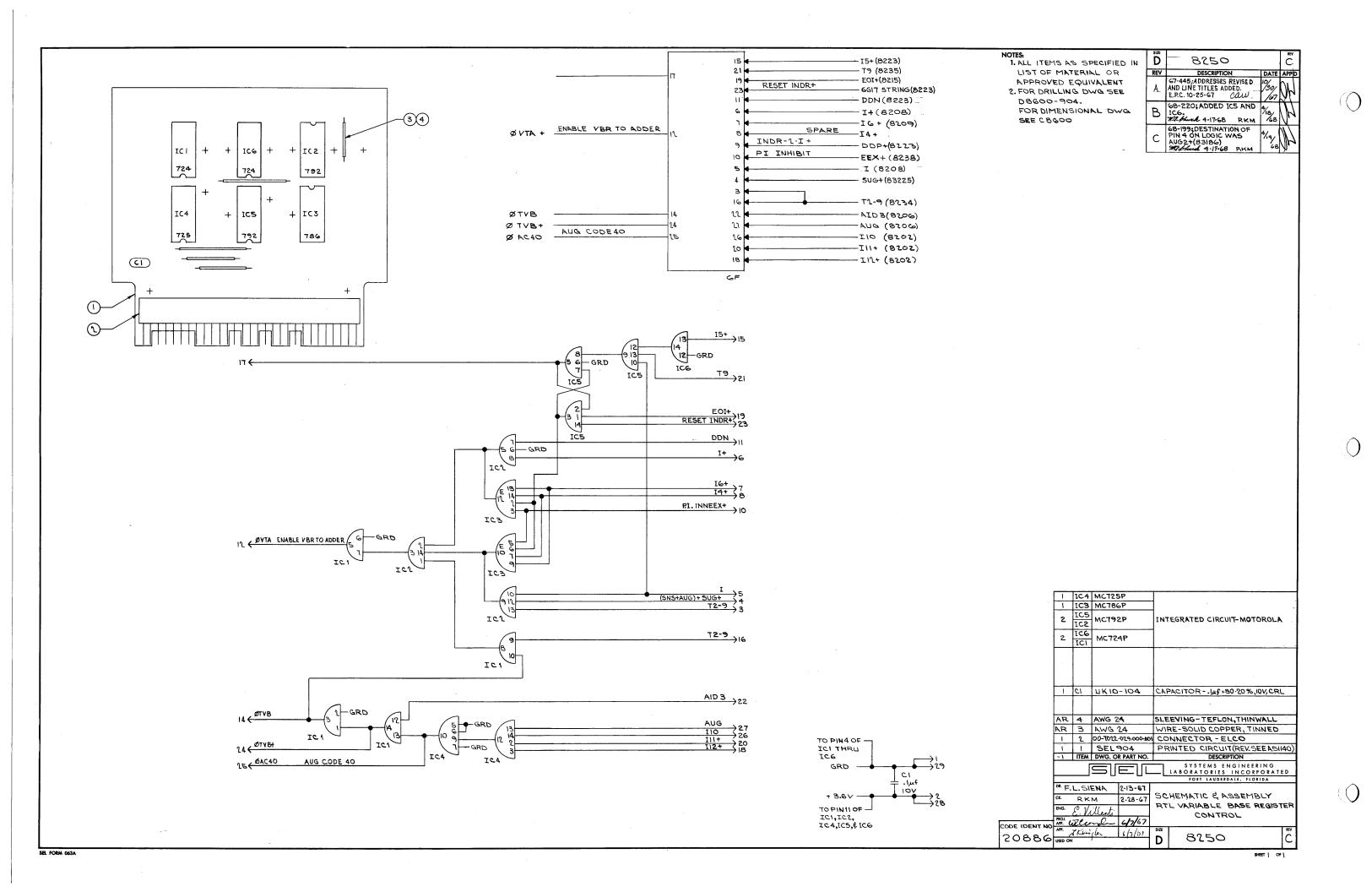


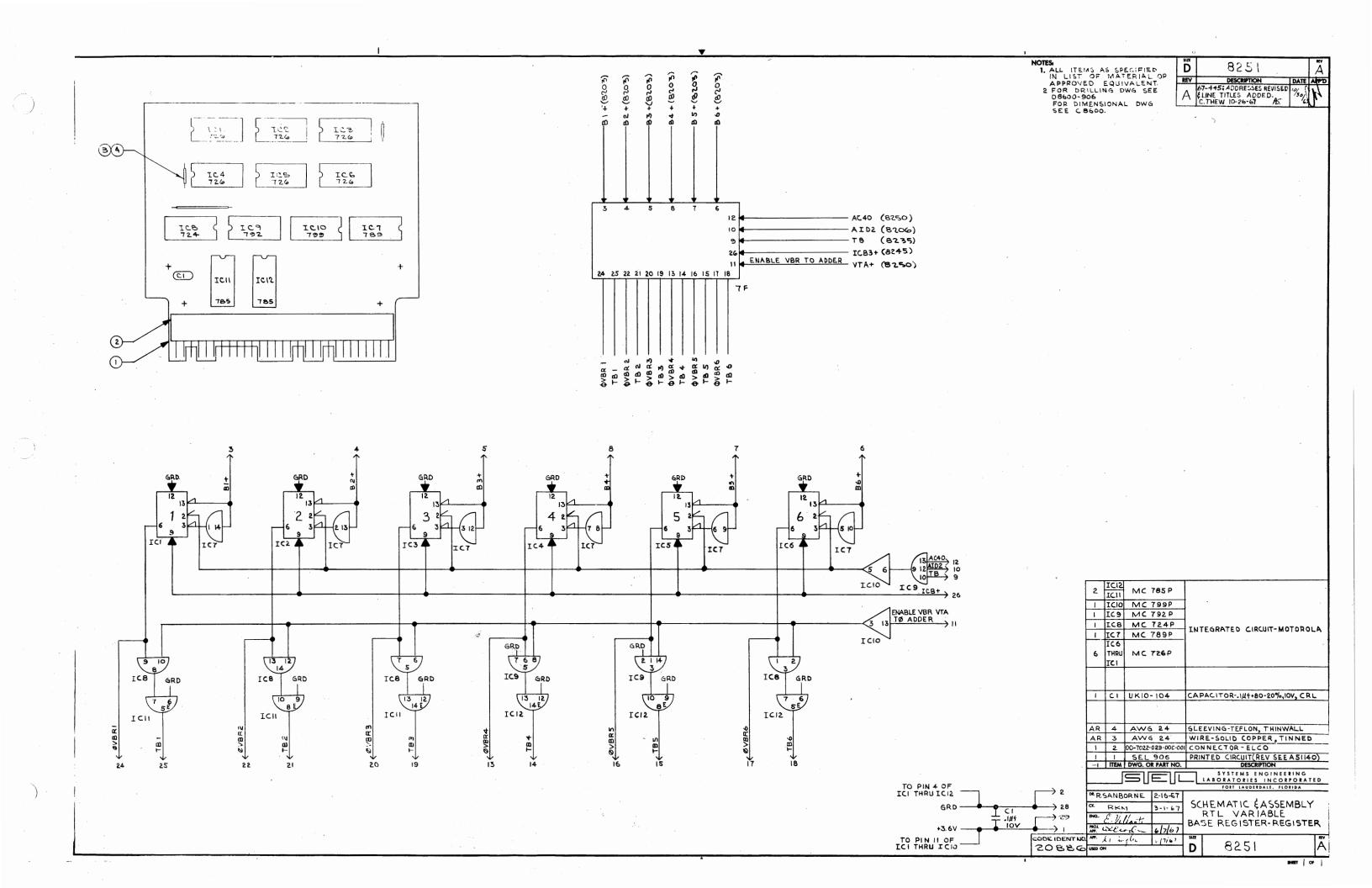


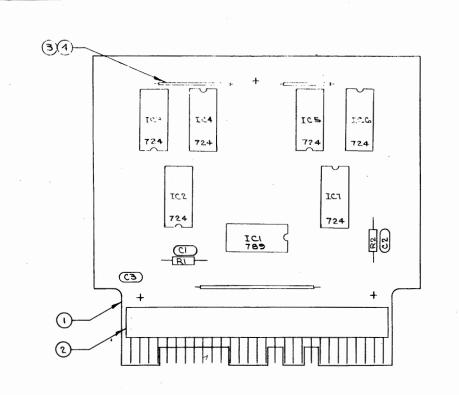


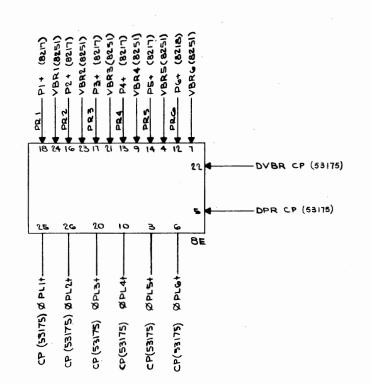


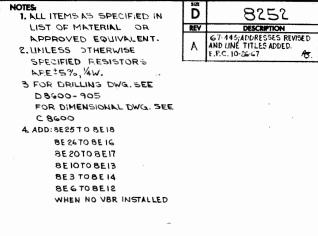




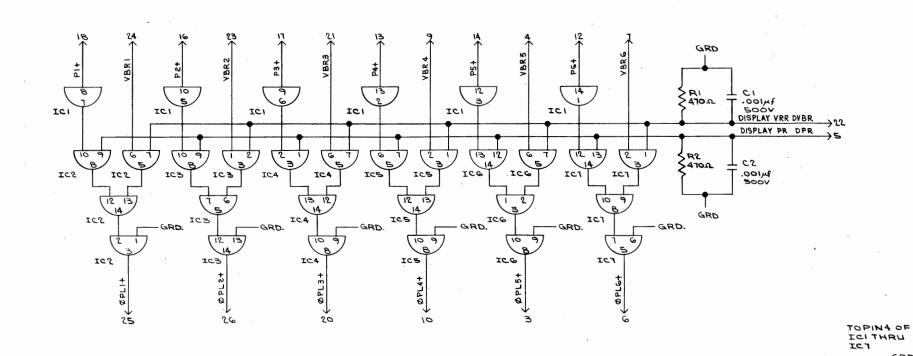






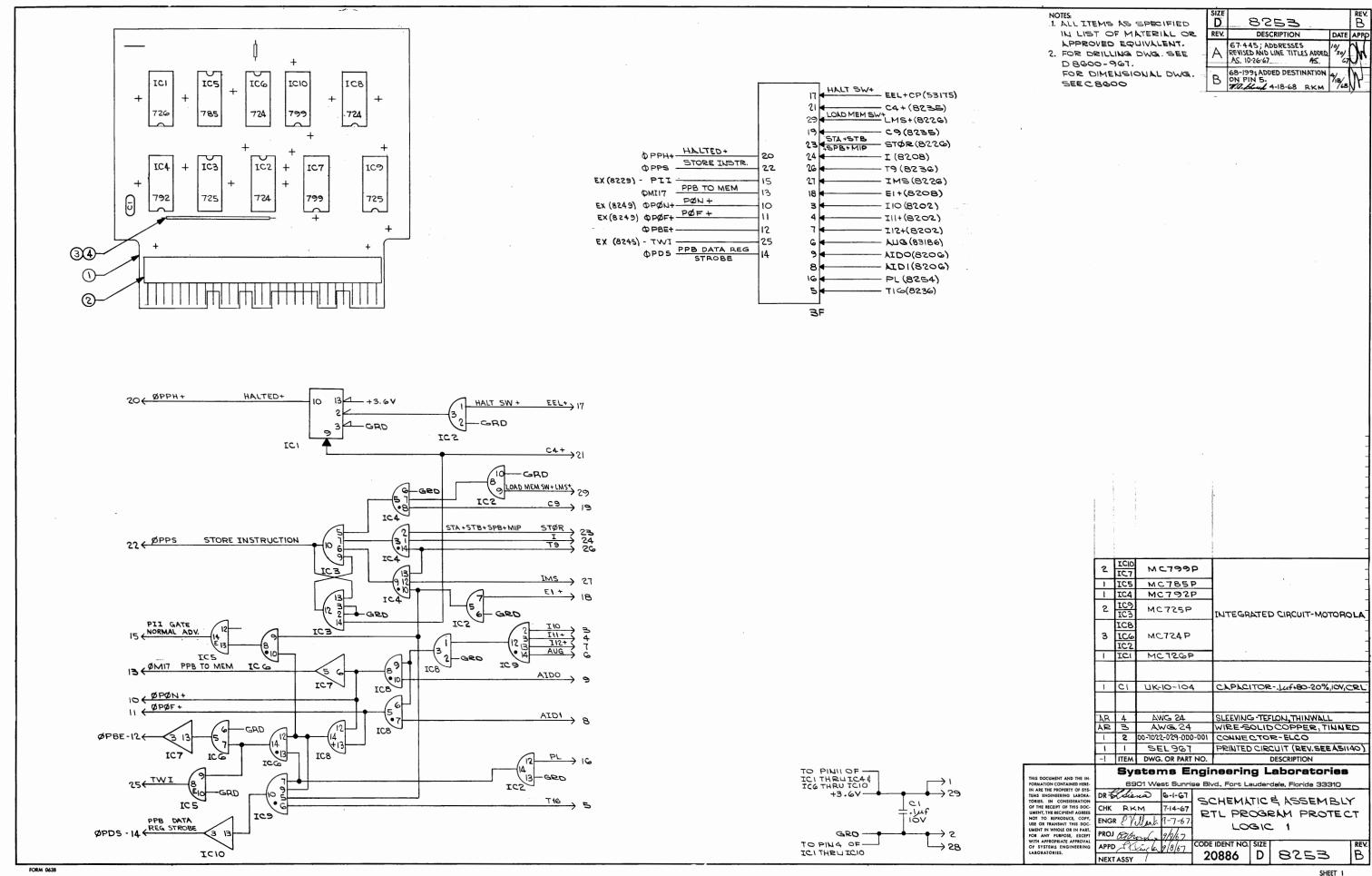


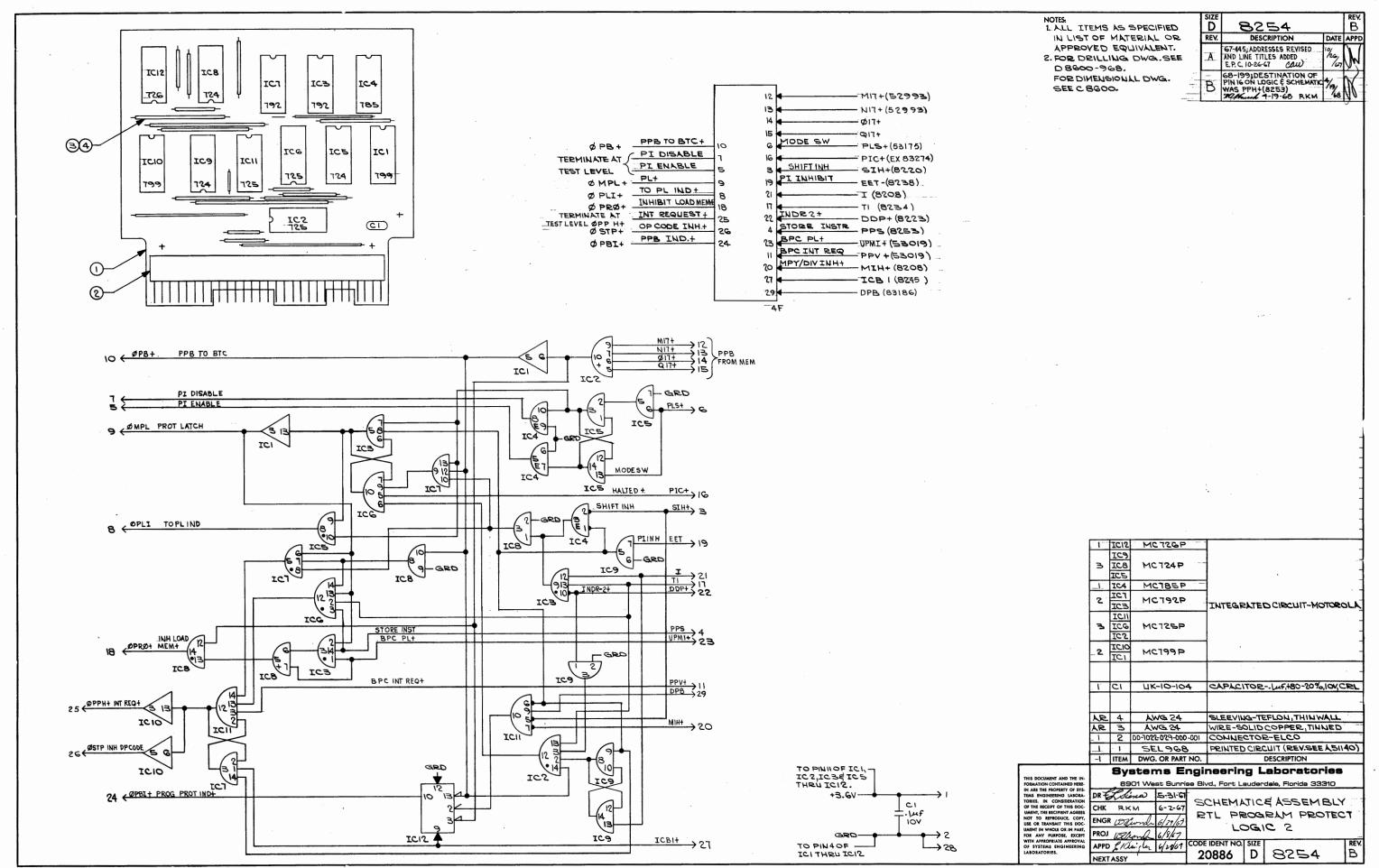
8252

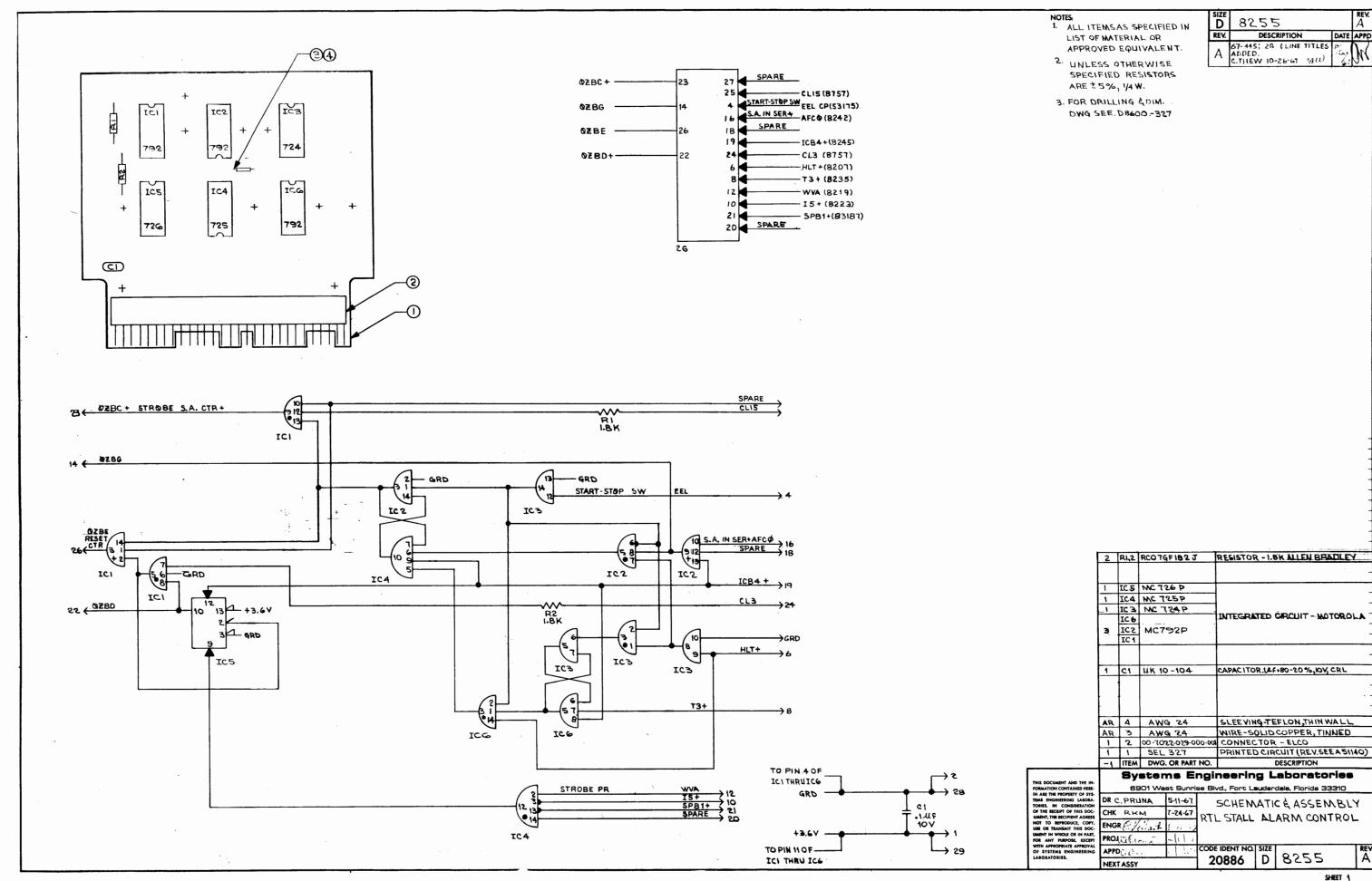


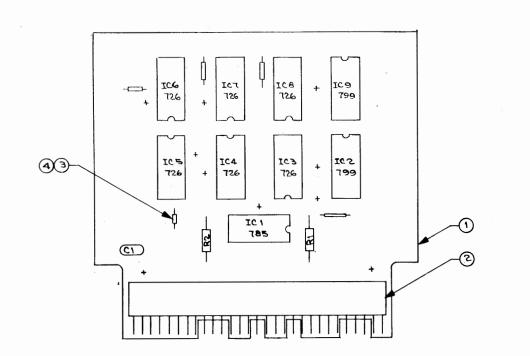
		2	RL.	RCOTGF47IJ	RES	SISTOR-470A,	ALLEN BRAD	EY.
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			roi					
			IC@					
		. Co	IC5	MC 724P	IN	ITEGRATED CIR	CUIT-MOTOR	OL
			ESI					
			ICZ					
		1	ICI	MC 789 P	L			
]						
		1						
		1	E2	UK10-104	CN	PACITOR-Juf+80	-20%,10V CRL	_
		2	CS	10105	CA	PACITOR001	f±10%,500V,C	
		-	CI		-			
		AR	4	AWG. 24	SL	EEVING -TEFLO	N, THINWALL	
		AR	3	AWG. 24	+	RE-SOLID COP		
		1	2	5EL 905		NNECTOR - ELC		40)
		-1	ITEM	DWG, OR PART NO.	-	DESCRIP		40,
		\vdash	٦		$\overline{}$	SYSTEMS EN	GINEERING	
→ ۱		<u> </u>			<u> </u>	FORT LAUDERD		D
→ ૧૧		DR. F.	L.SIE	12-16-67				
		CX.	RK	M . 3-2-67	50	HEMATIC & AS	SEMBLY	
		ENG,	C. Vi	llarte		L-VBR DISP		
-> 2	L	PROJ.	aren		7.1	A D K DI26	TUI GVIES	
$\rightarrow 5e$	CODE IDENTING		& Ke.	166- 4 (7/6)	SIZE	0250		REV
	20886	USED OF	1	/	D	8252	•	Α

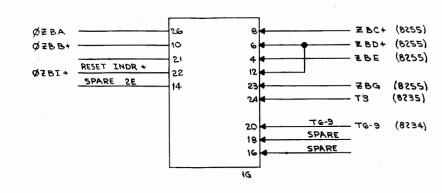
C3 .luf 10V

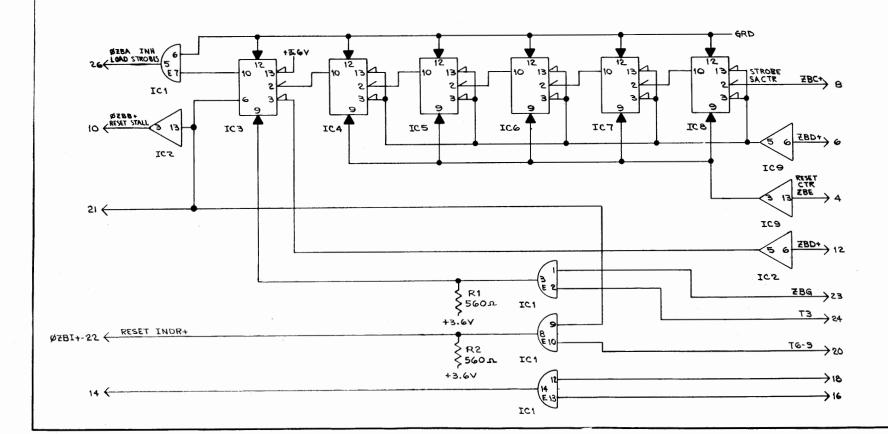












TO PIN4OFICITHRU IC 9	26
+3,6V TO PIN 11 OF	21 3,4,4 1 Voi 1 29

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WITH A PREOPERATE A PROVIAL
OF SYSTEMS REGIMENTED
LASORATORIES.

2	R2 R1	RCOTGF 561 J	RESISTOR - 560 _D, ALLEN BRADLEY			
_	821					
	721					
6	ICE	MC726P	INTEGRATED CIRCUIT-MOTOROLA			
	IC5 IC4					
	IC3					
	IC9					
2	ICS	MC 799P				
1	ICI	MC 785 P				
			-			
1	C1	UK-10~104	CAPACITORILLF+80-20%,104 CRL			
		-				
AR	4	AWG 24	SLEEVING-TEFLON, THIN WALL			
AR	3	AWG 24	WIRE- SOLID COPPER, TINNED			
1	2	00-7022-029-000-001	CONNECTOR - ELCO			
١	1	SEL 328	PRINTED CIRCUIT (REV. SEE A SI 140)			
-1	ITEM	DWG. OR PART NO.	DESCRIPTION			

NOTES:
1. ALL ITEMS AS SPECIFIED

INLIST OF MATERIAL OR

APPROVED EQUIVALENT.
2. UNLESS OTHEWISE.
SPECIFIED RESISTORS
ARE 15% 1/4 W.
3. FOR DRILLING & DIMENSIONAL DWG SEE D8600-328.

SIZE D

REV.

8256

DESCRIPTION

67:445; ALDRESSES
REVISED, LINE TITLES AND
CARD NUMBERS AIDED,
AS. 10:2667

DATE APPD

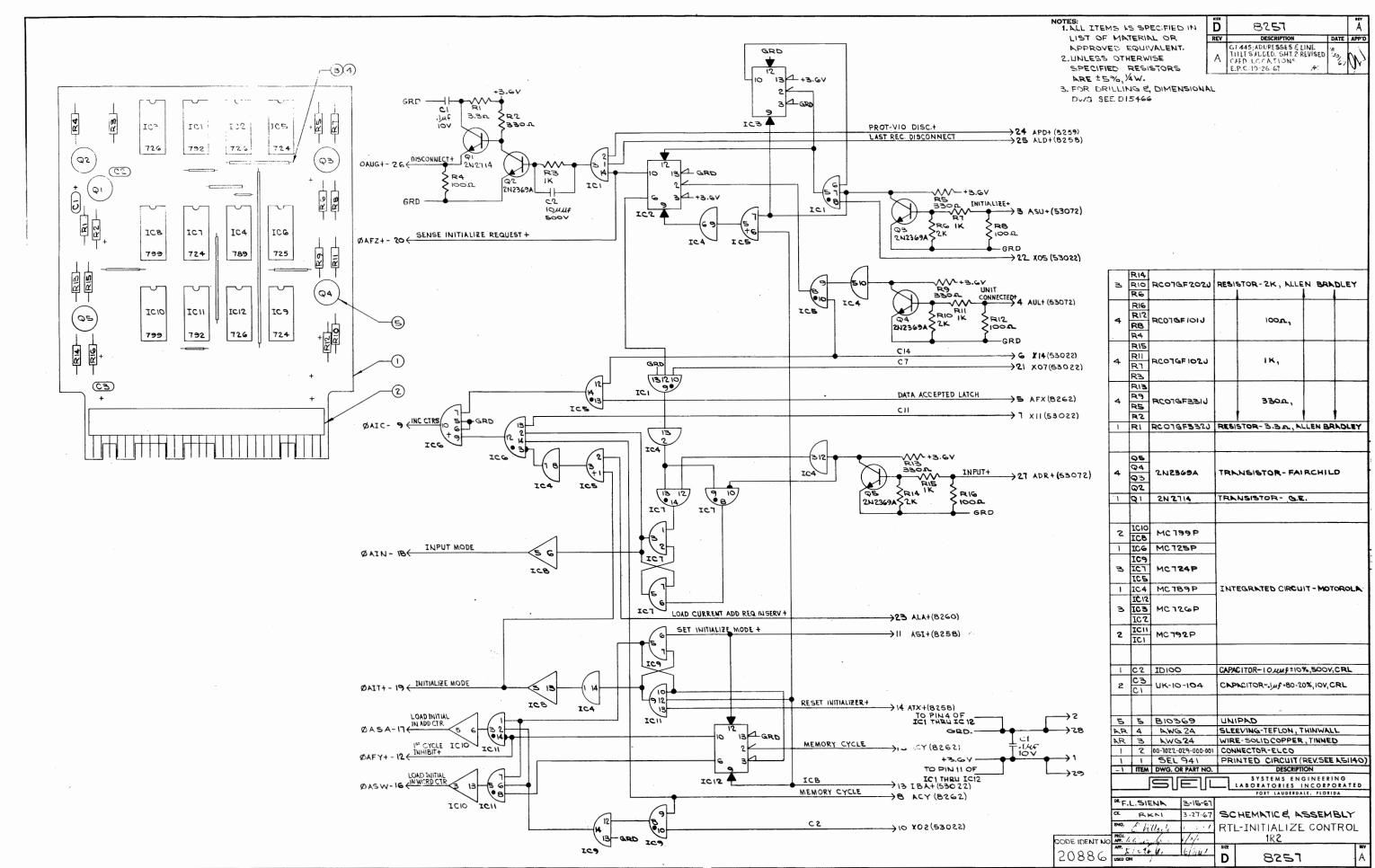
Systems Engineering Laboratories

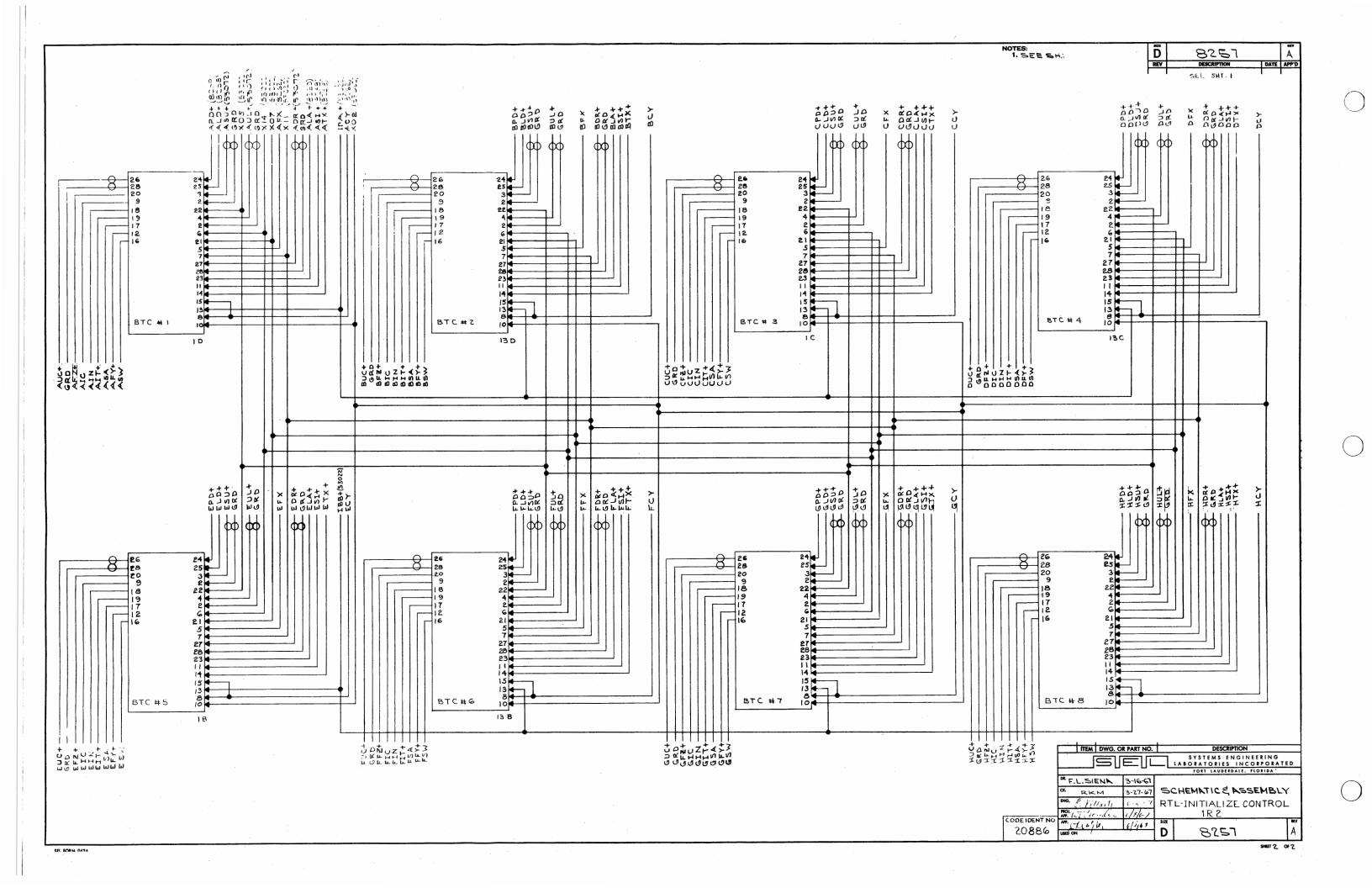
6901 West Sunnise Blvd., Fort Lauderdele, Floride 33310

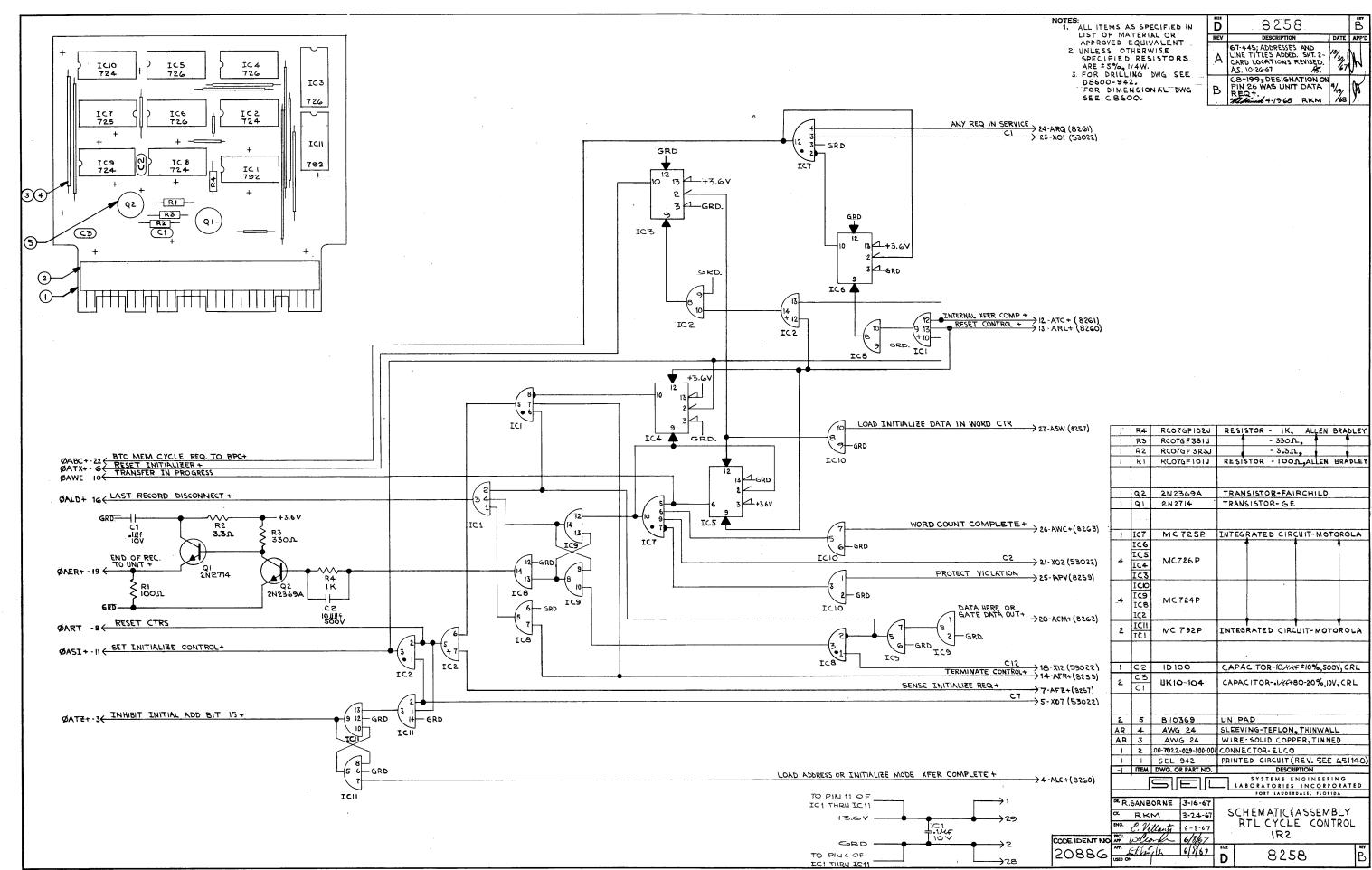
DR C. PRUNA 7-17-67

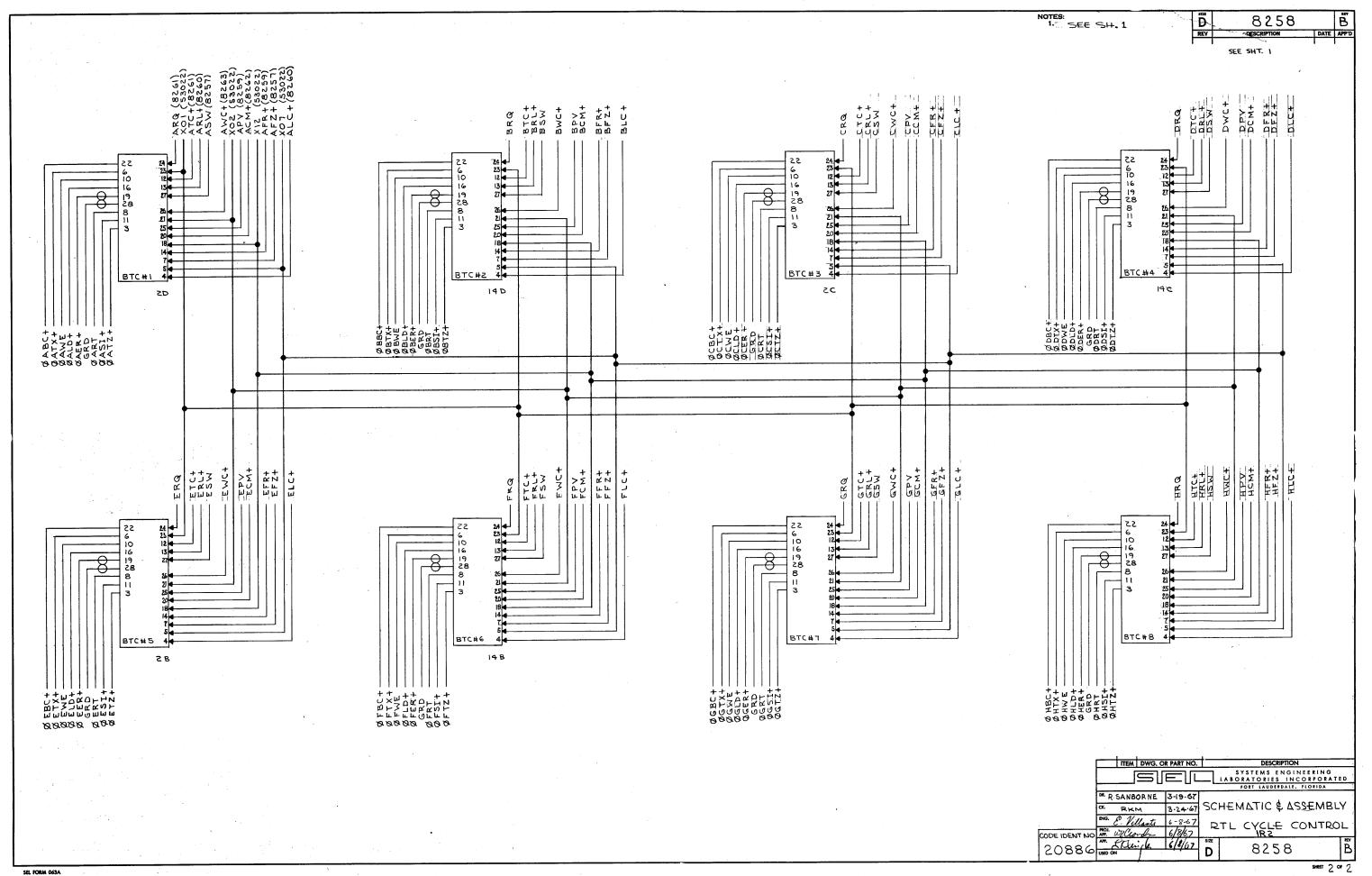
CHK RKM 7-19-67

ENGR 7-19-67

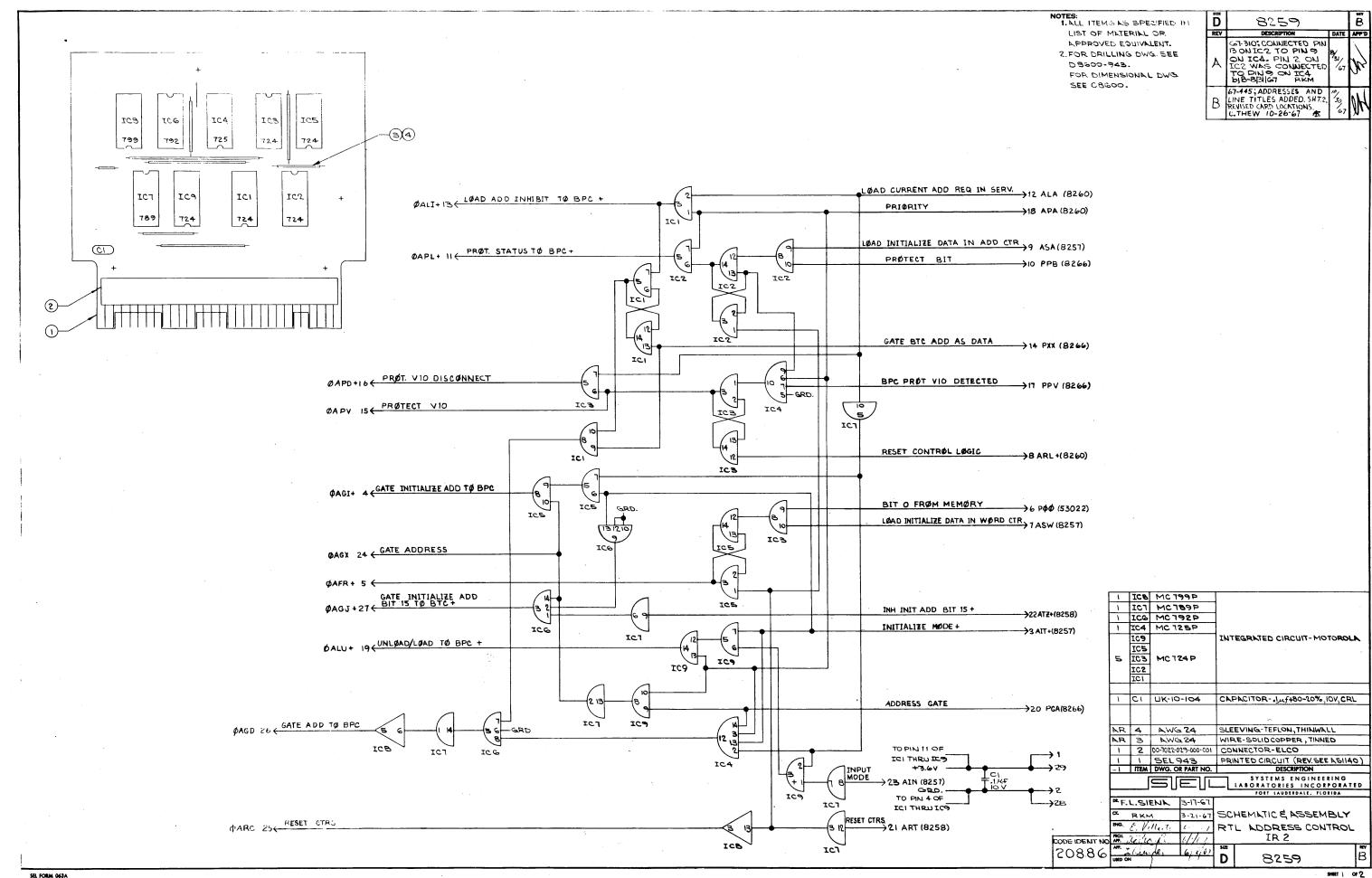


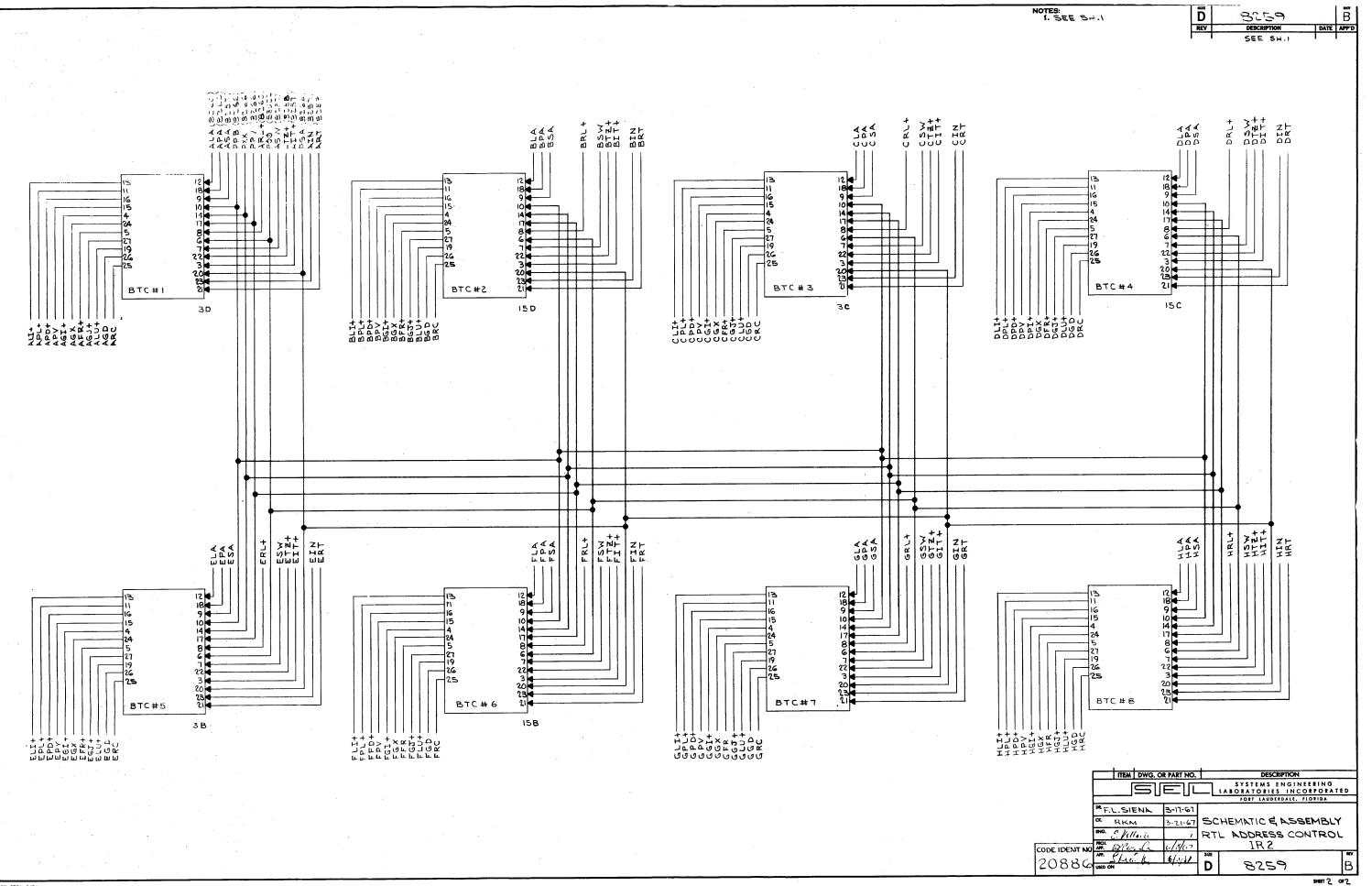


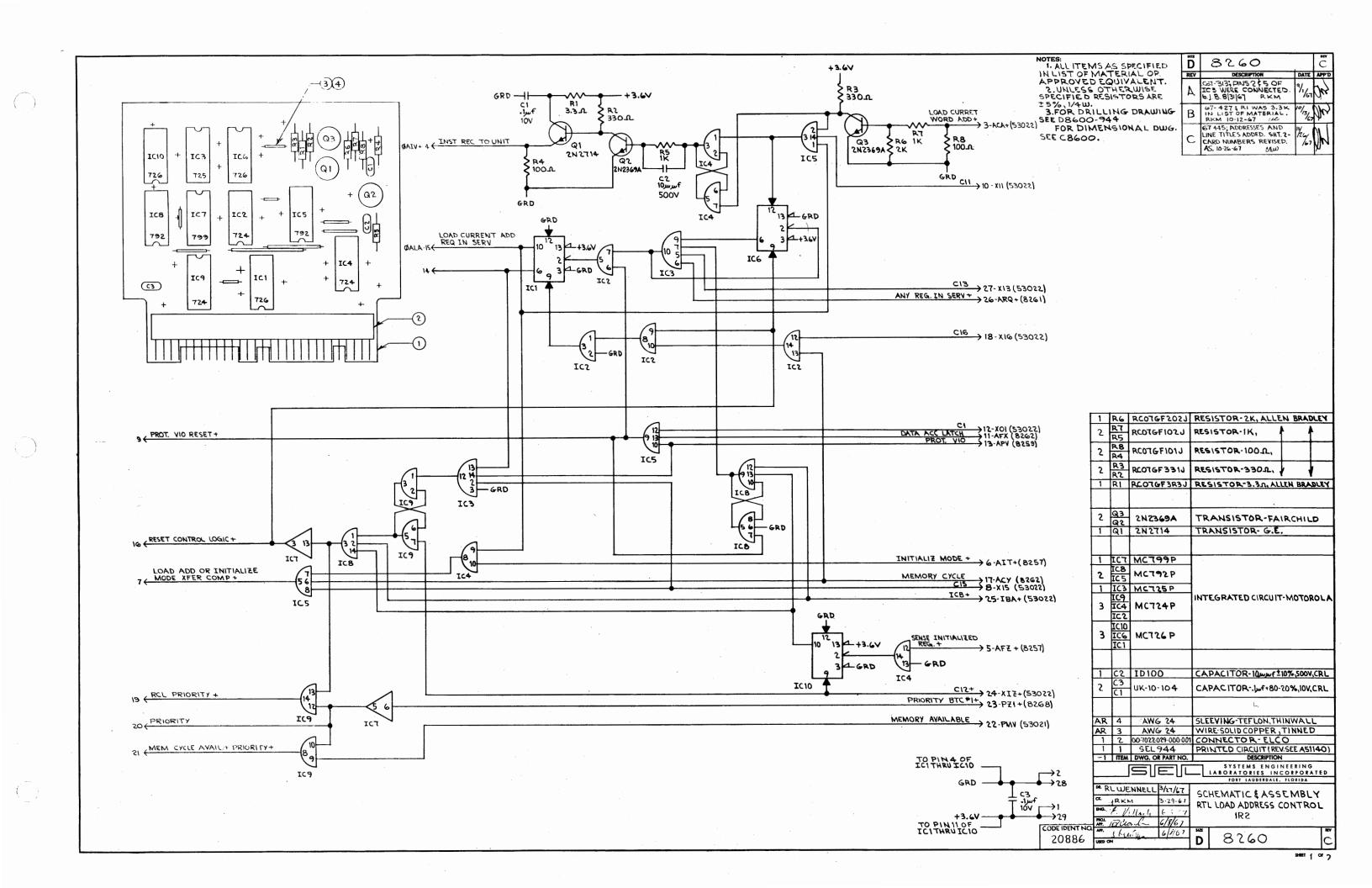


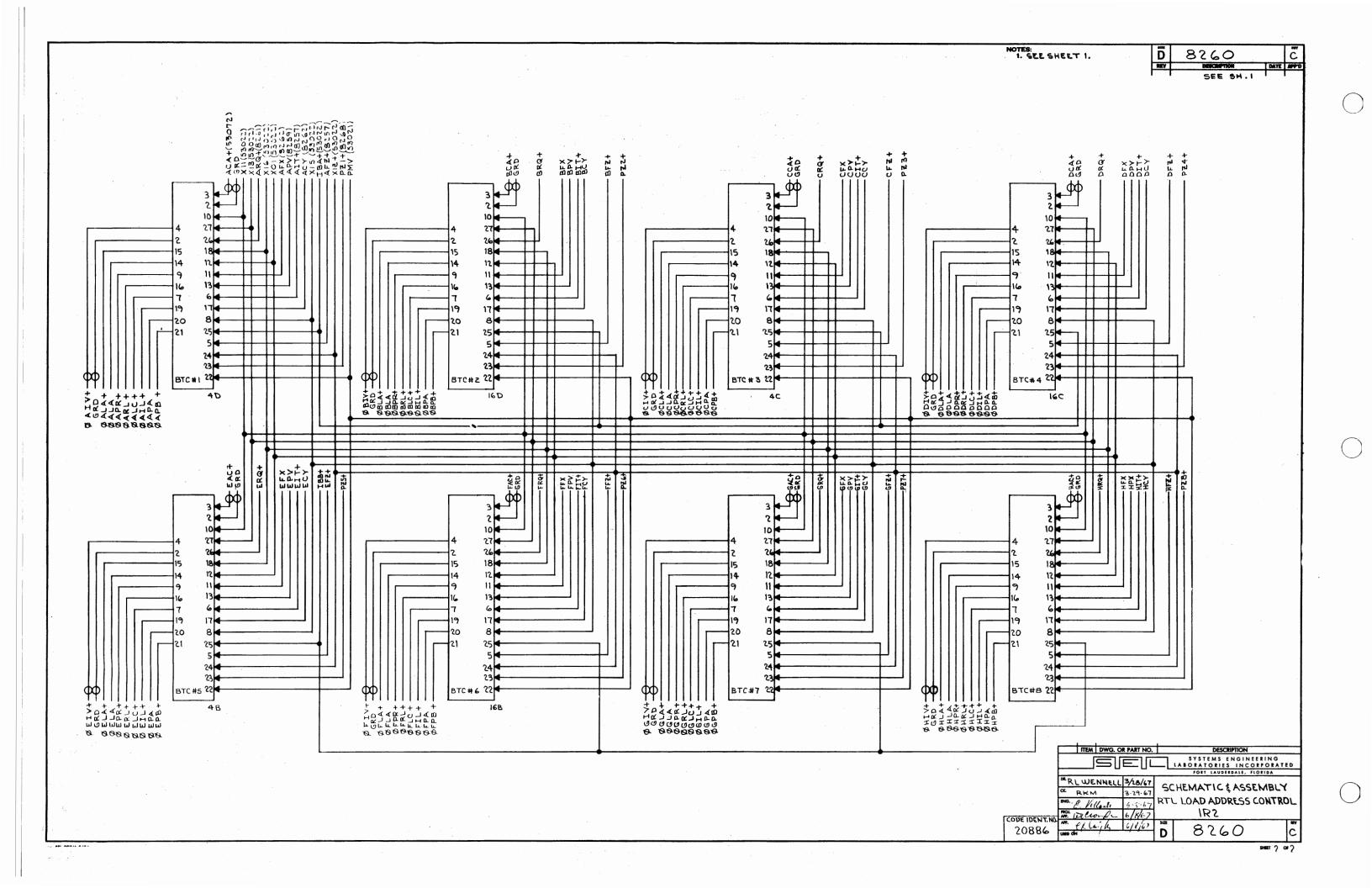


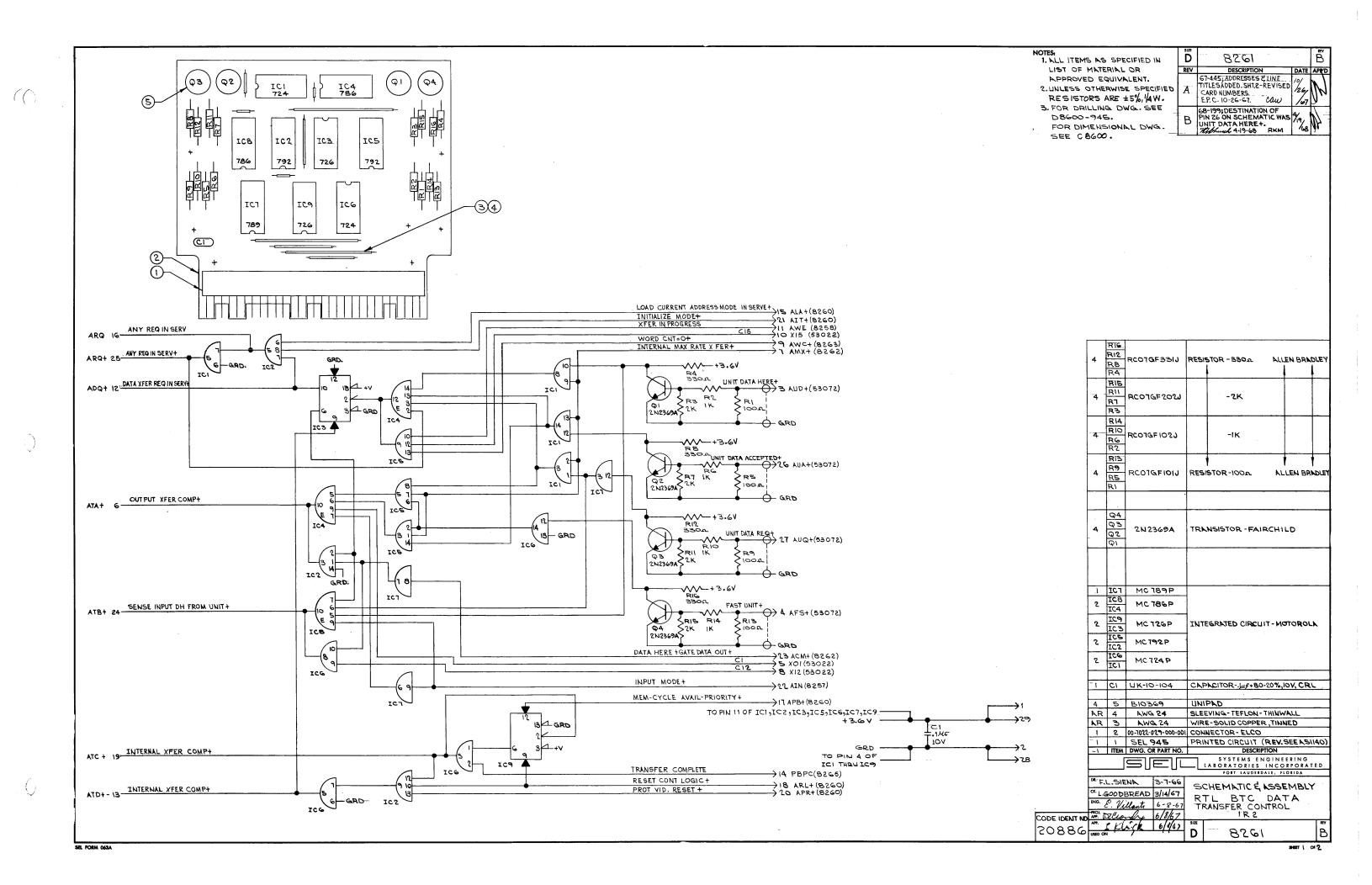
SHEET 2 OF 2

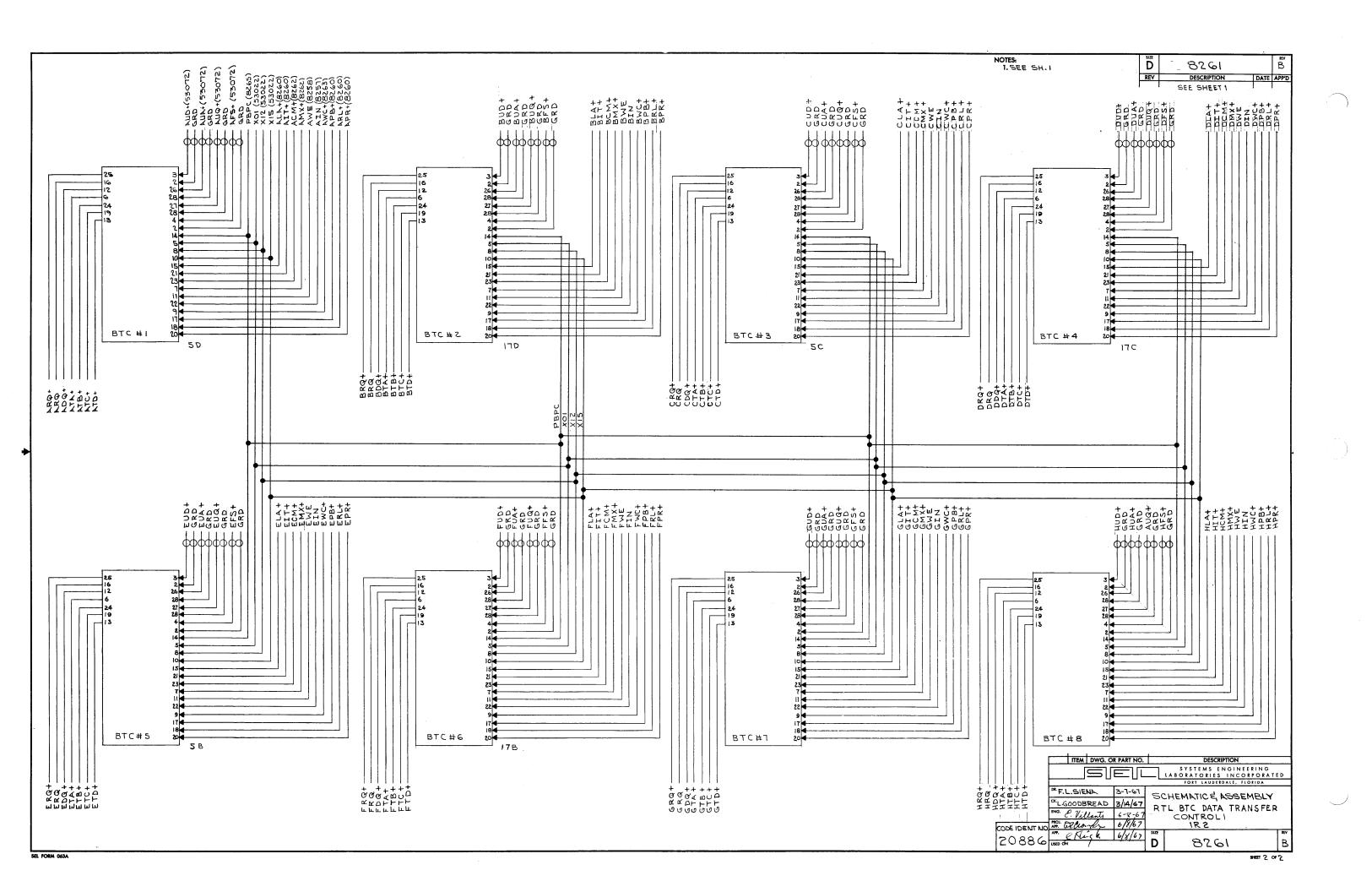


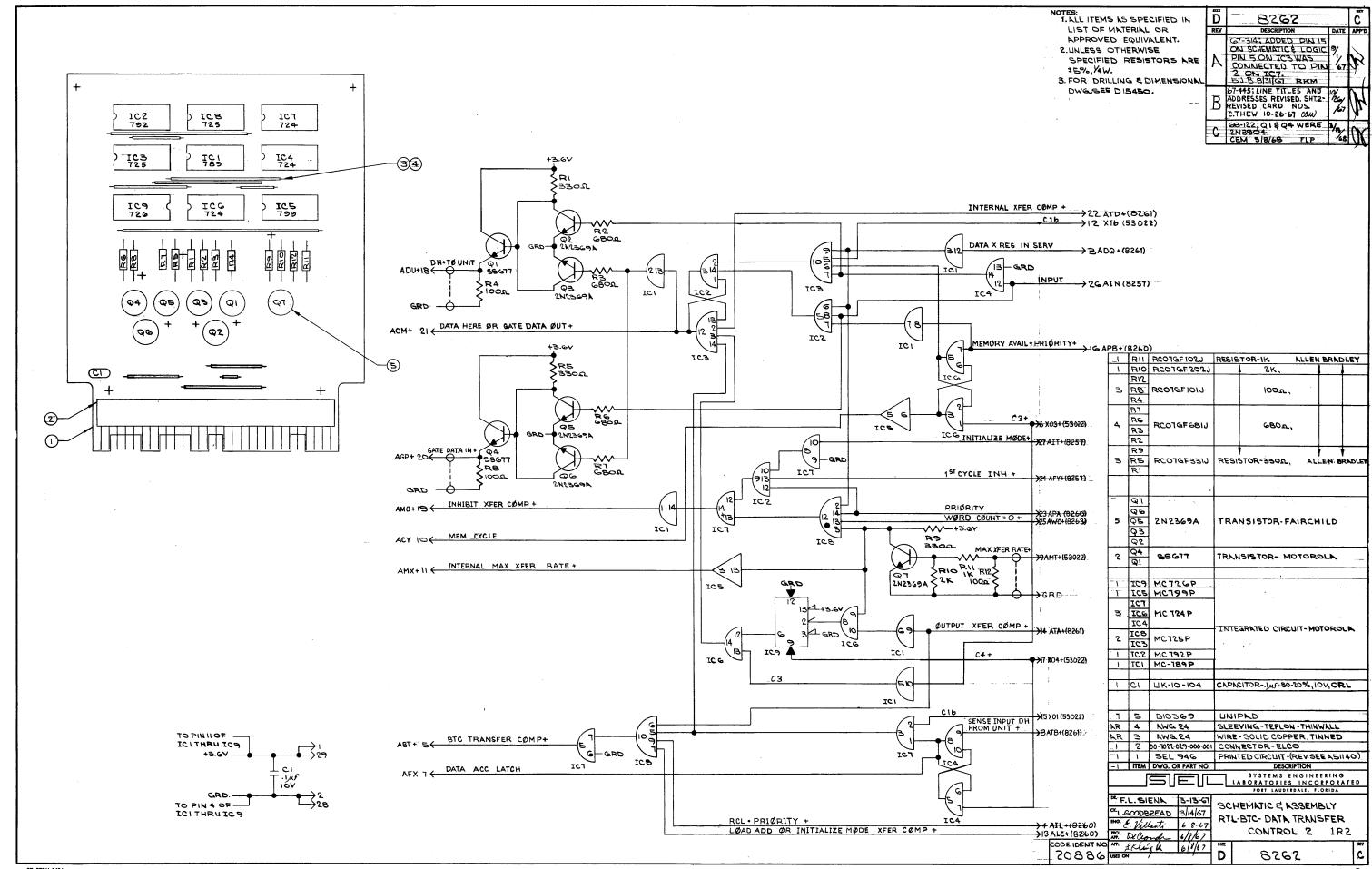


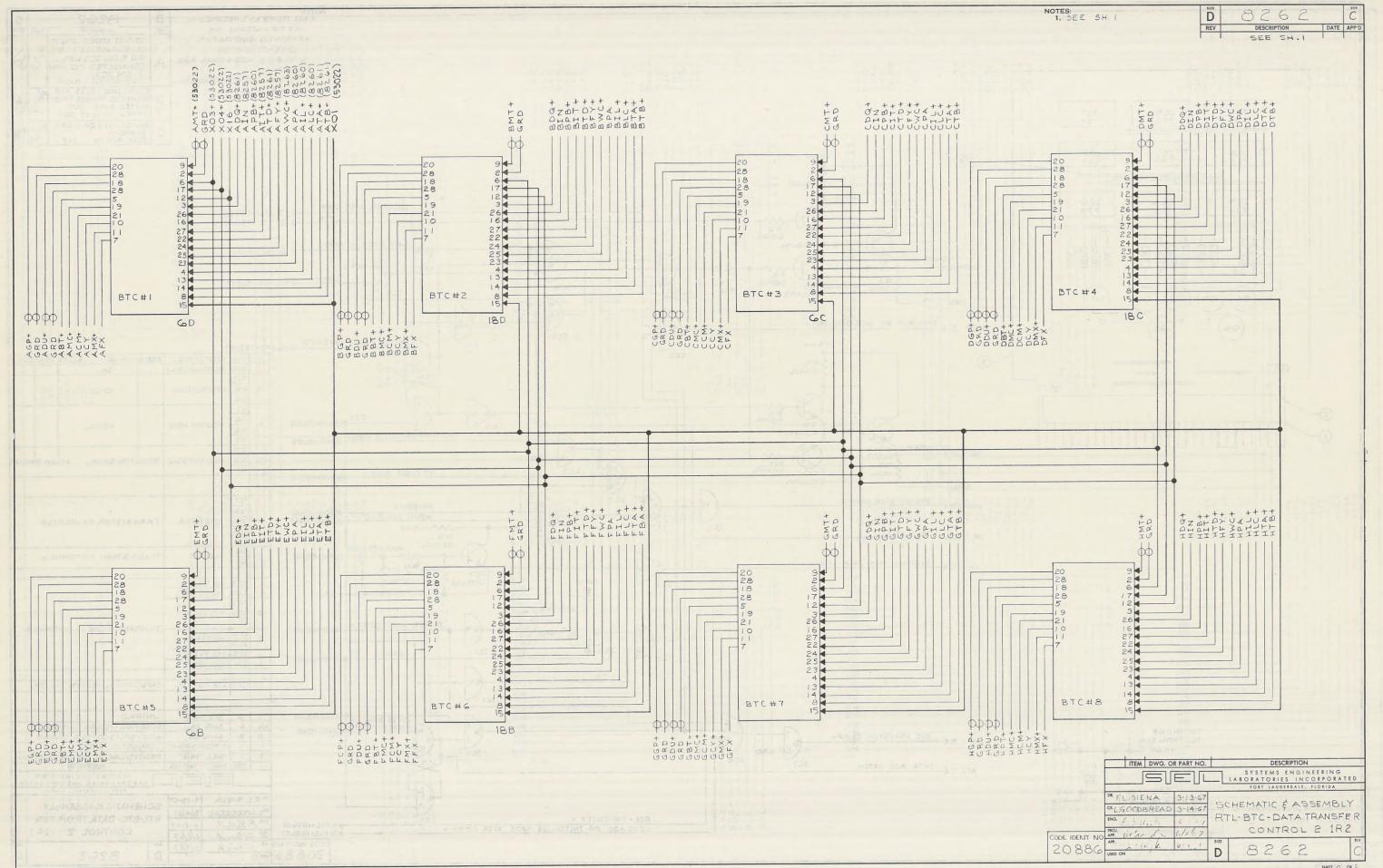




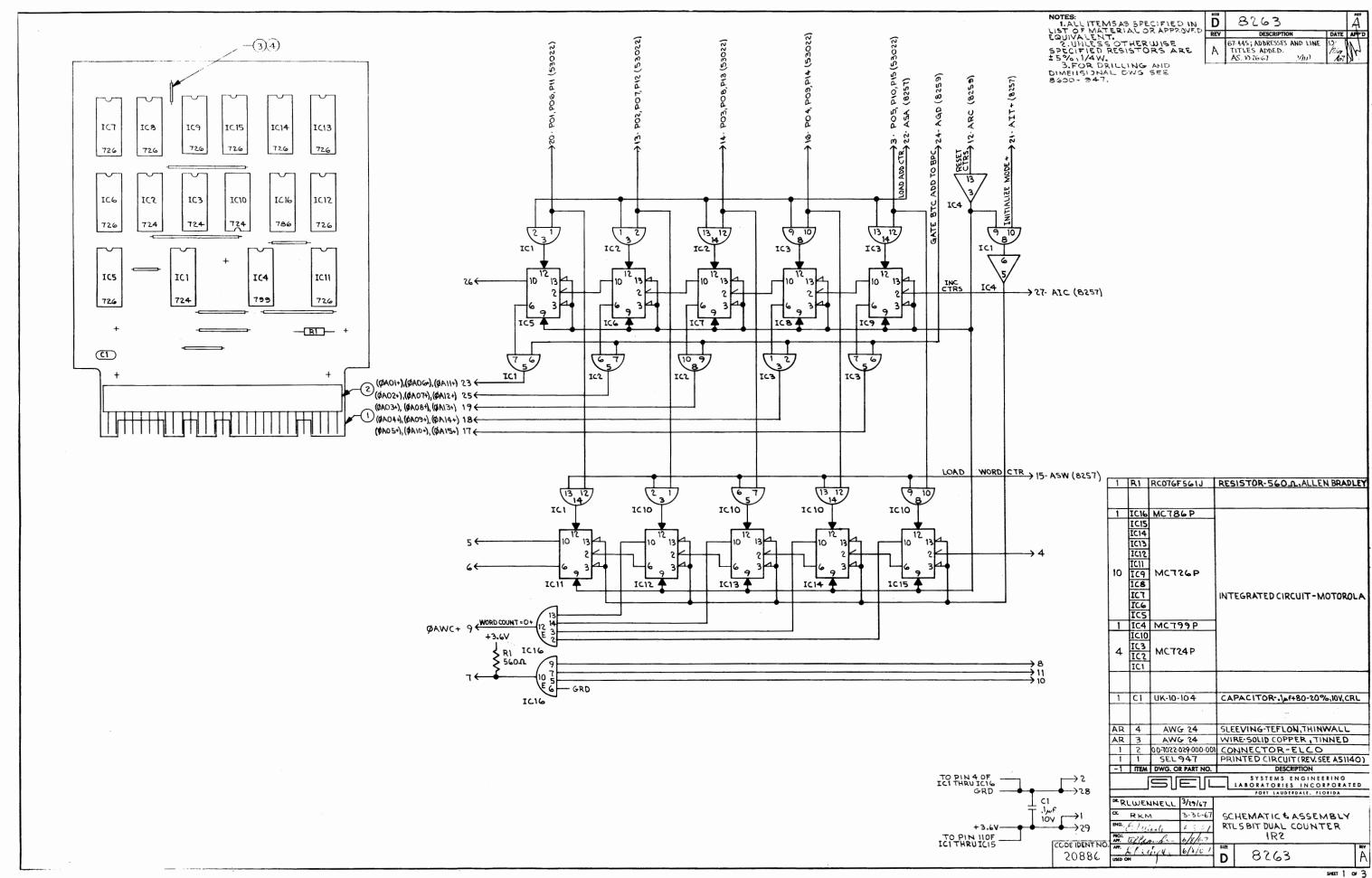


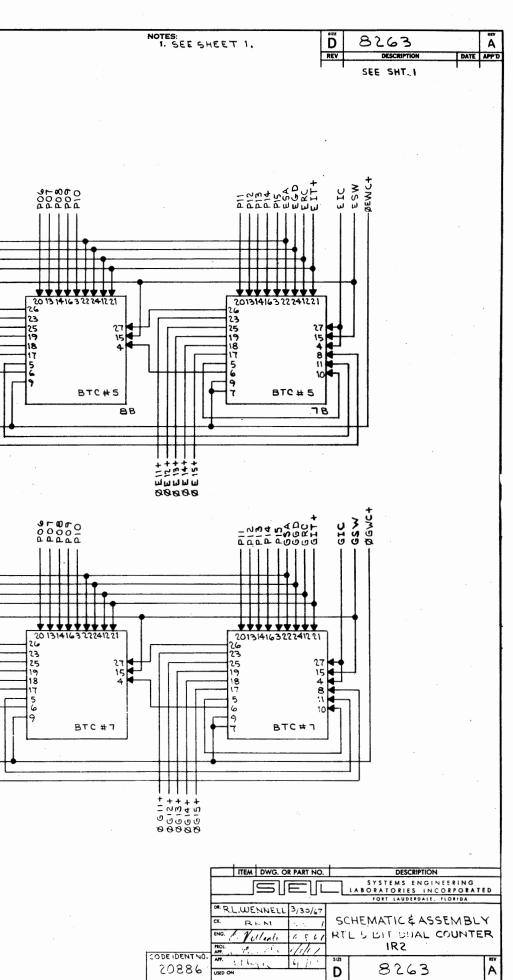


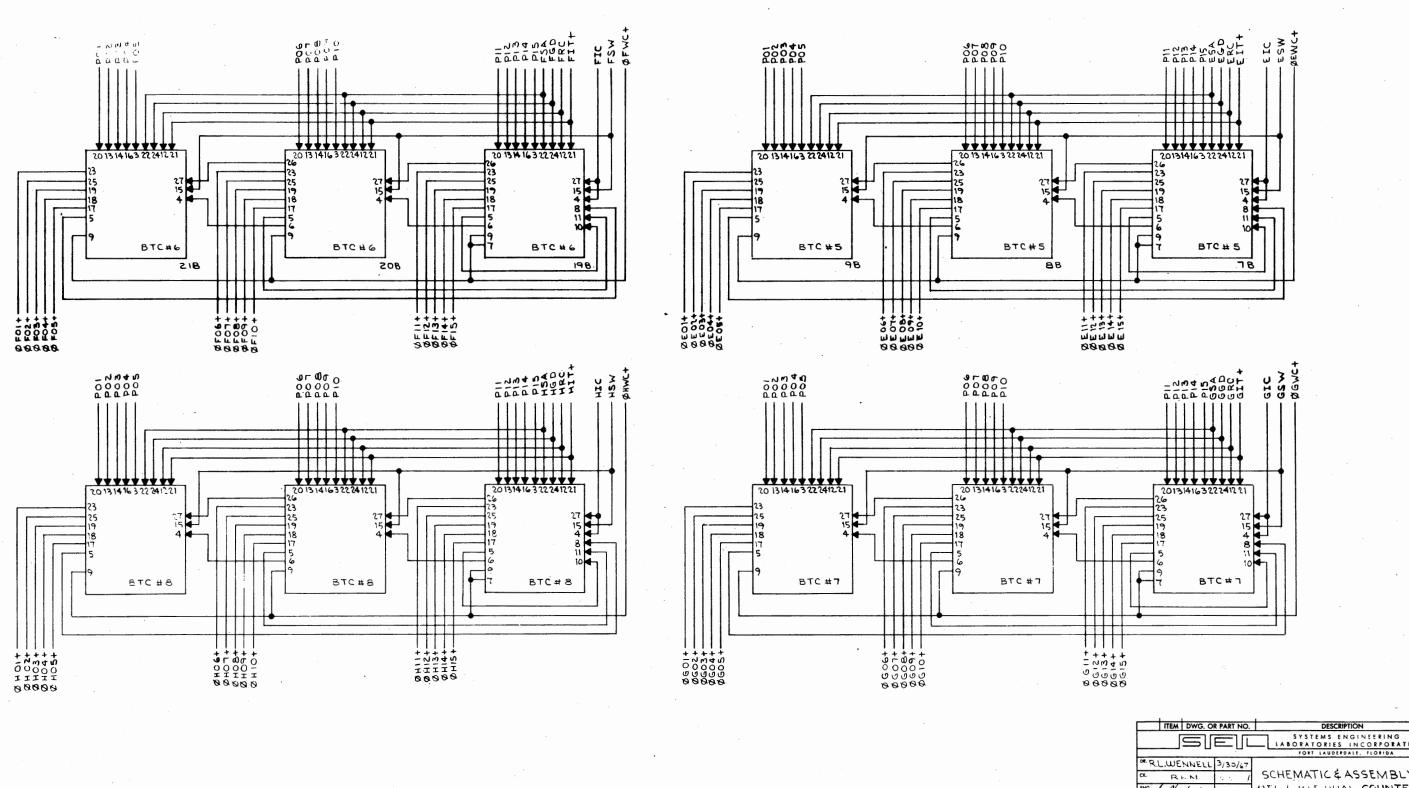




SEL FORM 063A







NOTES: 1. SEE SHEET 1. 8263 D REV DESCRIPTION DATE APP'D

NG. A Vullenti 6561

thur.

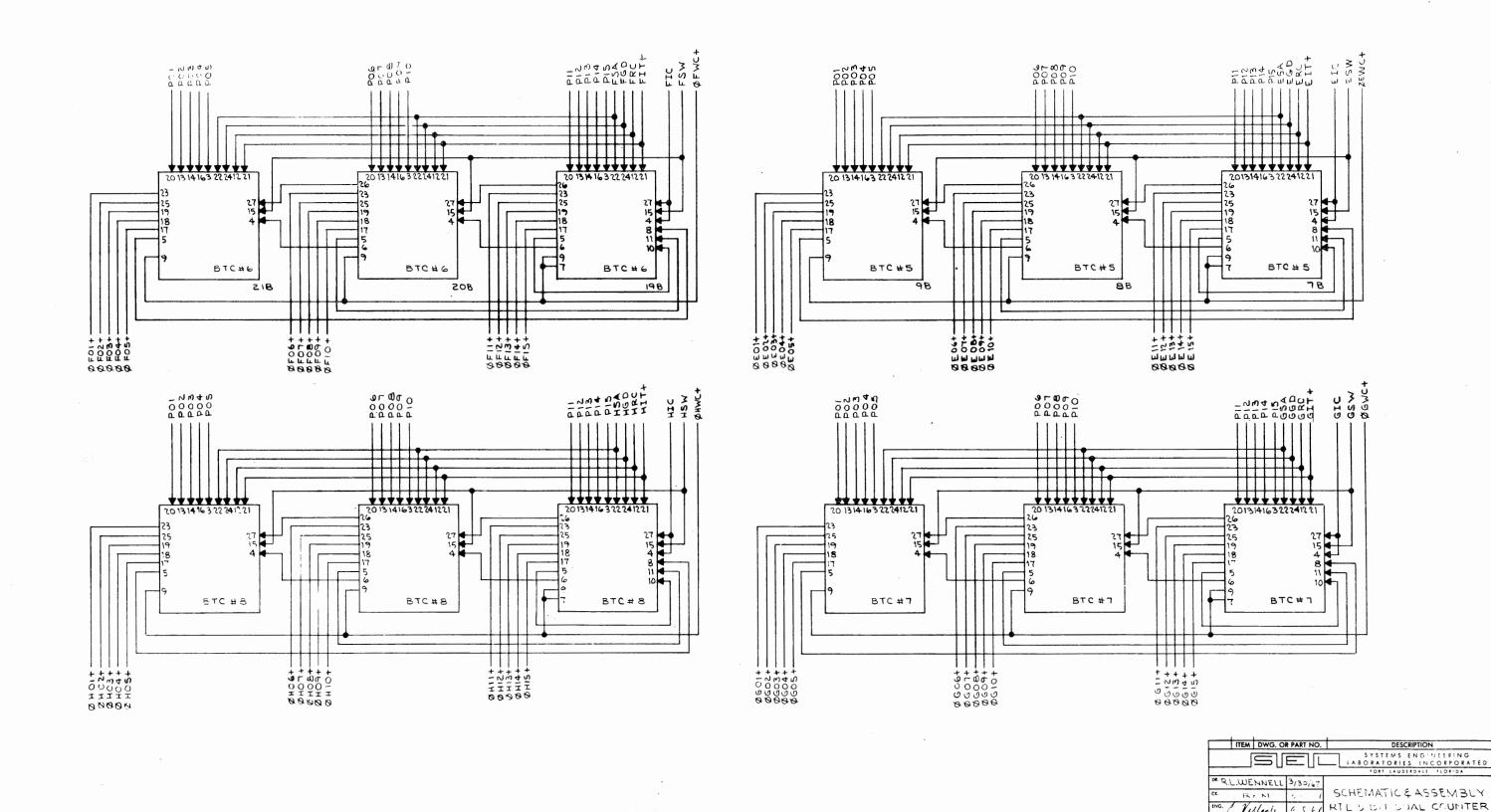
CODE IDENT NO.

20886

Cer. 15 1/1/11

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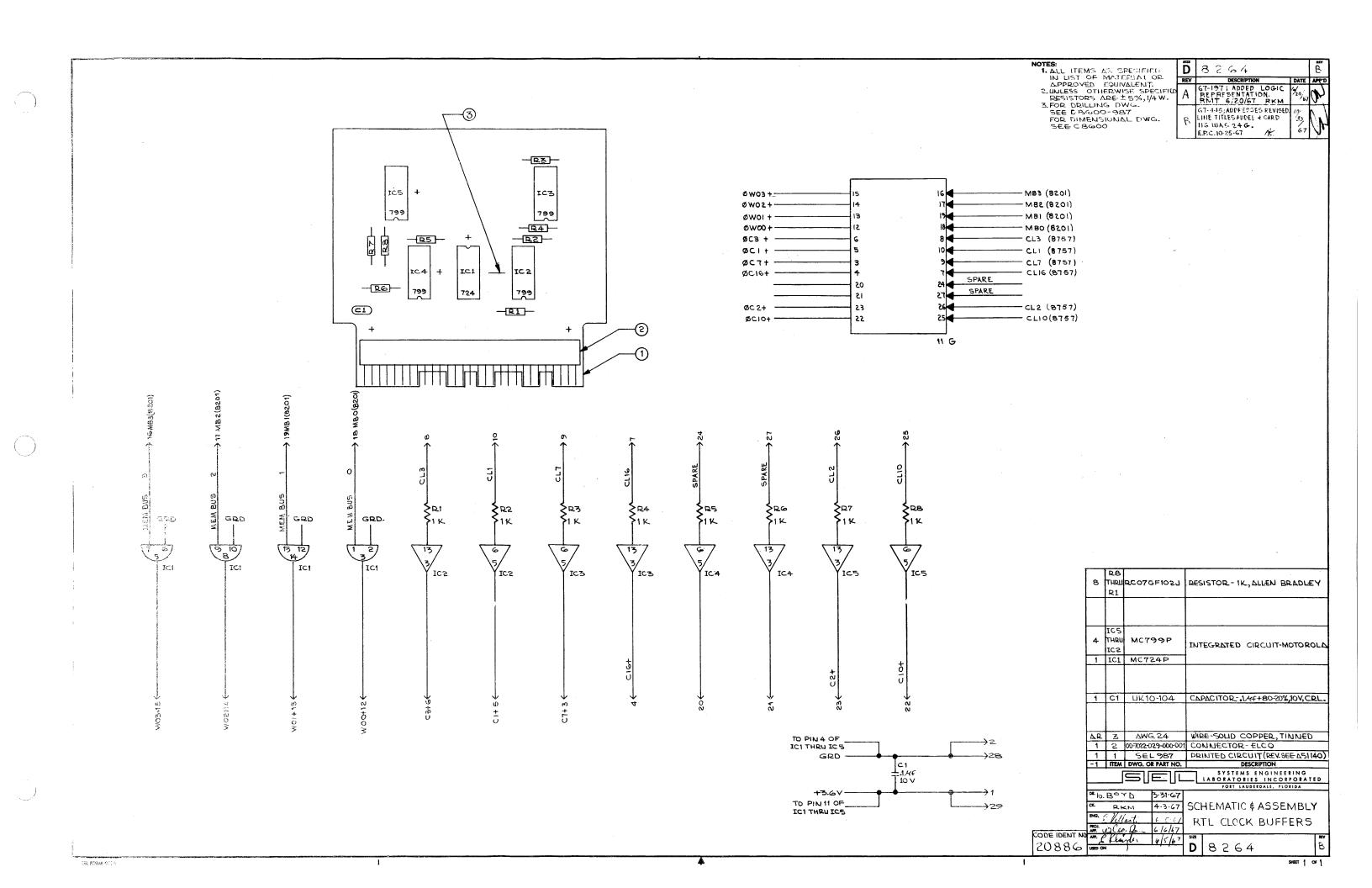


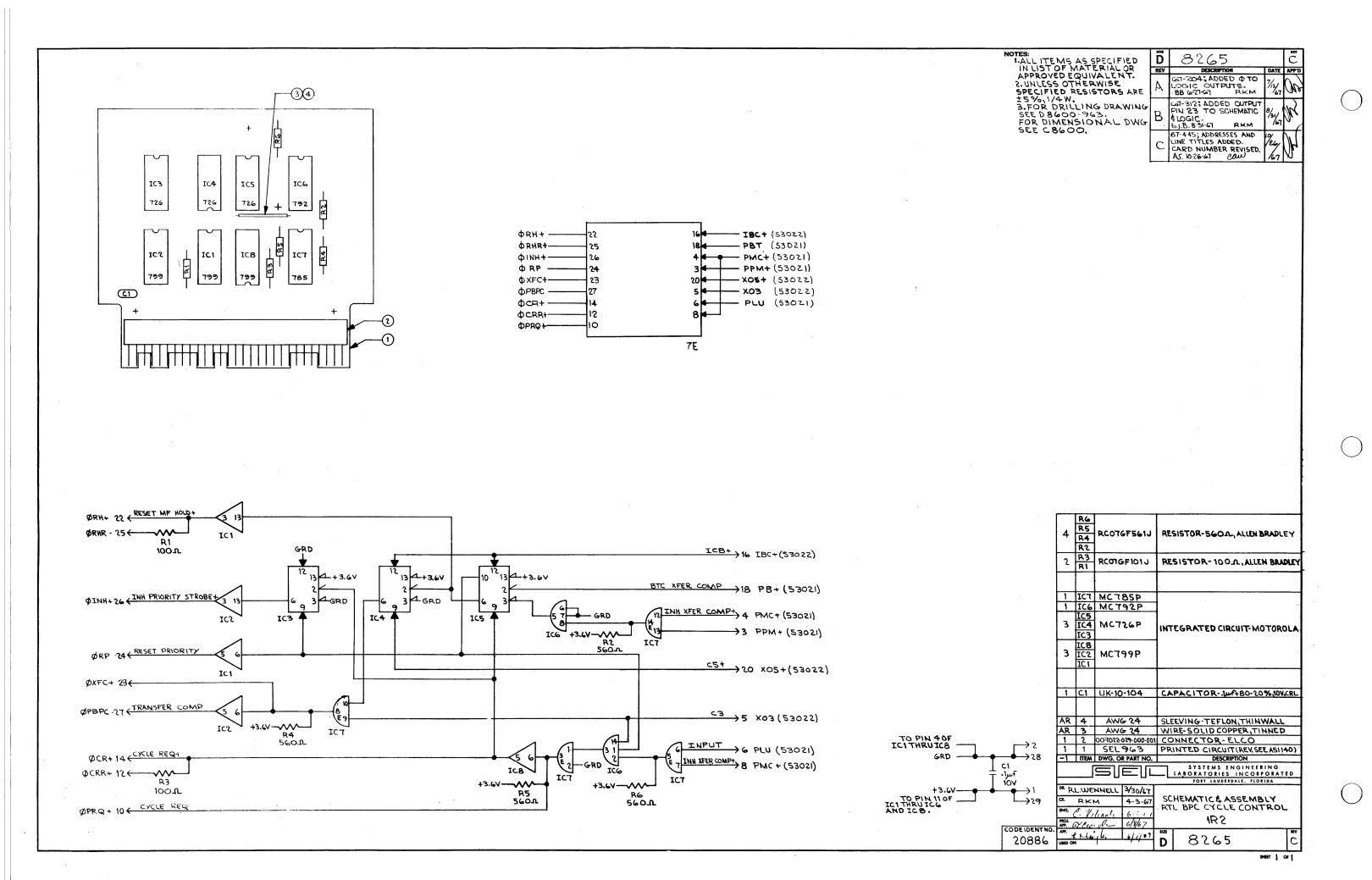
CEI ECMM 0434

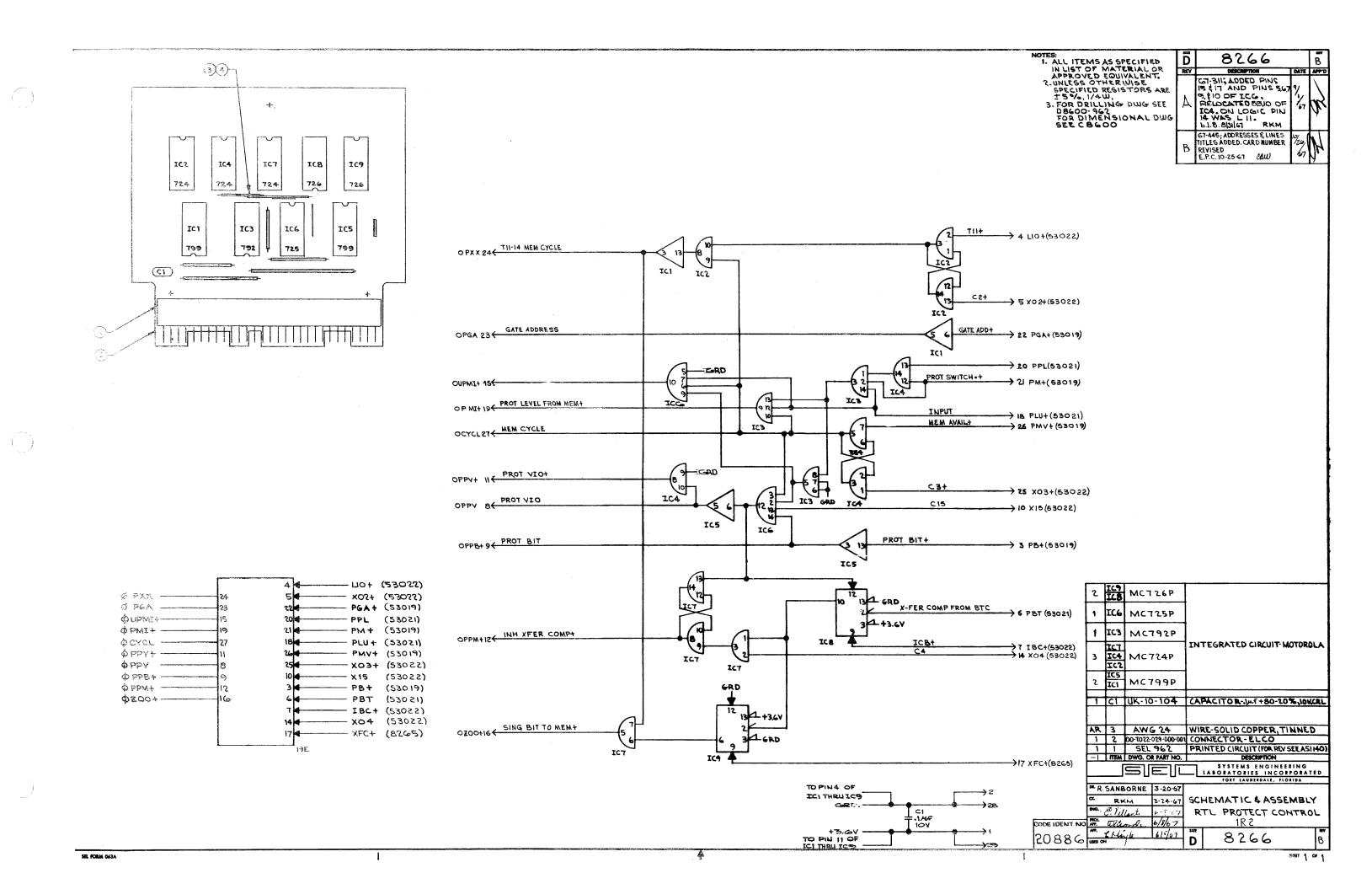
IRZ

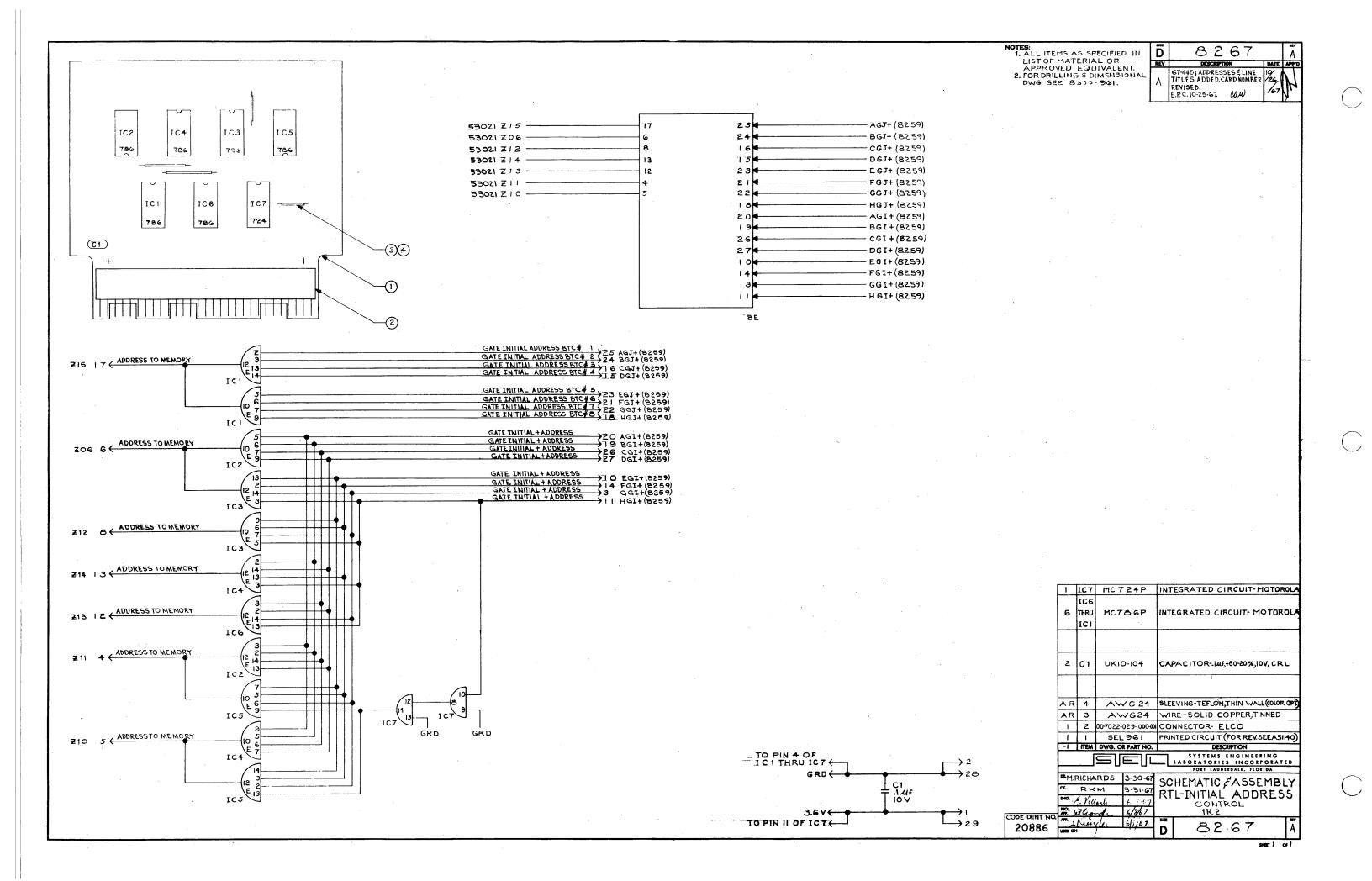
8263

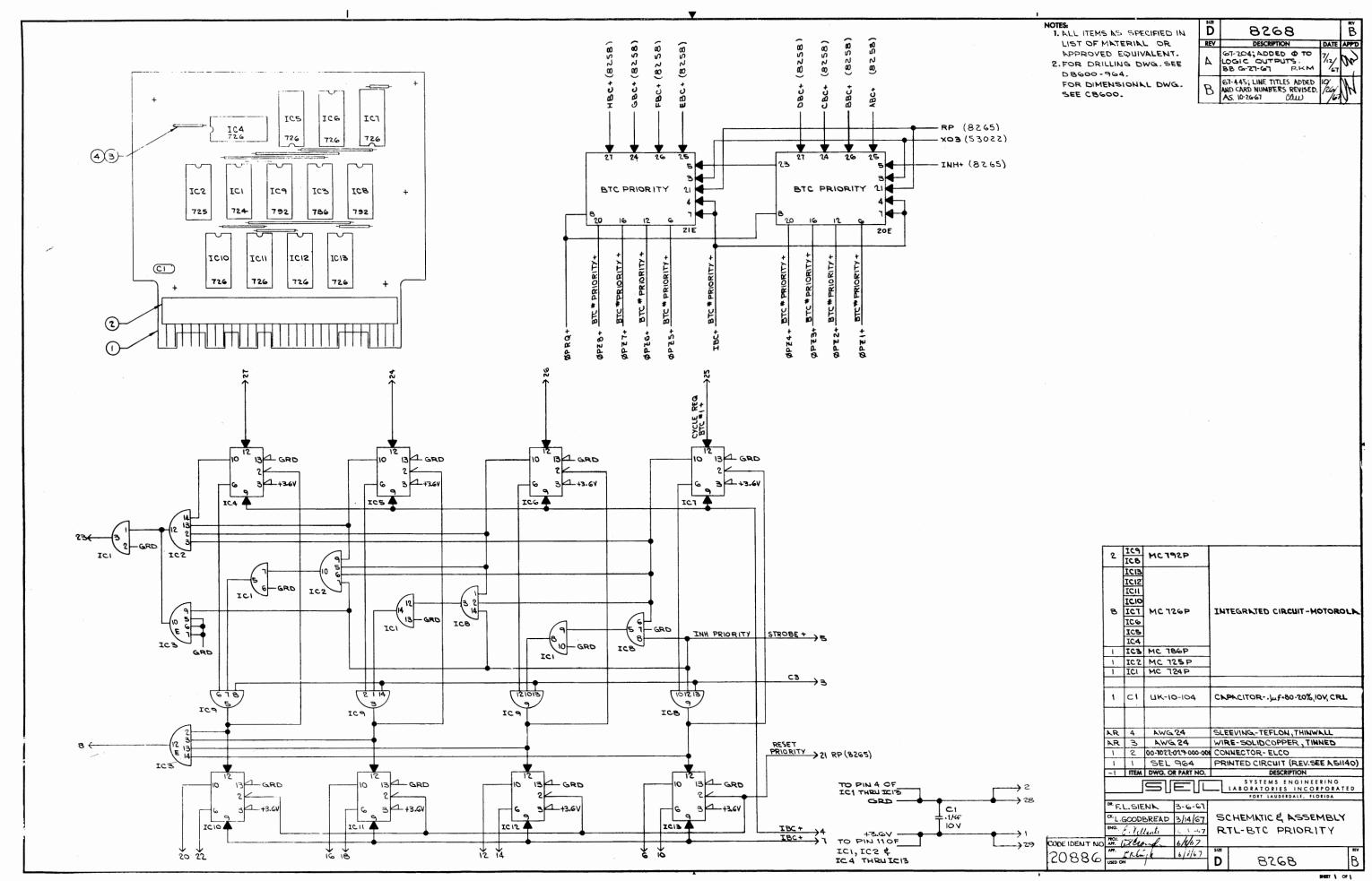


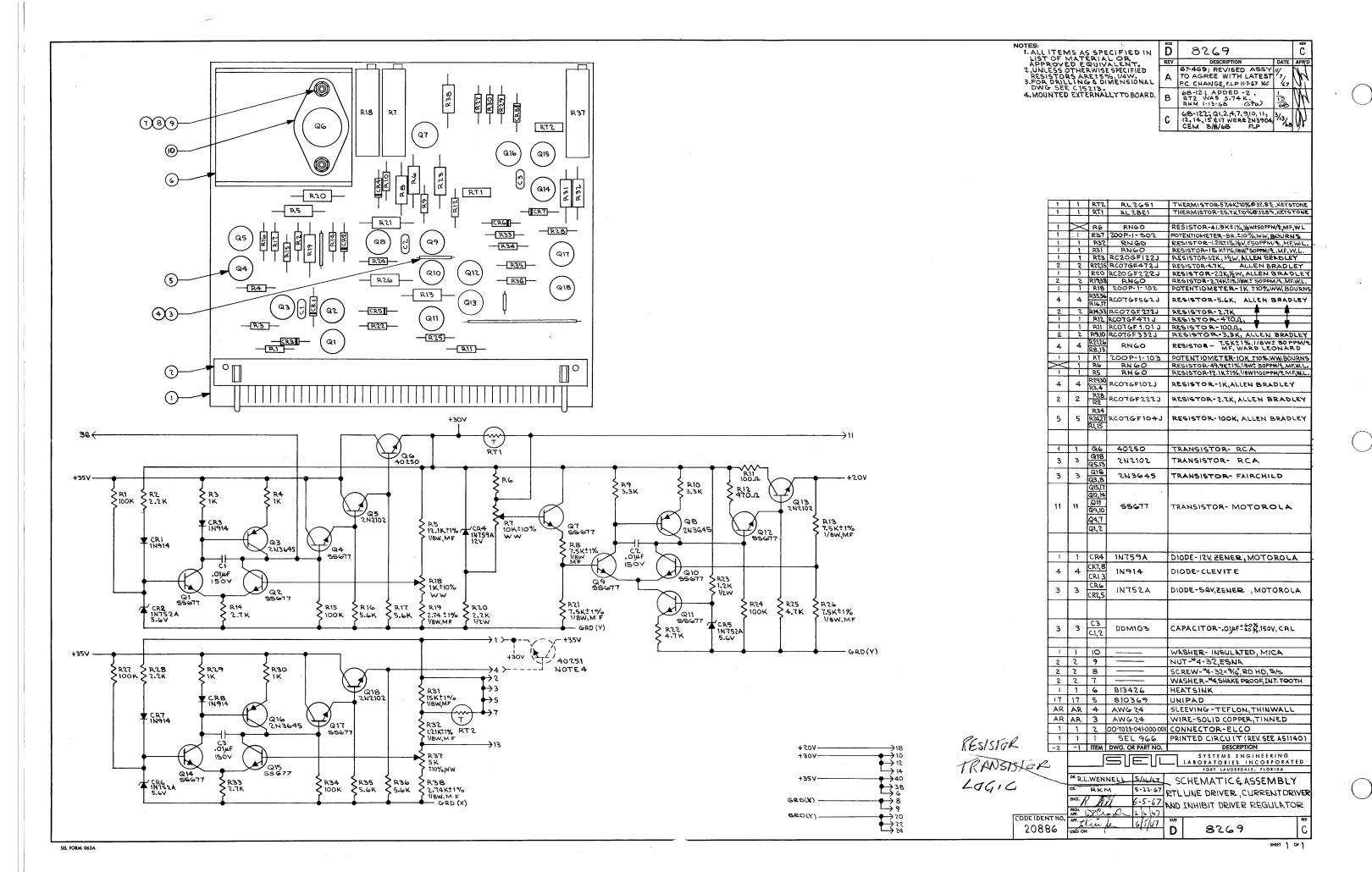


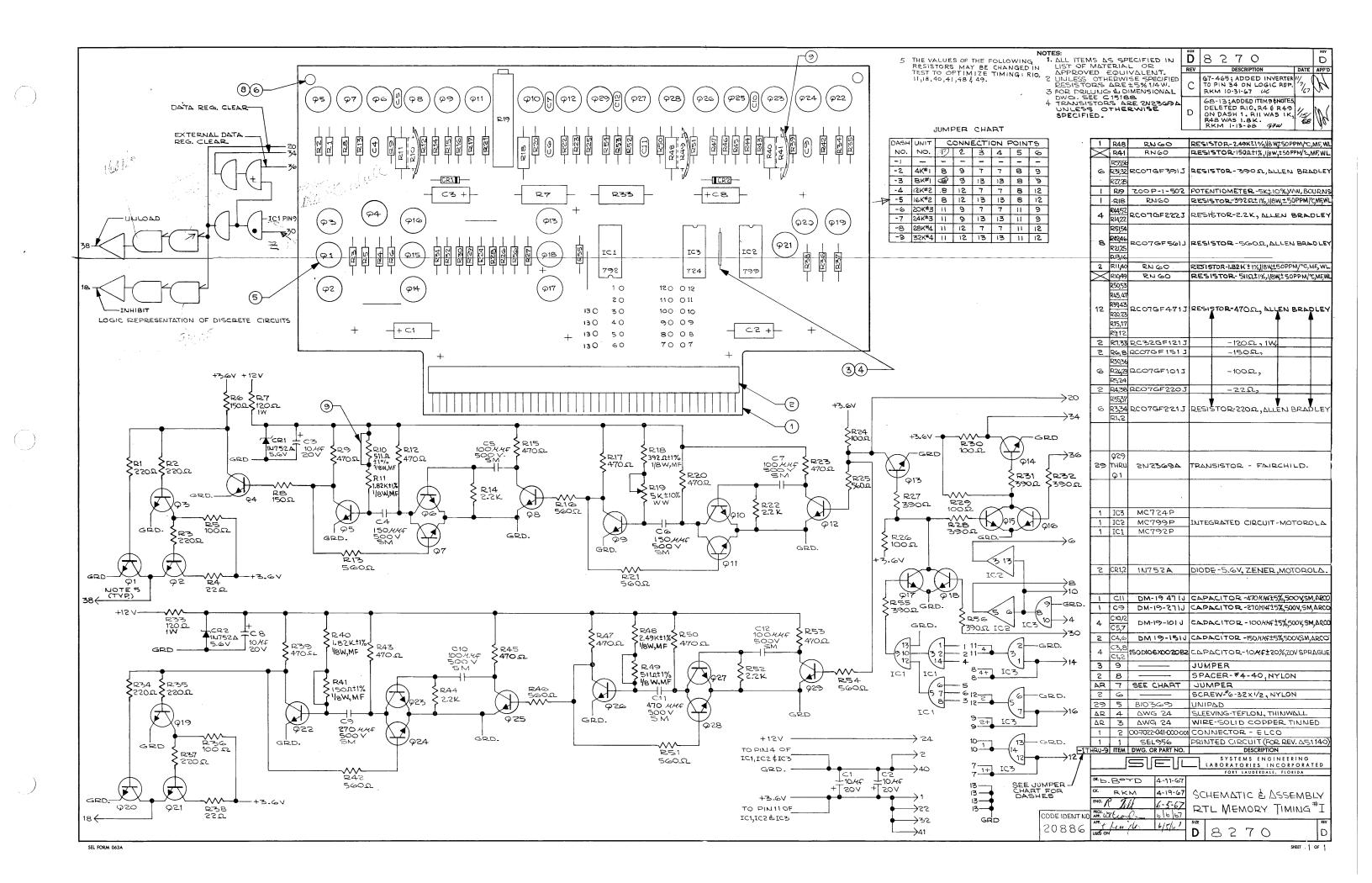


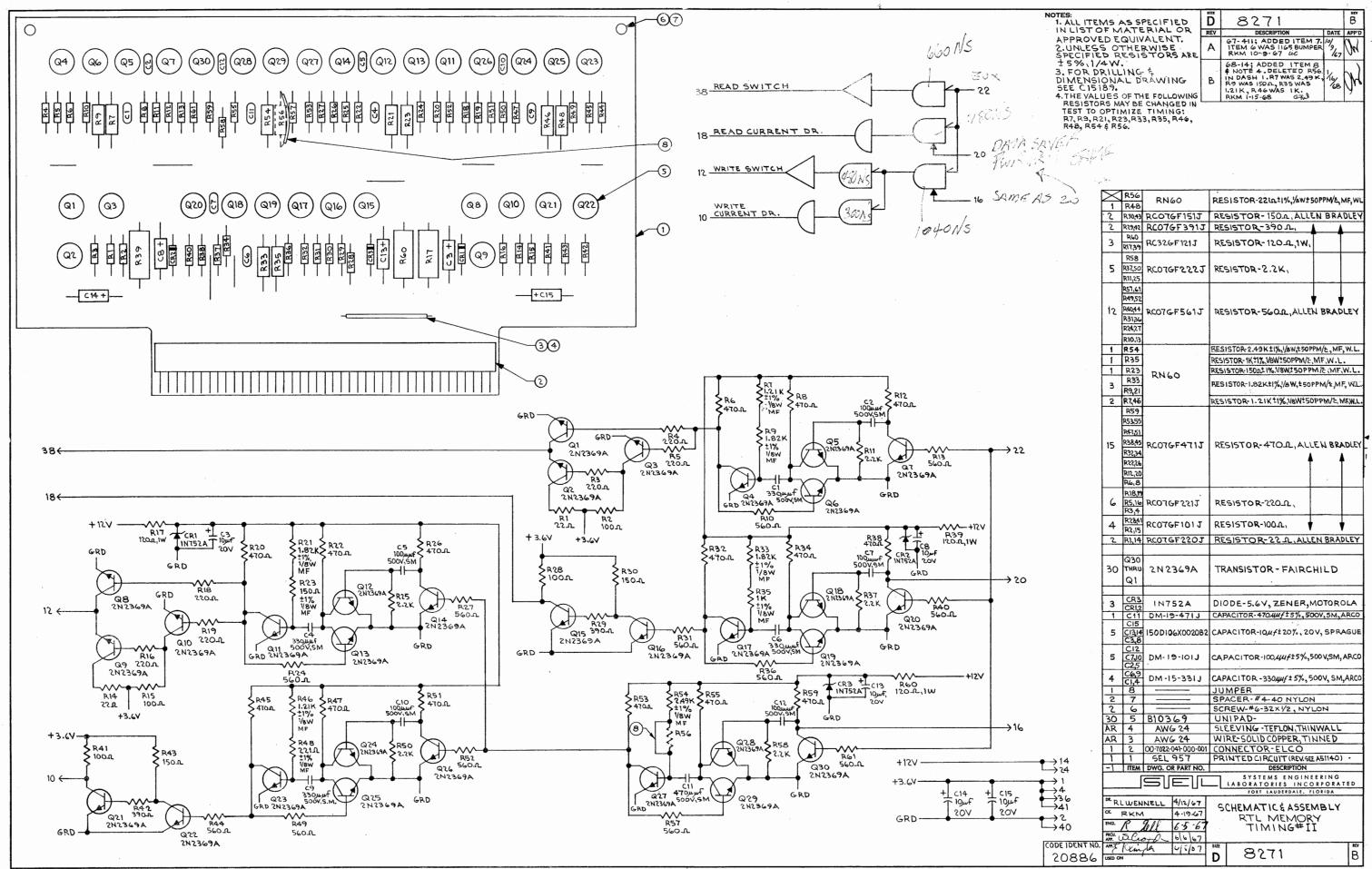


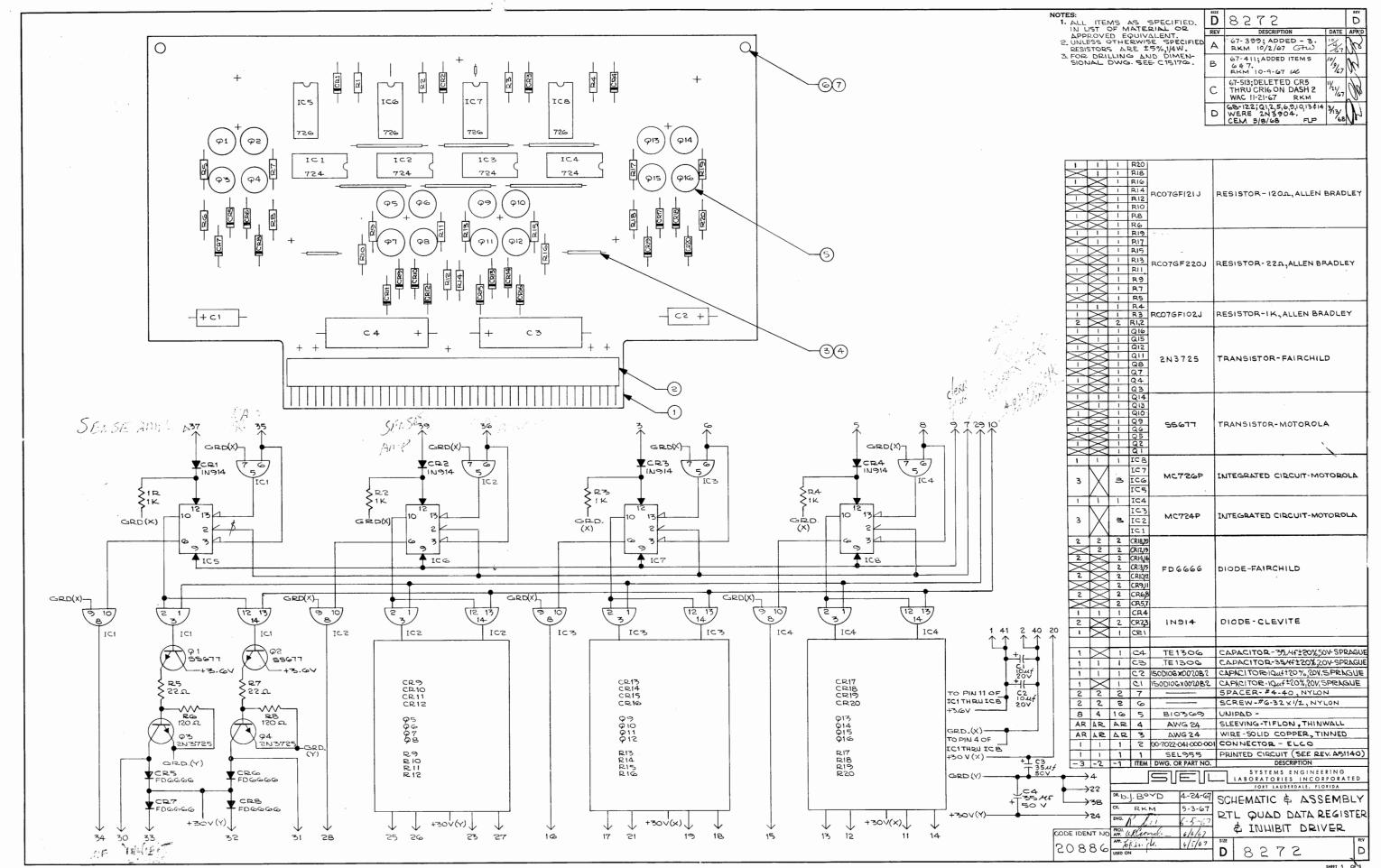




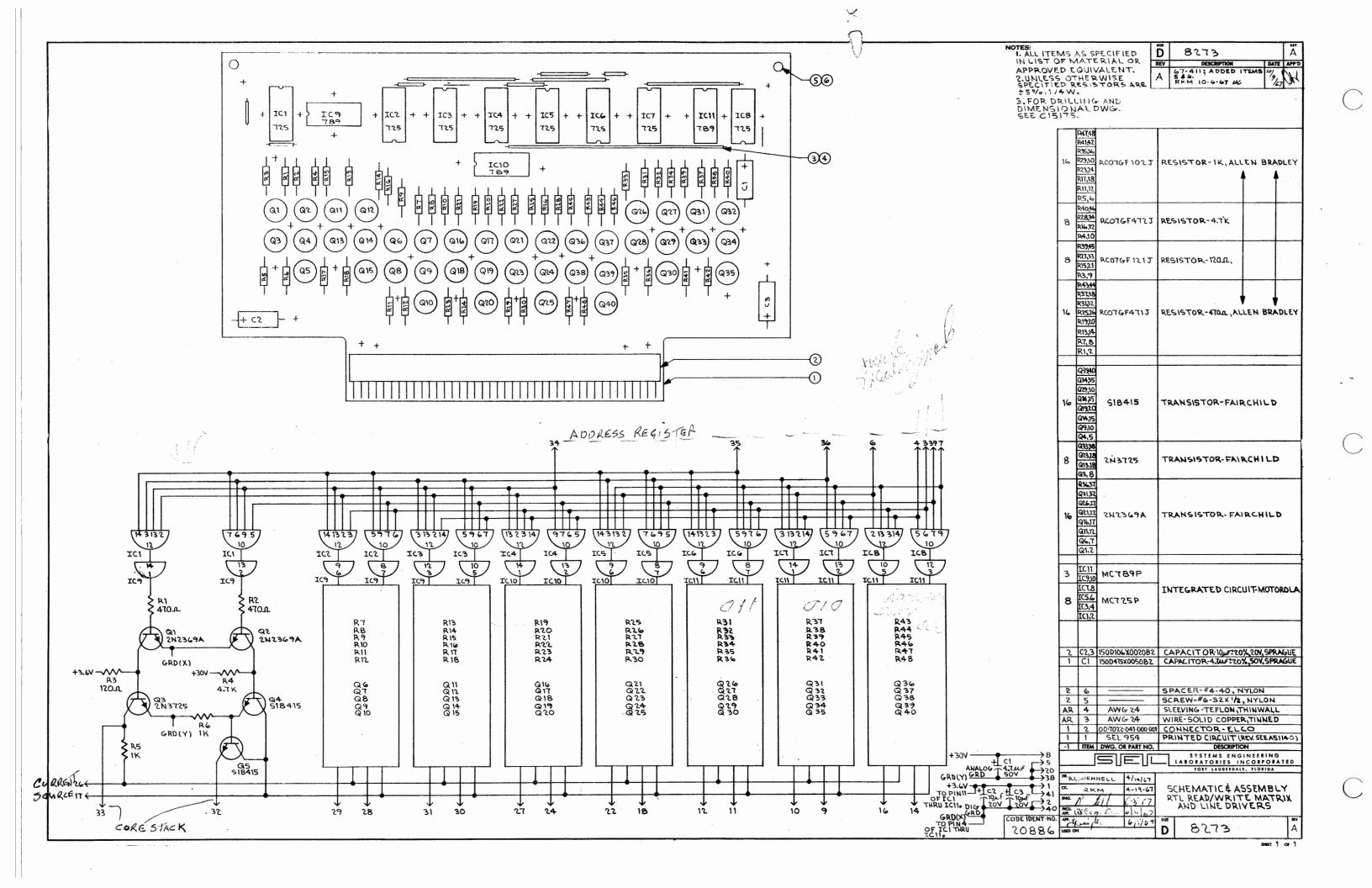


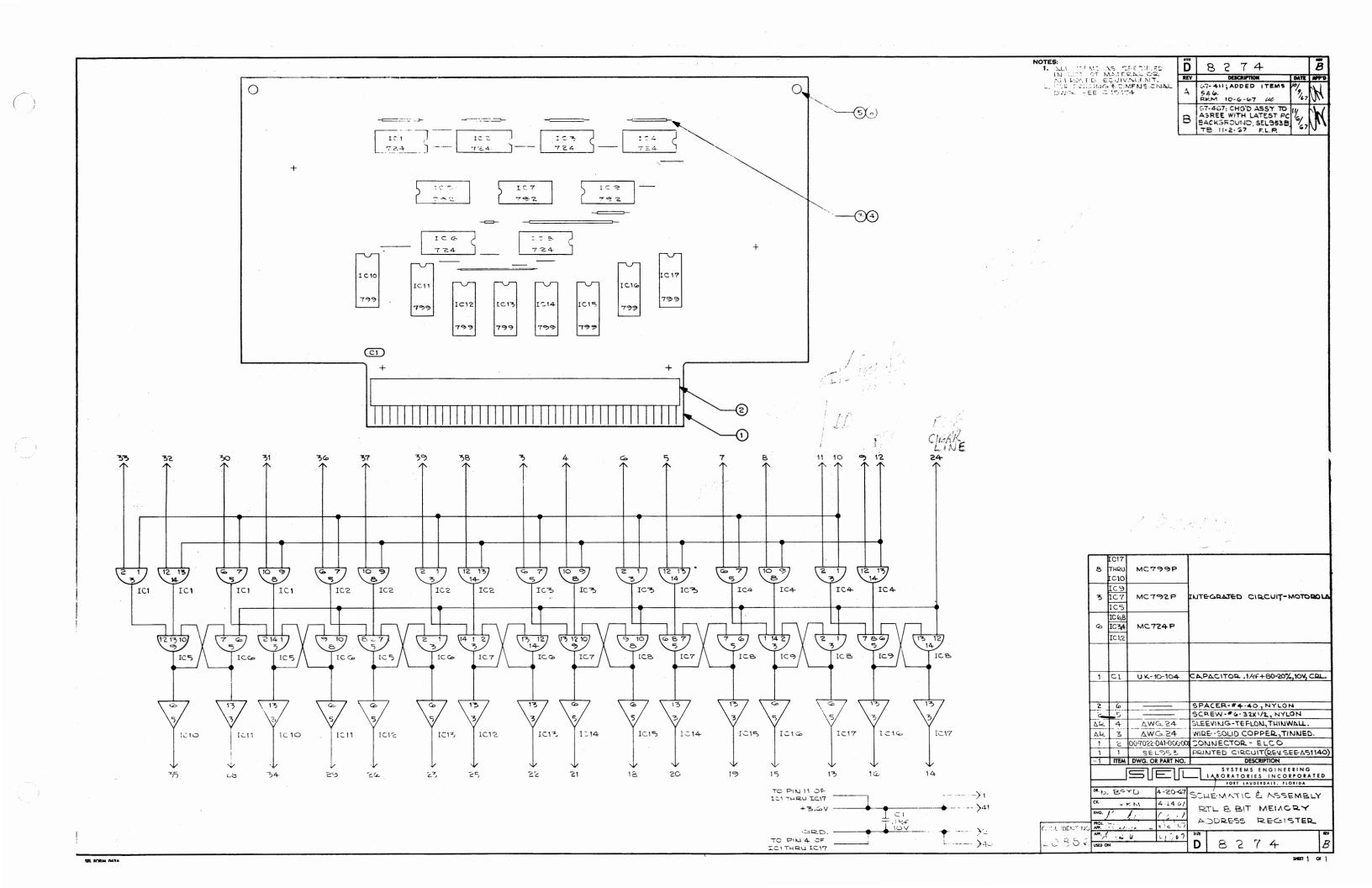


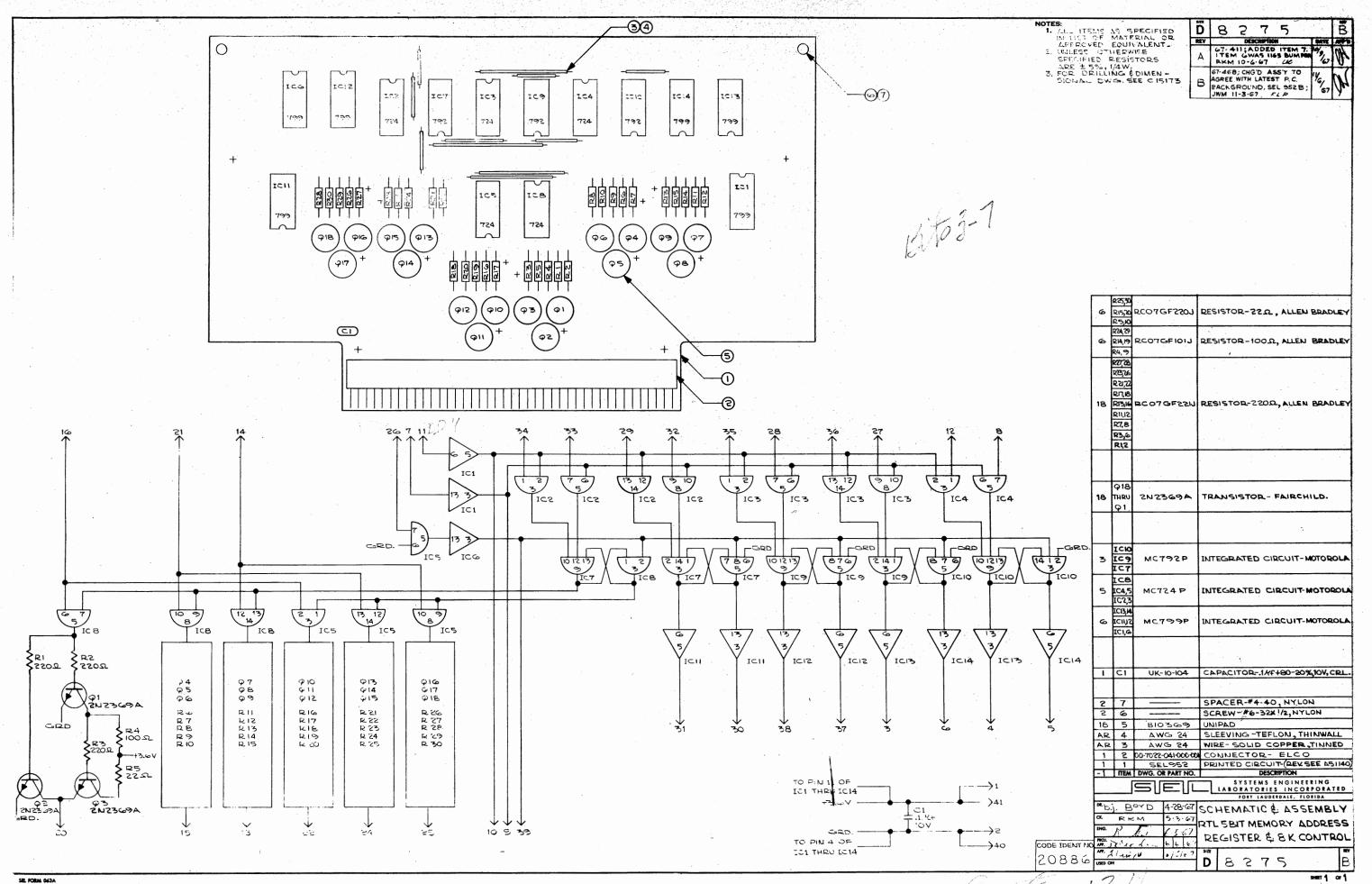




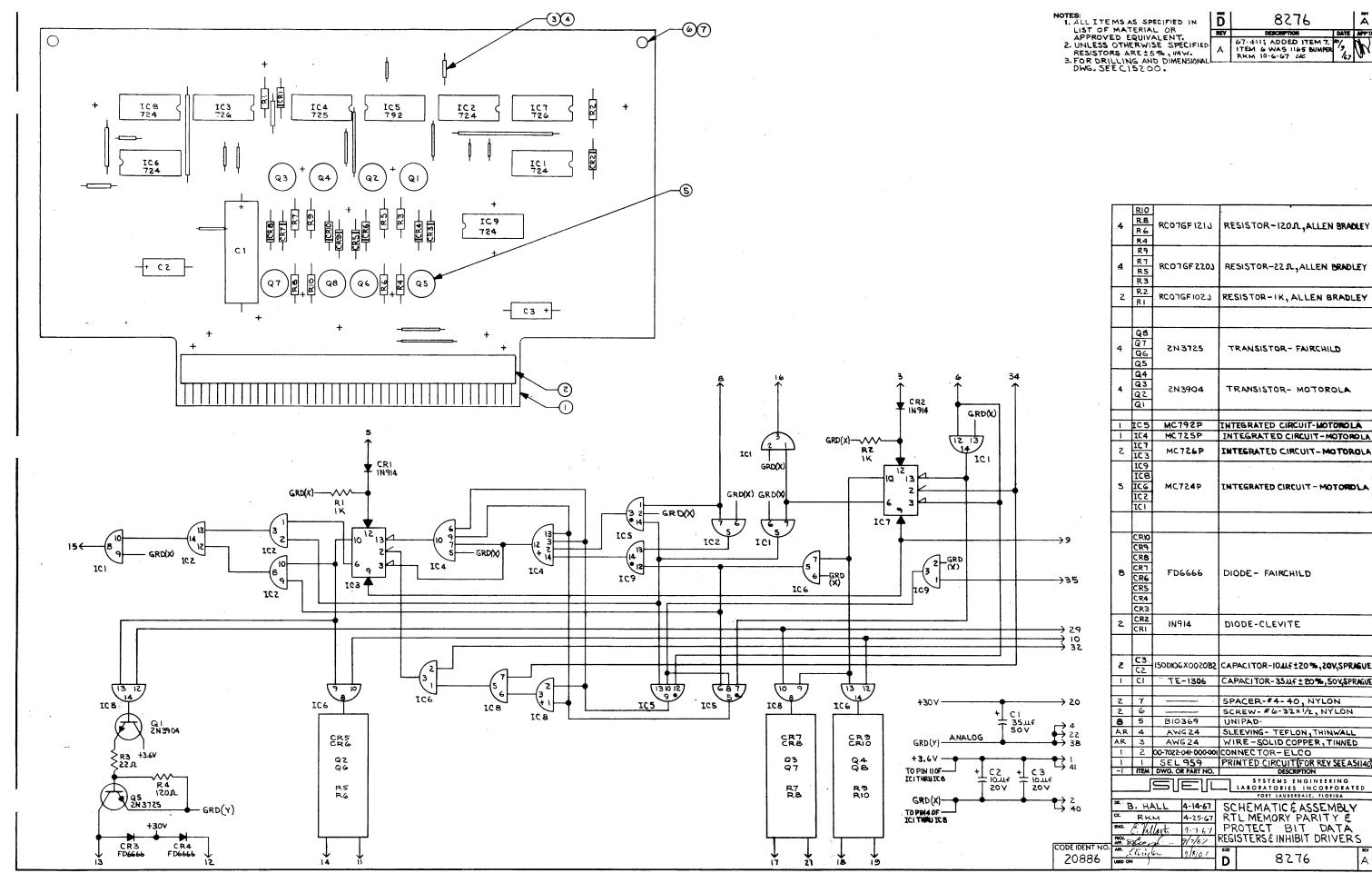
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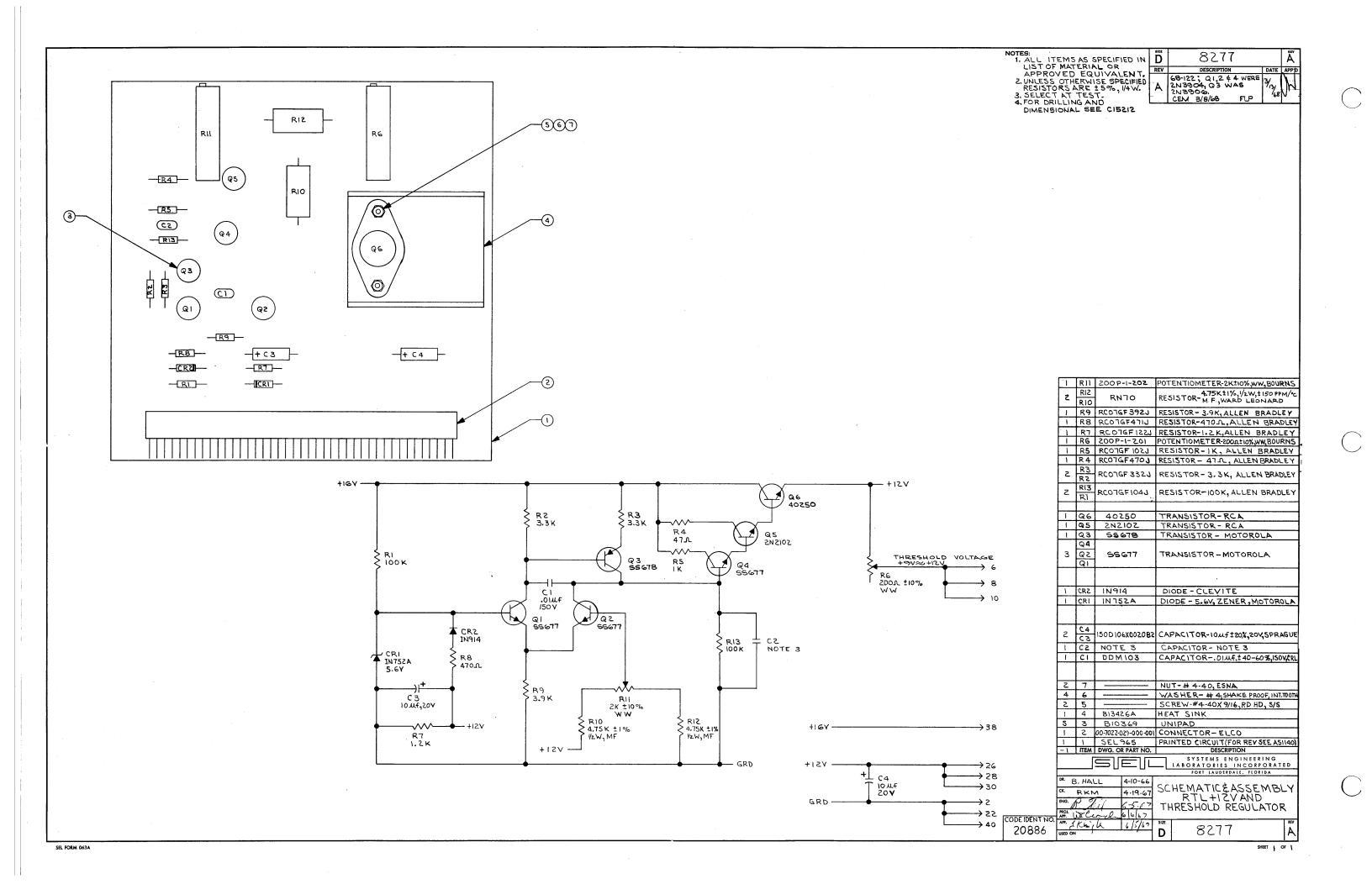


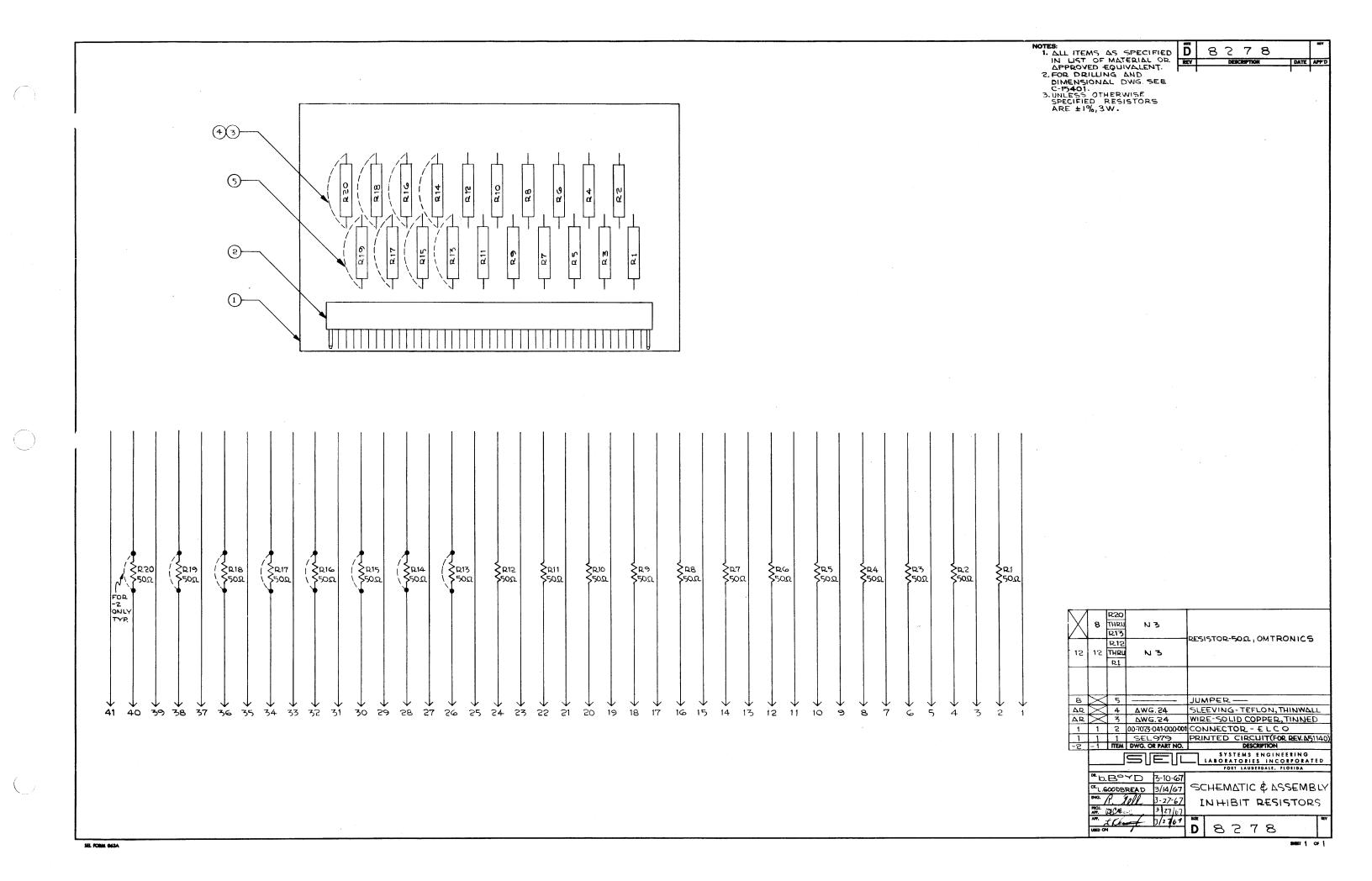




Location 13/1









A.1N971B MAY BE USED IN PLACE

OF IN4750.

5. SCHEMATIC ALSO SHOWN ON

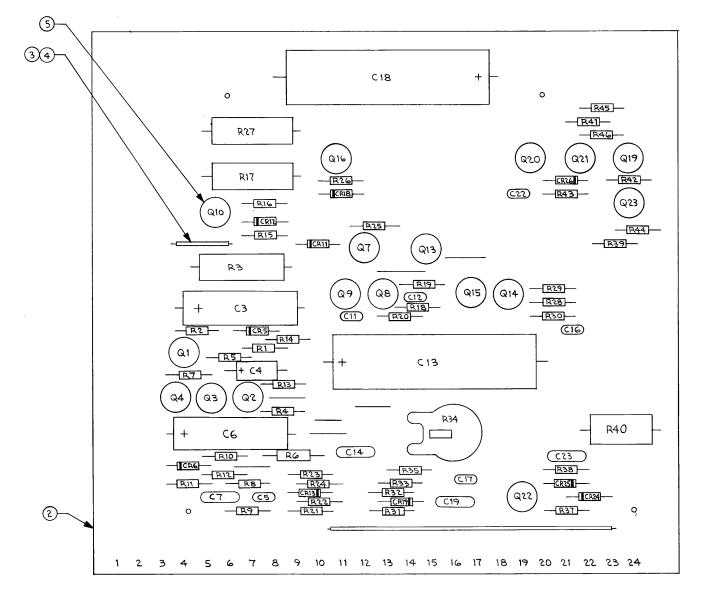
D53140.

D53140.

NOTES:

1. ALLITEMS AS SPECIFIED
IN LIST OF MATERIAL OR
APPROVED EQUIVALENT.
2. UNLESS OTHERWISE
SPECIFIED RESISTORS
ARE ±5%,1/4.W.
3. FOR DRILLING & DIM DWG
SEE. C15473

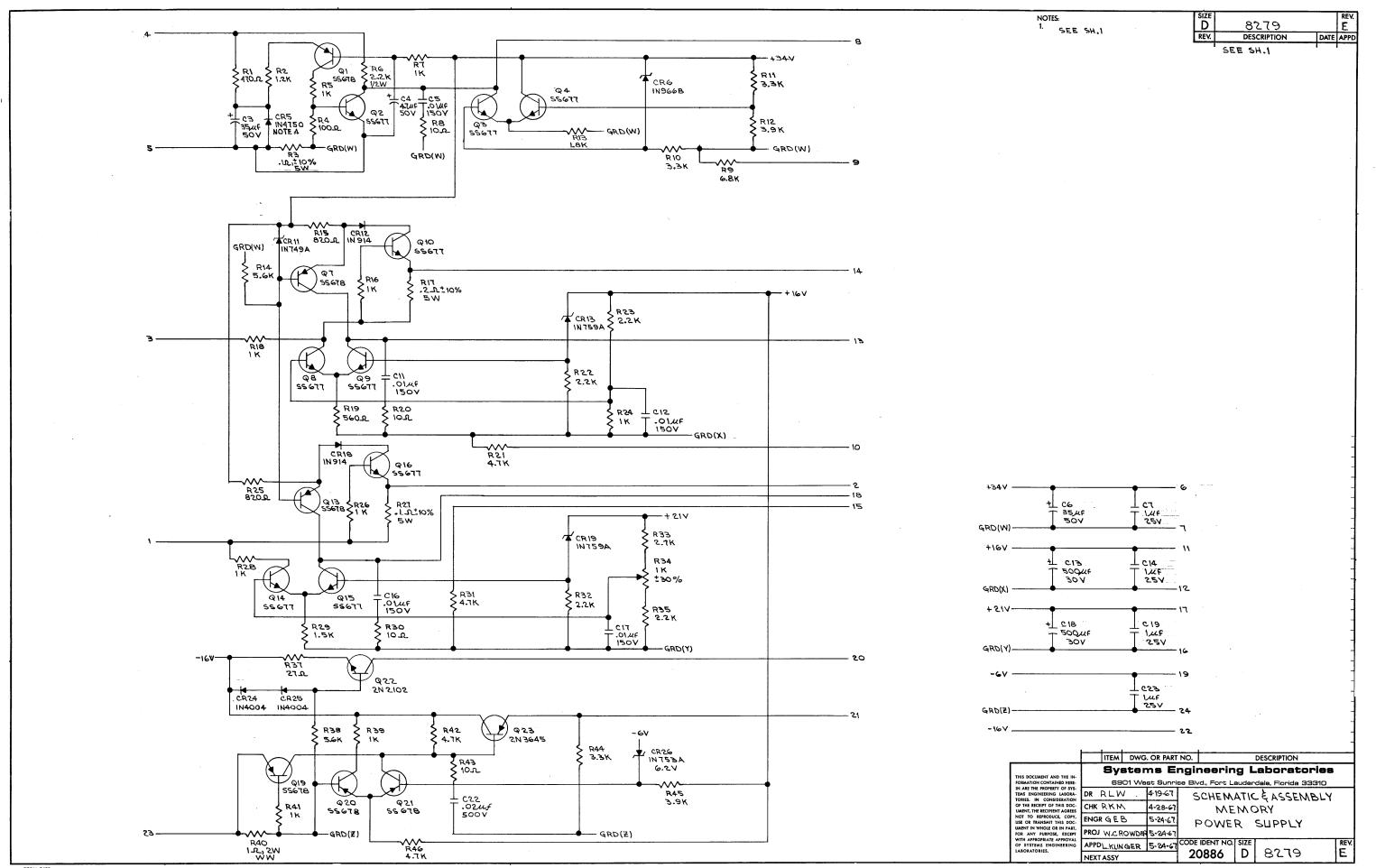
D	8279		E
REV	DESCRIPTION	DATE	APP'D
U	67-388; VALUE OF C22 WAS .022.46, 150V. RKM 10-5-67 G7W	15/07	M
D	67-436; Q20 ¢ Q21 WERE SHOWN AS NPNS. RKM 10-19-67 - 46	10/20/07	B
E	67-520; R27 WAS . 2 1 WAC 11-21-67 RKM	11/21/67	M

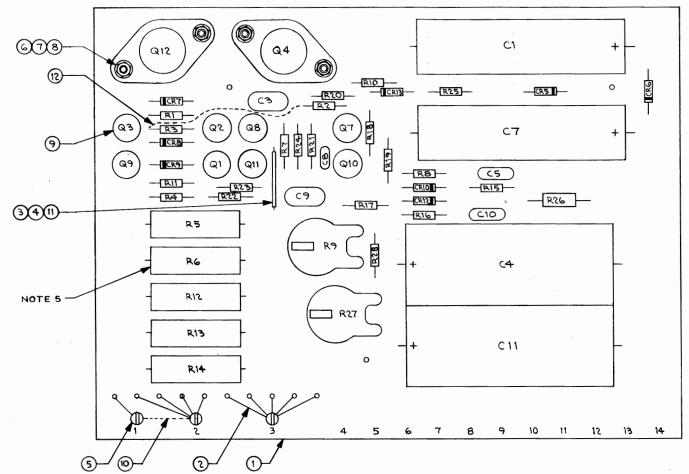


1	R40	BWH	RESISTOR-1_L, 2W, WW, IRC				
1	R37	RCO7GF270J	RESISTOR-27.1. ALLEN BRADLEY				
1	R34	MTC-4	POTENTIOMETER-1K +30%, MALLORY				
1	R33	RCOTGF2725	RESISTOR-2.7K, ALLEN BRADLEY				
. 1	R29	RC076F152J	RESISTOR- 1.5K,				
4 —	R32,35	10にクコピ Eりりつ エリ	A 224				
	R22,23		- 2.2K,				
4	R4246	R4246	-4.7K,				
	R81,31	RCO7GF472J	··K, V				
1	R19	RCOTGF561J	-5601, ALLEN BRADLEY				
1	RIT	PW5	21 ±10%,5W,IRC				
0	a R25	2647650047	0000 11151 5845151				
5	R15	RC07GF821J	-8201, ALLEN BRADLEY				
٦	R14,38	RCO7GF562J	-5.6K, ALLEN BRADLEY				
1	R13	RCOTGF182J	RESISTOR-1.8K, ALLEN BRADLEY				
-1	ITEM	DWG. OR PART NO.	DESCRIPTION				

2		RCO7GF392J	RESISTOR- 3.9K, ALLEN BRADLEY			
3	R44 R10,11	RC076F332J	- 3.3K, ALLEN BRADLEY			
1	R9	RCO7GF682J	- 6.8K, ALLEN BRADLEY			
4	R30,43 R8,20		-101, ALLEN BRADLEY			
1	R6	RCZOGFZZZJ	- 2.2K, 1/2W, ALLEN BRADLEY			
9	R39;41 R26;28 R18;24 H7;16 R5	RC07GF102J	-1K,ALLEN BRADLEY			
1	R4	RCO7GF101J	-1001, ALLEN BRADLEY			
2	R3,27	PW5	11, ±10%, 5 W, IRC			
1	RZ	RCOTGF122J	- 1.2K, ALLEN BRADLEY			
1	R1	RCO7GF471J	RESISTOR-4701, ALLEN BRADLEY			
-1	ITEM	DWG. OR PART NO.	DESCRIPTION			

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	1	-	000	0110	(A E	77,	WEISTAR FAIRCHLE	
	ļ	1	Q23	2N3			WSISTOR- FAIRCHILD	
		1	QZZ	SNS	10.2	IRA	ANSISTOR-RCA	
		_				Į.		
		_						
	t		Q16					
			Q14,15					
		9	Q9,10	556	77	TRA	NSISTOR-MOTOROLA	i
			Q4,8			-		į
			Q2,3					1
	ŀ		Q21			 		
		6	Q19,20	556	870	TRA	LUSISTOR-MOTOROLA	
			QT,13		_			- 1
			Q1					
	l							
	ļ					l		
		1	CR26	1N75	3A	וחום	DE-6.2V, ZENER, MOTOROL	
		5	CR2425				E- MOTOROLA	
		5	CR13,19	1N7			DE-12Y, ZENER, MOTORO	N A
								- CA
		5	CRI2J8	1119			DE- CLEVITE	
		1	CRII		49 A		DE-4.3V, ZENER, MOTOR	
		1	CR6		166B	010	DE-16V, ZEHER, MOTORO	7 LV
		1	CR5	1N 4	750	DIC	DE - 27V, ZENER, MOTORO	انماد
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		٠	C13/10	10301	5550F CO	1-2.	יייייייייייייייייייייייייייייייייייייי	
		-	C22	100	^3	CAS	ACITOR- 02 4/410% 5004 C	B.
		1		105	<u> </u>	CAF	ACITOR- 0245 + 10%, 500V, C	~_
		4	C19.23	5C	13	CAP	ACITOR-JUF + 20%, 25Y, SPRAG	JUE
			C7,14	_		i i		
		1	C4				ACITOR-4.7/LF, +20%, 50V.5PR	
		2	C3′P	TE-1	306	CAI	PACITOR-354F-10+75%,50V,SPRA	UUE
		<u> </u>	L			ļ	·	
			C17			1	,60%	
		5	C12,16	DDN	1103	CAPACITOROMF-40% ,150V, CRL		RL.
			C5,11					
		$\overline{}$						
		1				1		
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		ĺ		l		1		
		1	1					
		\vdash	+			-		
				D10010		1111	IBAD	
		17	5	B10369			IPAD	-
		AR	4	AWG 24		ا علا	EVING-TEFLON, THIN WALL	
		AR	3	AWG 24			RE-SOLID COPPER, TINNED	
		1	2	SEL982		PRI	NTED CIRCUIT (REVISEE AS11	40)
			<u> </u>	D)4/0 OD = : = : : =		<u> </u>		
		-1	ITEM	DWG, OR PART NO.		DESCRIPTION		
						SYSTEMS ENGINEERING LABORATORIES INCORPORAT	E D	
		<u> </u>				FORT LAUDERDALE, FLORIDA		
				4/24/67				
					4 - 28 - 67	20	HMETIC & ASSEMBLY	
				171	 		MEMORY POWER SUPPLY	
		N	115		5/24/61		-2MCV 2011-71	
1	CORFINENCE	APP. Do Ceone		Q 5/24/67				
	CODEIDENT NO	~~~T\ /		-10-	5/24/67	SIZE	0070	REV
	20886	USED O	N)	1-1-1-	D	8279	E
				3 OH 3				1





1 | R26 RC20GFG 81J RESISTOR-G8QQ, 112W, ALLEN BRADLEY
2 | R2325 RC07GF392J RESISTOR-3.7K,
1 | R24
1 | R21 RC07GF821J RESISTOR-27.Q,
1 | R10 RC07GF270J RESISTOR-27.Q,
1 | R10 RC07G222J RESISTOR-27.K, ALLEN BRADLEY
1 | R27 MTC-4 POTENTIOMETER-1K±30%, MALLORY
1 | R8 RC07GF370J RESISTOR-39.Q, ALLEN BRADLEY
1 | R22 RC07GF100J RESISTOR-39.Q, ALLEN BRADLEY
1 | R22 RC07GF100J RESISTOR-10.Q, ALLEN BRADLEY
4 | R314 PW5 NOTE 5 RESISTOR-.1.Q ±10%, 5W, IRC
1 | R5 PW5 NOTE 5 RESISTOR-.2.2.±10%, 5W, IRC
2 | R2.A RC07GF332J RESISTOR-1K, ALLEN BRADLEY
1 | R3 RC07GF332J RESISTOR-33K, ALLEN BRADLEY
-2 | -1 | ITEM DWG OR PART NO DESCRIPTION

NOTES:

1. ALL ITEMS AS SPECIFIED IN LIST OF MATERIAL OR APPROVED EQUIVALENT.

2. UNLESS OTHER WISE SPECIFIED RESISTORS ARE ±5%, 1/4W.

3. FOR DRILLING AND DIMENSIONAL DRAWING SEE C 15471.

DE 8280

REV DESCRIPTION DATE APPD

67-243; DELETED

NOTE 4 & ALL

COMPONENTS NOT

CONNECTED TO

CARD.

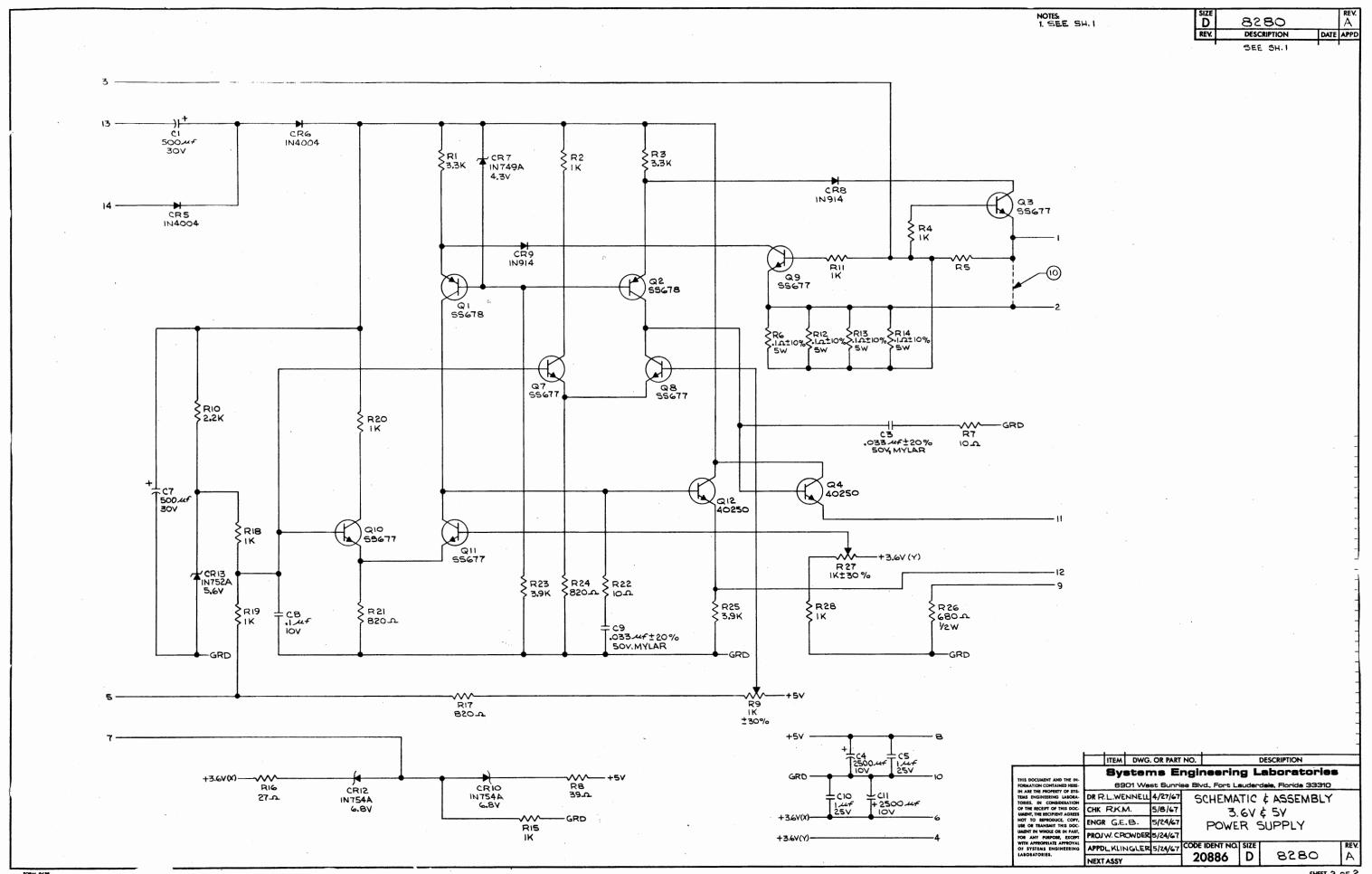
C.M. 7/117/67 RKM

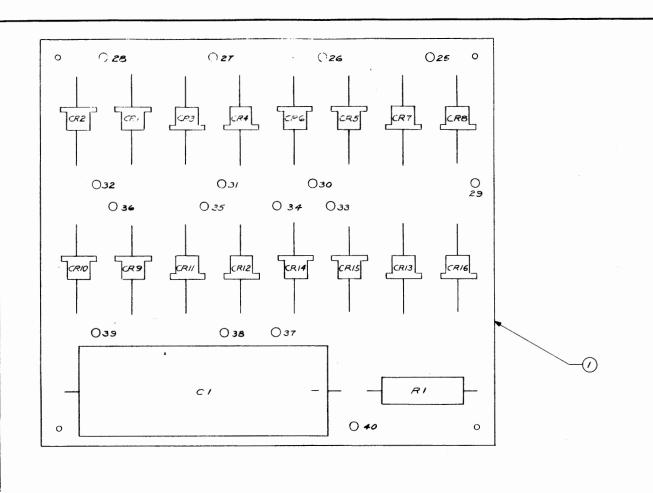
TRANSISTOR - RCA

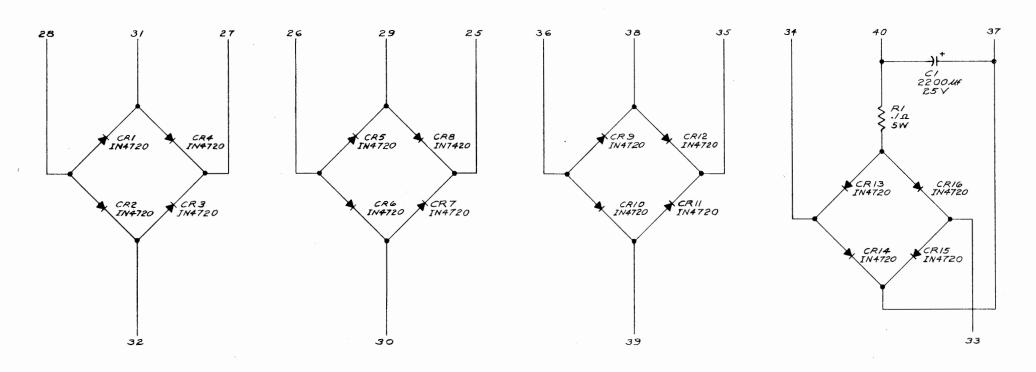
5. R5, R6, R12, R13 ¢ R14 ARE TO BE MOUNTED WITH SPACE BETWEEN COMPONENT & BOARD.

1 1 912 40250

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	3	3	<u> </u>	664	77	T 2.	ANGLET OR - MATOROLL
		3	<u>QB</u>	556		1 RA	ANSISTOR-MOTOROLA
	\bigotimes	1	Q3,7 Q2				
	1	1	Q1	556	378	TR	ANSISTOR-MOTOROLA
	1						
	1	1	CD13	10757	<u> </u>	סום	DE-5.64, ZENER, MOTOROLA
	H	<u> </u>	CNIS	114 ()			56 3.6.1 [2 3.6.1 [1.10.10.10.10.10.10.10.10.10.10.10.10.10
	1	1	CRIZ	1N75	4.4	חומי	DE-G.8V,ZENER,MOTOROLA
	X	1	CRIO	רי או	77	210	oe disvidence into to hoek
	1	1	CR9	14914	4	DIC	DDE-CLEVITE
	\sim	1	CR8 CR7	I.N74	9 ^	DIO	DE-4.3V, ZENER, MOTOROLA
	2	1	CR5.6	1140			DE-MOTOROLA
		٠	CNA	114 70		0.0	DE MOTORIOLIT
	ł						
	1	1	C8	111/45	104	CAD	04.CITAB - 1
	1	-	CO	UKIC)-104	CAP	ACITORJur.+80%-20%,10V CRL
	1	1	CIO				
	\Rightarrow	1	C5	5C1	3	CAF	PACITOR-1juf=20%,25V,5PRAGUE
	1	1	C11	29025	2(-010C)A	CAE	PACITOR-2500WF,10V, SPRAGUE
	\bowtie	1	C4	570250	36010664		
	1	1	<u>c9</u>	60	(PE	CAP	ACITOR033mf,+20%, 50V, MYLAR
	\times	1	C3				- GOODALL
	2	2	C17	390507	G030FL4	CAP	ACITOR-500mf,30V, SPRAGUE
	-	-	34.				
	L						
	.1	\geq	12				MPER
	\geq	L!	11	$\vdash =$			MPER
	1	₩	10	BIG	3(9		MPER IPAD
	4 2	8	8	810	369		T- # 4-40 ESNA
	4	8	7				SHER-#4, SHAKE PROOF, INT. TOOT
	2	4	6				REW- #4-40X7/16,RD HD, S/S
	3	3	5	178			RMINAL- CTC
	AR	AR	4		G 24		EEVING-TEFLON, THINWAL
	AR	AR	3		G 24		RE-SOLID COPPER, TINNED
	AR	AR	2		૯ ૧૦ . 9 83		RE-SOLID COPPER, TINNED INTED CIRCUIT (REV SEE A51140
	-2	1-1	ITEM	DWG. O	R PART NO.	150	DESCRIPTION
		<u> </u>				$\overline{}$	SYSTEMS ENGINEERING
		-				L	FORT LAUDERDALE, FLORIDA
		DRR	J.WE	NNELL	5/2/67		
		cx.	RK		5-8-67	·SC	CHEMATIC & ASSEMBLY
		BNG.			5/247		. 3.GV & 5V
		PROJ.	tolle		5/24/67		POWER SUPPLY
CODE IDE		AT.	Ply		5/24/67	3422	9390
508	988	USED C	N	1	-	D	0858
							SHEET 1 OF ?



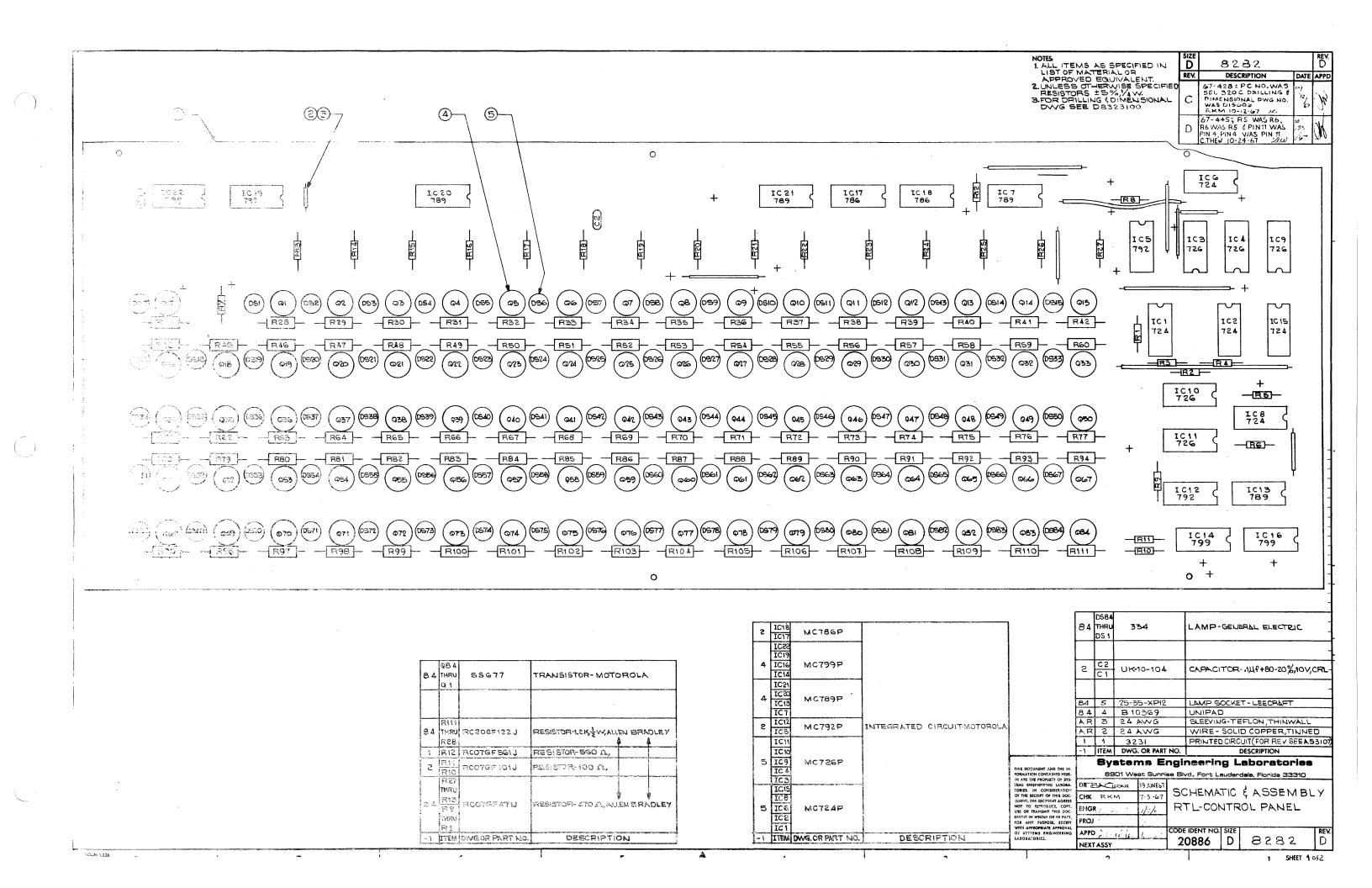


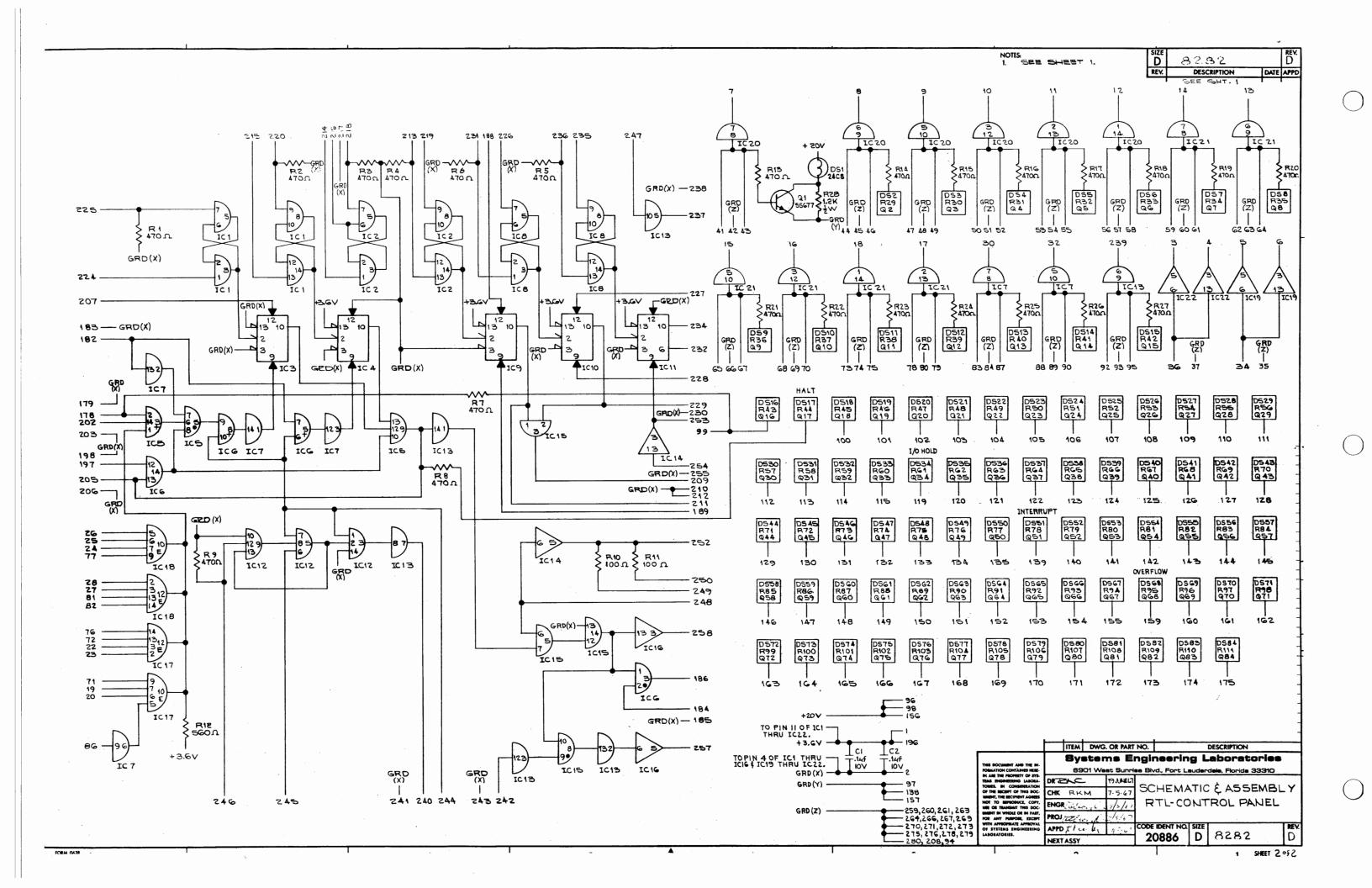


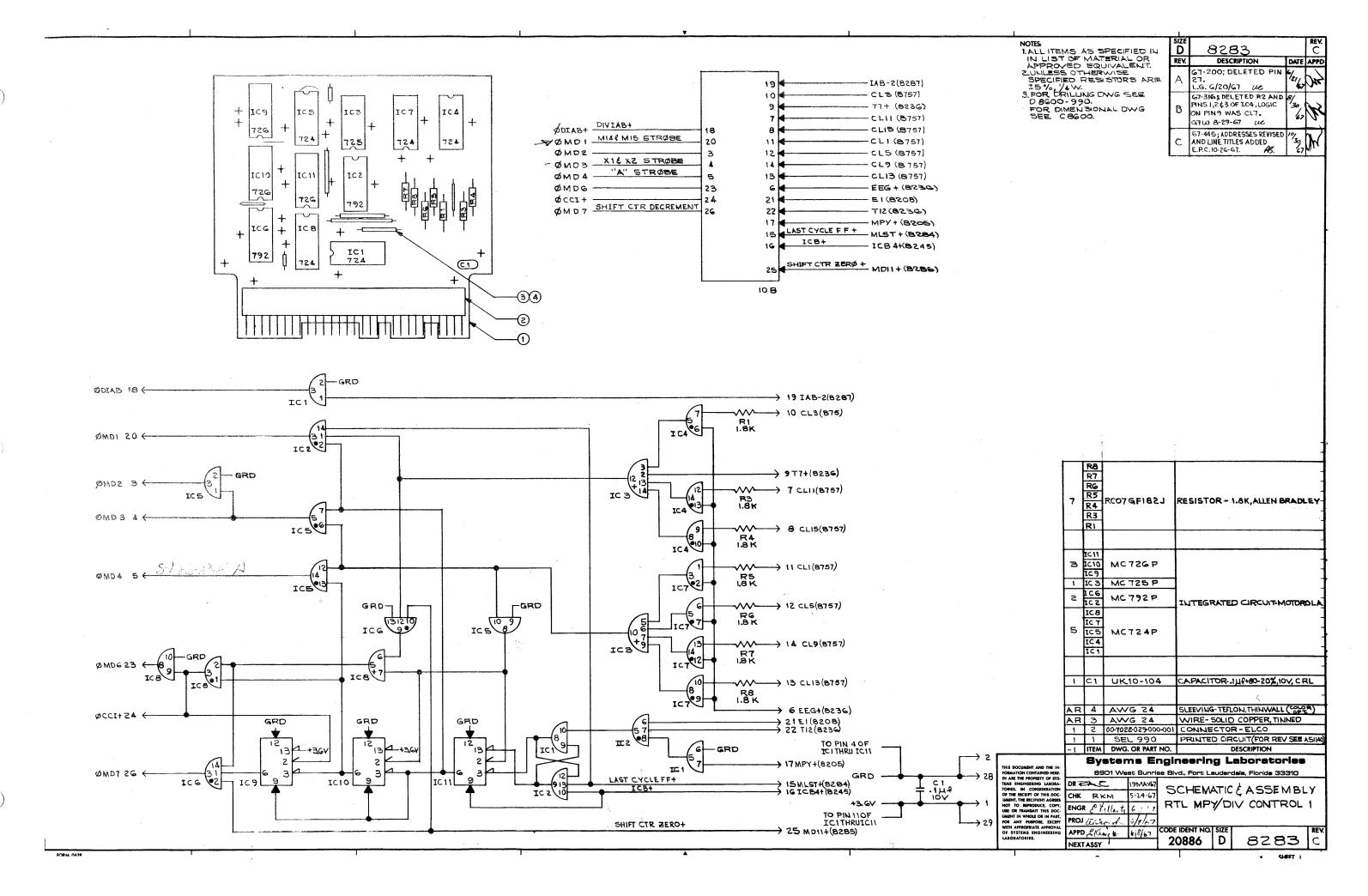
NOTES:

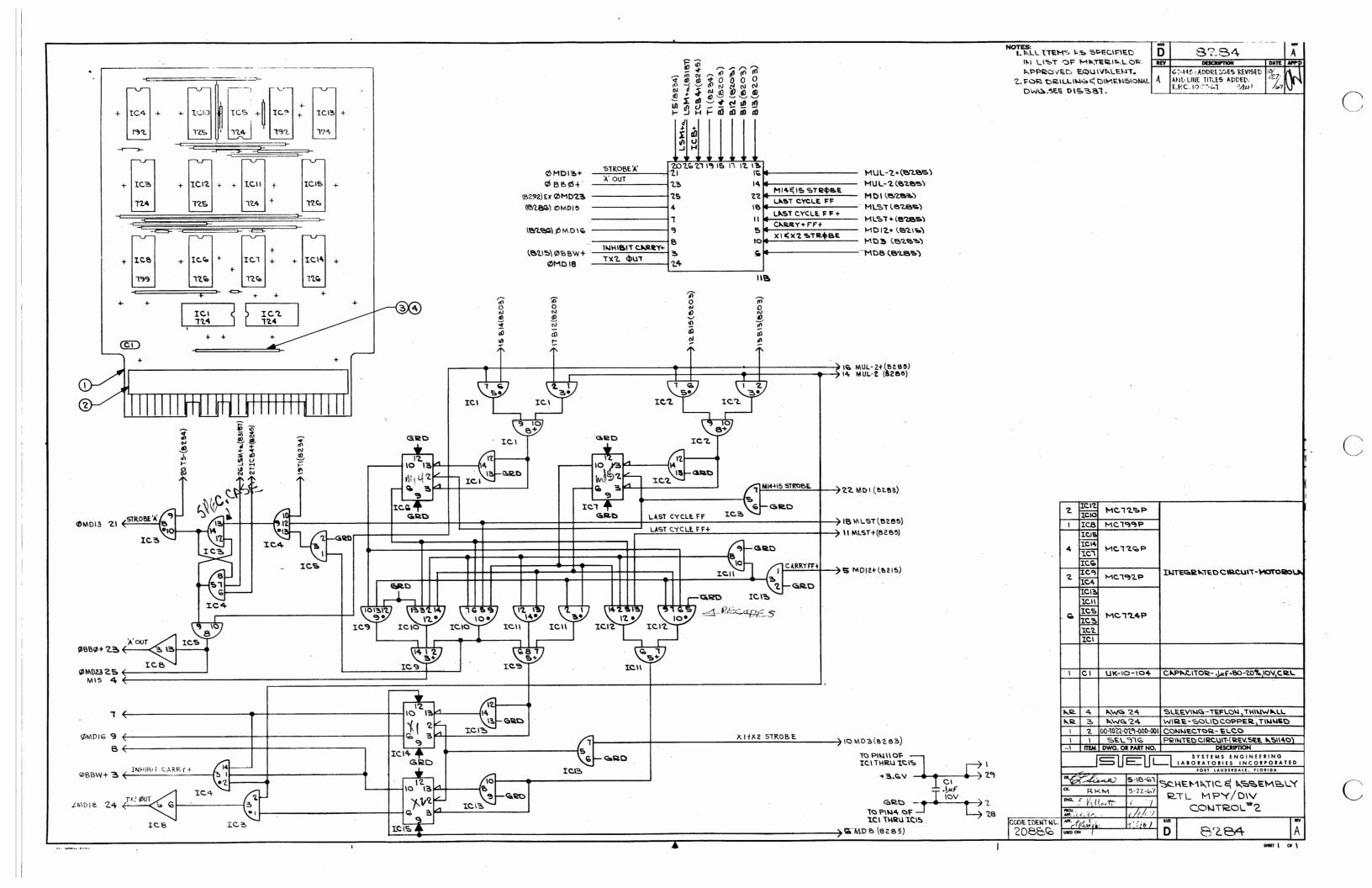
1. ALL ITEMS AS SPECIFIED IN LIST OF MATERIAL OR APPROVED EQUIVALENT
2. UNLESS OTHERWISE
3. SPECIFIED RESISTORS
ARE ± 5%, 1/4 W
3. FOR DRILLING AND DIMENSIONAL DWG. SEE
C 154 74
4. FOR COMPLETE SCHEMATIC OF POWER SUPPLY MODEL
222 SEE D53/24 8281 DESCRIPTION

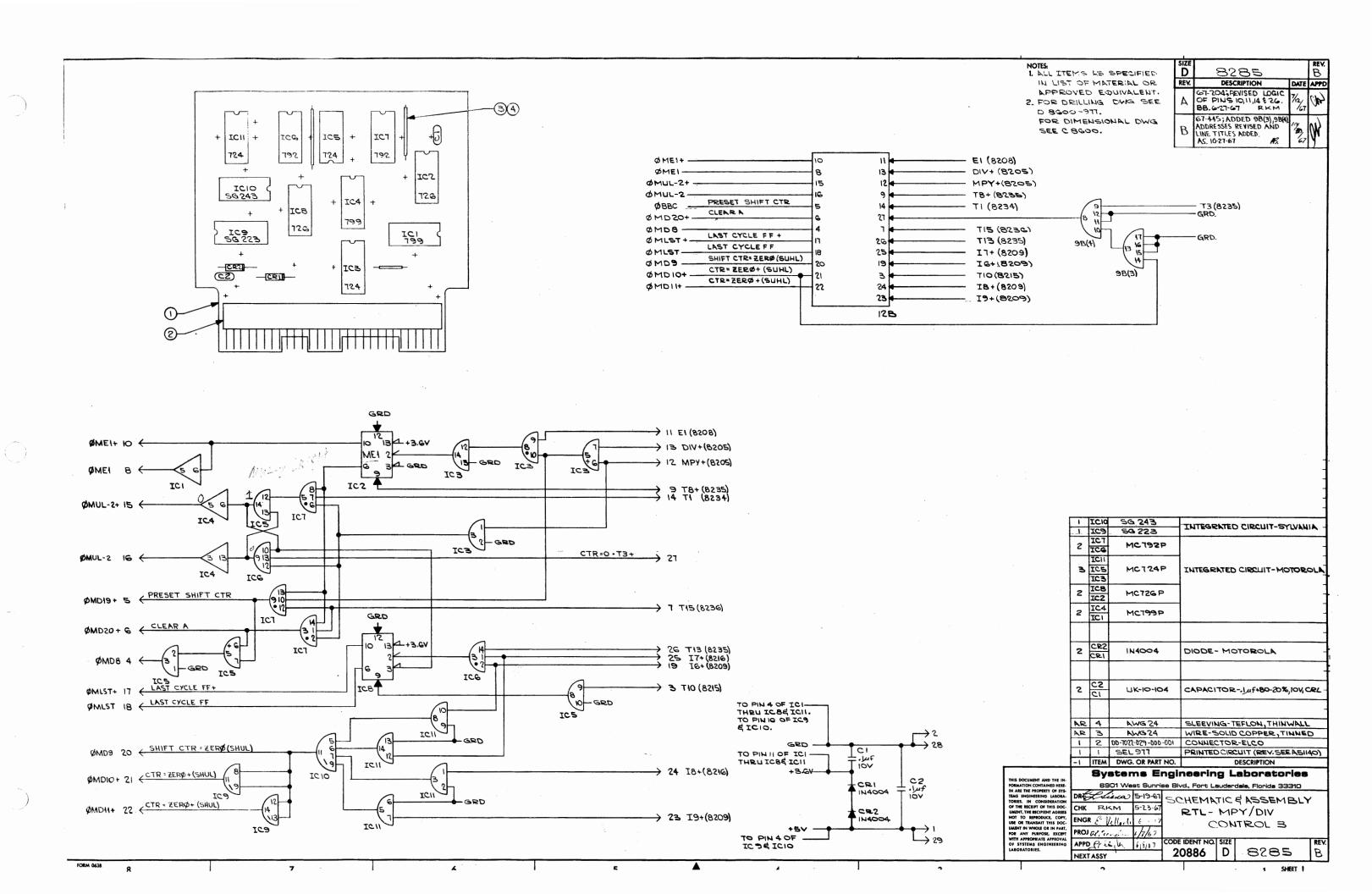
	/	RI	PV	N5	RES	SISTORID, 5W, WW, IR	=]
	L				1		\Box
	16	CRI6 THRU	IN4	720	DIC	ODE- IRC	
	<u> </u>	CRI	-		-		
	-	C1	39022	:86025HR	CAP	PACITOR-220DMJ.25V, SPRAGE	IE
	<u> </u>	1			1		
		1	6.51	0.0.4	I may	INTED CIRCUIT (REV. SEE A 51140	77
		1/		984	PAI		2
	-1			R PART NO.	PAI	DESCRIPTION	2
	-1				丰	DESCRIPTION SYSTEMS ENGINEERING	
	=1				丰	DESCRIPTION	
	-1	ITEM		R PART NO.		DESCRIPTION SYSTEMS ENGINEERING LABORATORIES INCORPORATE FORT LAUDERDALE, FLORIDA	D
	-1	ITEM	DWG. O	# PART NO.		DESCRIPTION SYSTEMS ENGINEERING LABORATORIES INCORPORATE FORT LAUDERDALE, FLORIDA CHEMATICÉ ASSEMBLE	D
	DR. M	RICH	IARDS	# PART NO. 4-4-67 4-5-67		DESCRIPTION SYSTEMS ENGINEERING LABORATORIES INCORPORATE FORT LAUDERDALE, FLORIDA	D
	DR. M CK. BNG.	RICH	IARDS	4-4-67 4-5-67		DESCRIPTION SYSTEMS ENGINEERING LABORATORIES INCORPORATE FORT LAUDERDALE, FLORIDA CHEMATICÉ ASSEMBL MEMORY	D
CODE IDENT NO	DR. M CK. BNG.	RICH	IARDS	# PART NO. 4-4-67 4-5-67	50	SYSTEMS ENGINEERING LABORATORIES INCORPORATE FOIT LAUDISPAIL, FLOTIDA CHEMATICÉ ASSEMBL MEMORY RECTIFIERS	D LY
CODE IDENT NO	DR. M CK. BNG.	RICH RK WELL WELL ST.C.	IARDS	4-4-67 4-5-67		SYSTEMS ENGINEERING LABORATORIES INCORPORATE FOIT LAUDISPAIL, FLOTIDA CHEMATICÉ ASSEMBL MEMORY RECTIFIERS	D

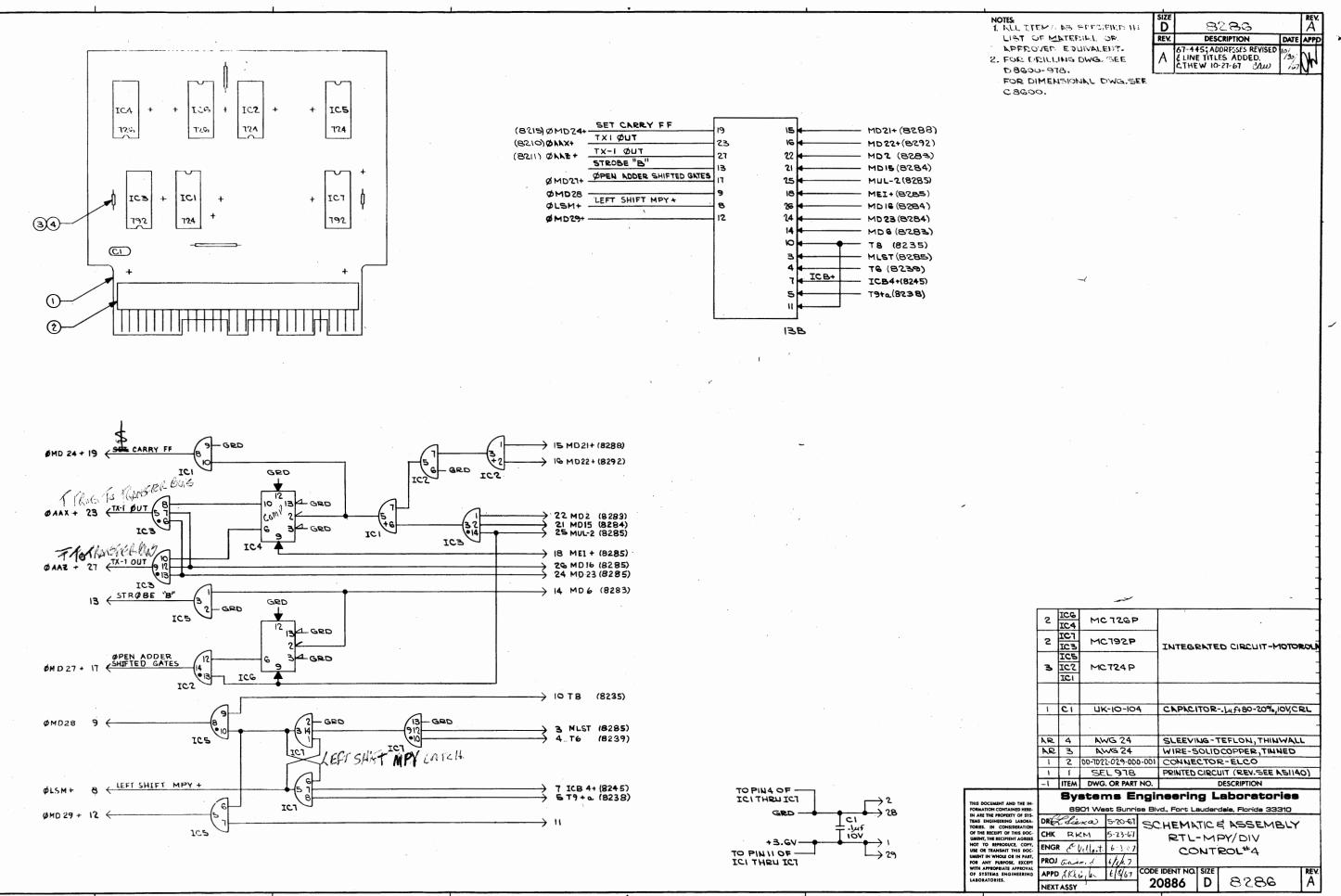




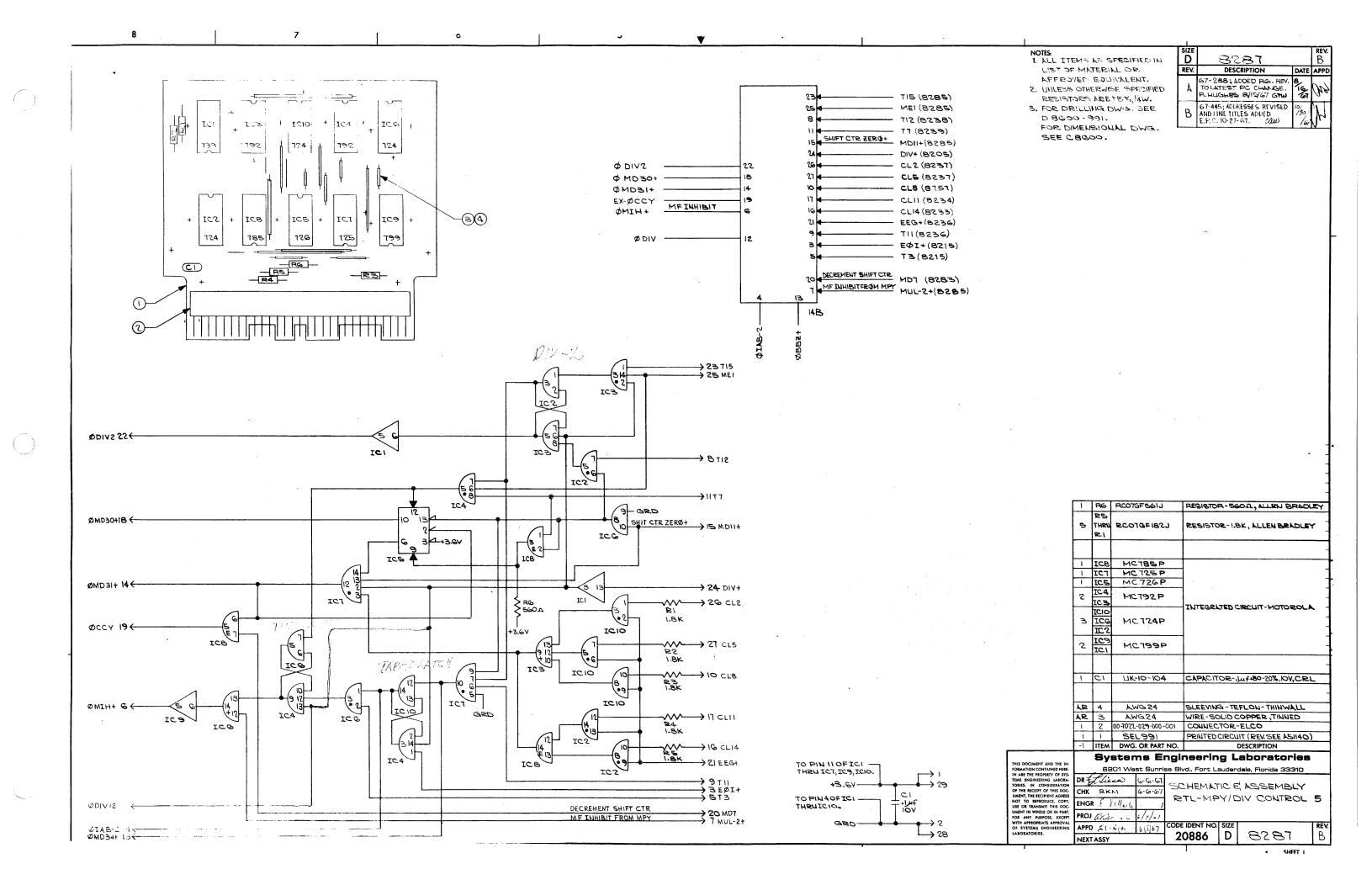


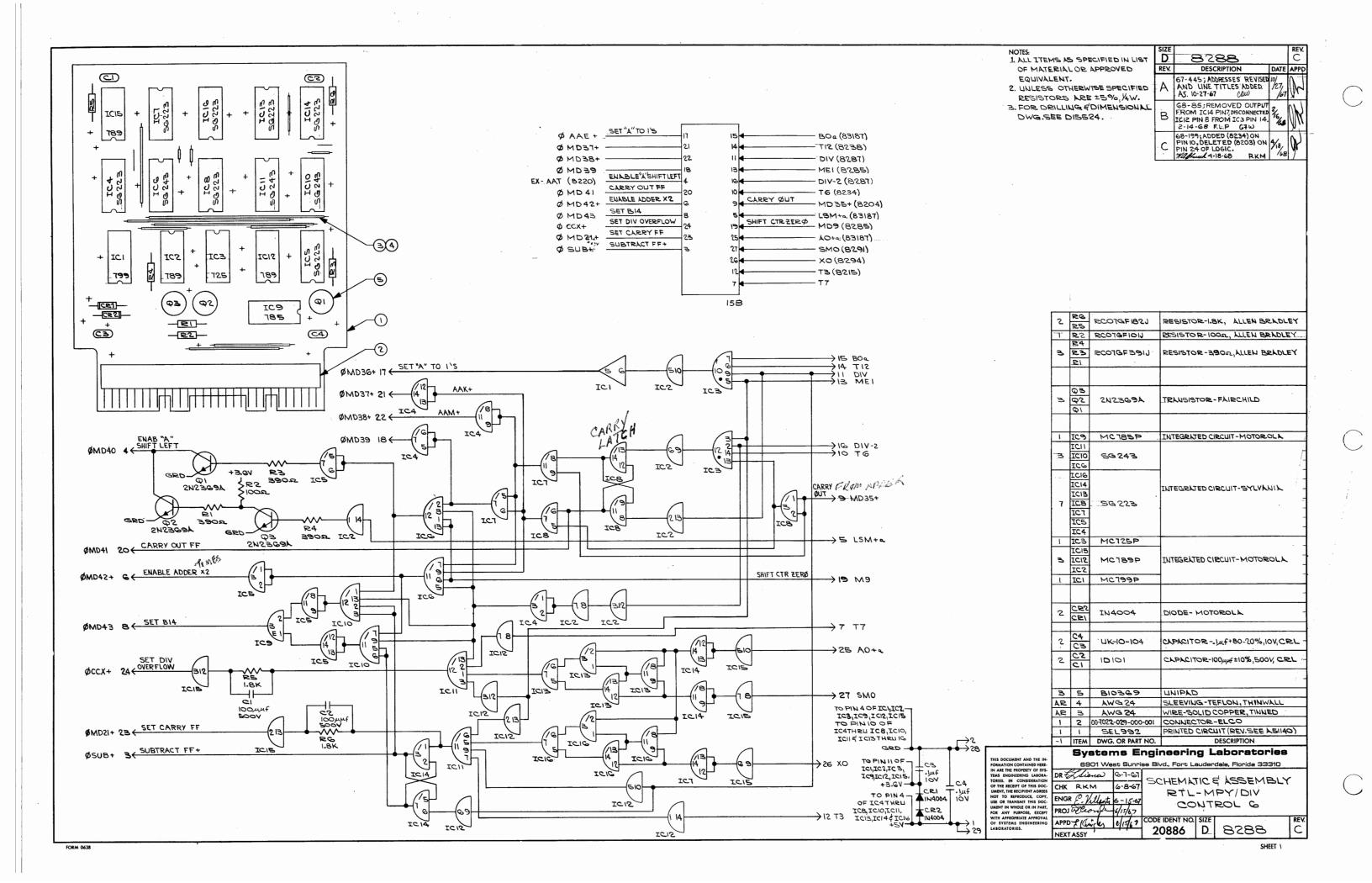


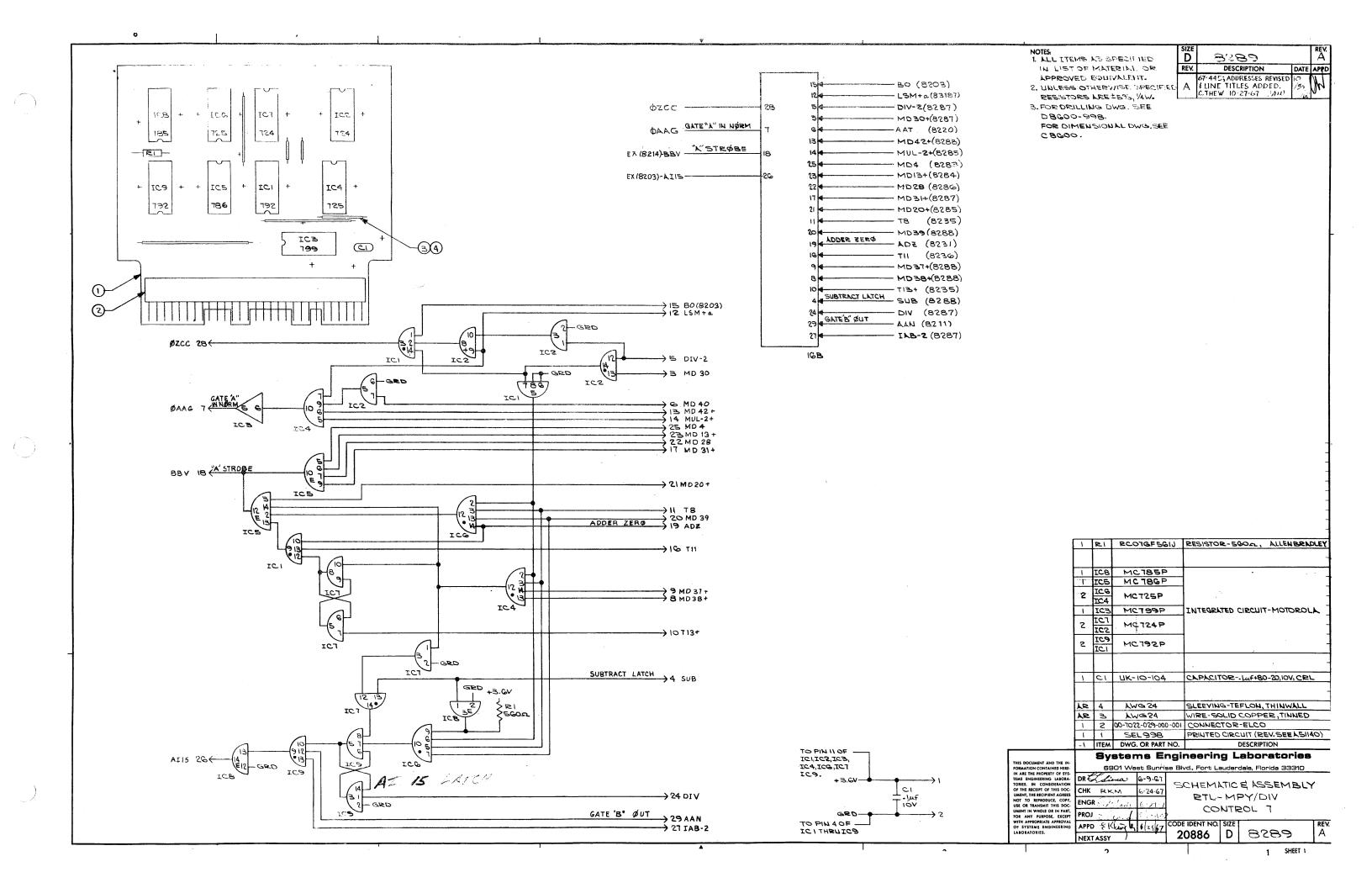


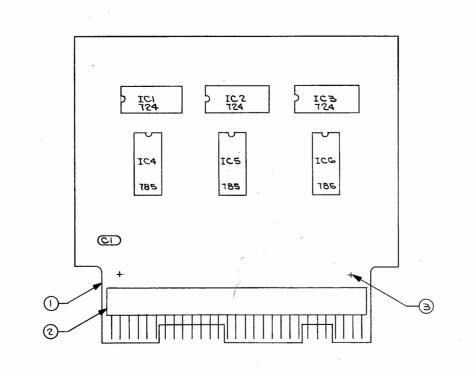


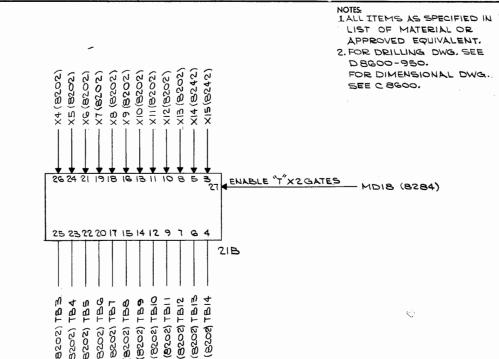
SHEET (

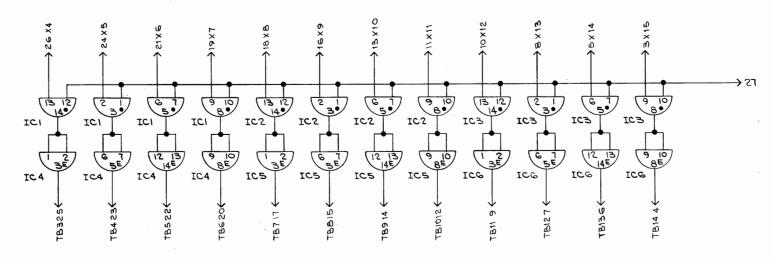












TO PIN 4 OF ICI THRU ICO	28
+3.6V — TO PIN 11 OF ICI, ICZ & IC3	I cl I luf I lov 29

3	ICS ICA	MC785P	
3	ICI ICS	MC724P	INTEGRATED CIRCUIT - MOTOROLA
			-
ı	CI	UK-10-104	CAPACITOR-Juf+80-20%, IOV, CRL
AR	3	AWG24	WIRE-SOLID COPPER, TINNED
I	2	00-7022-029-000-001	CONNECTOR - ELCO
. 1	. 1	SEL 950	PRINTED CIRCUIT (REV. SEE A51140)
-1	ITEM	DWG. OR PART NO.	DESCRIPTION

8290 DESCRIPTION

A + ADDED. E.P.C. 10-27-67.

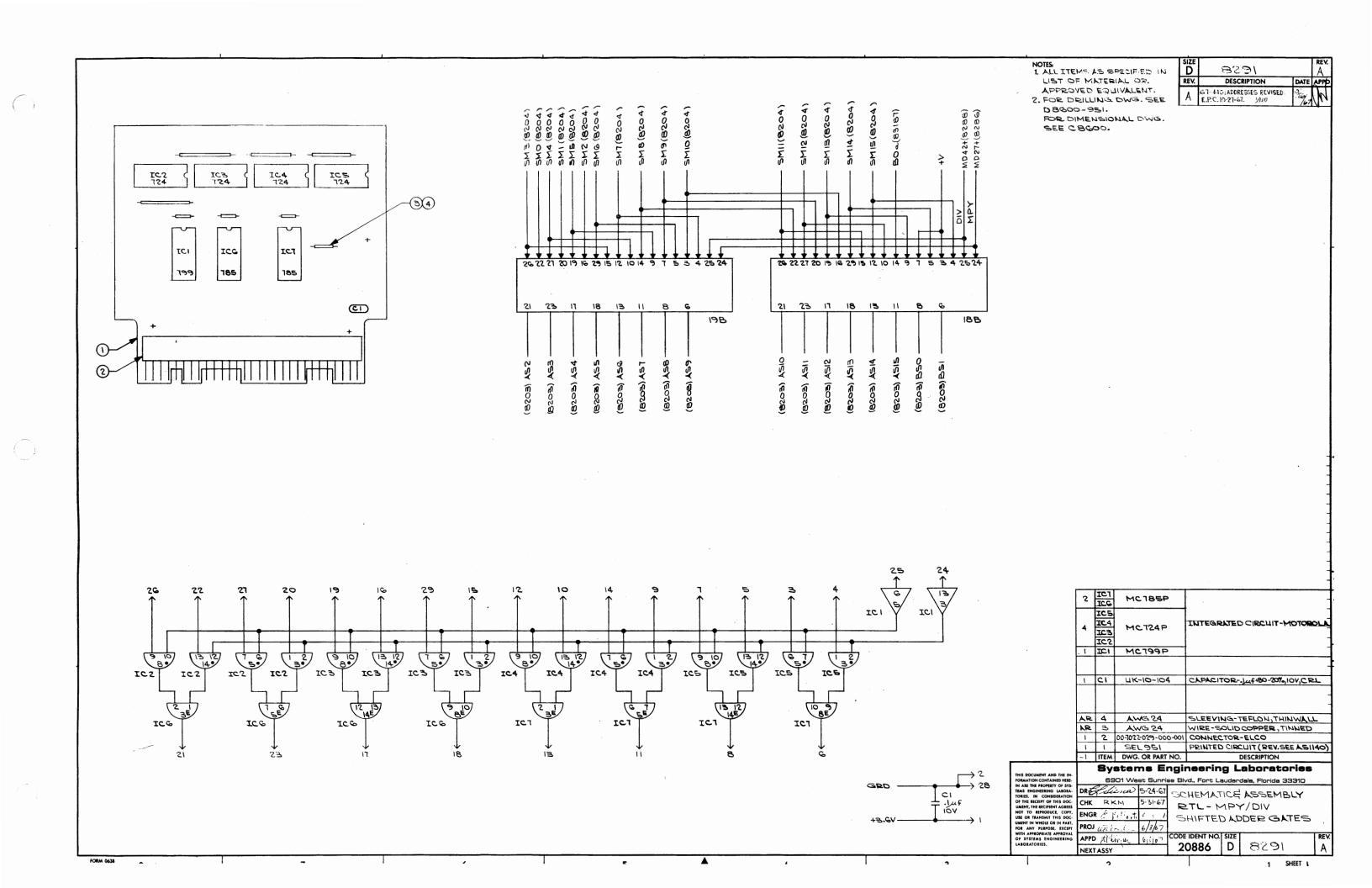
67-445; ADDRESSES REVISED 10/34/25/26 P.P.C. 10-27-67. CAW 1/37

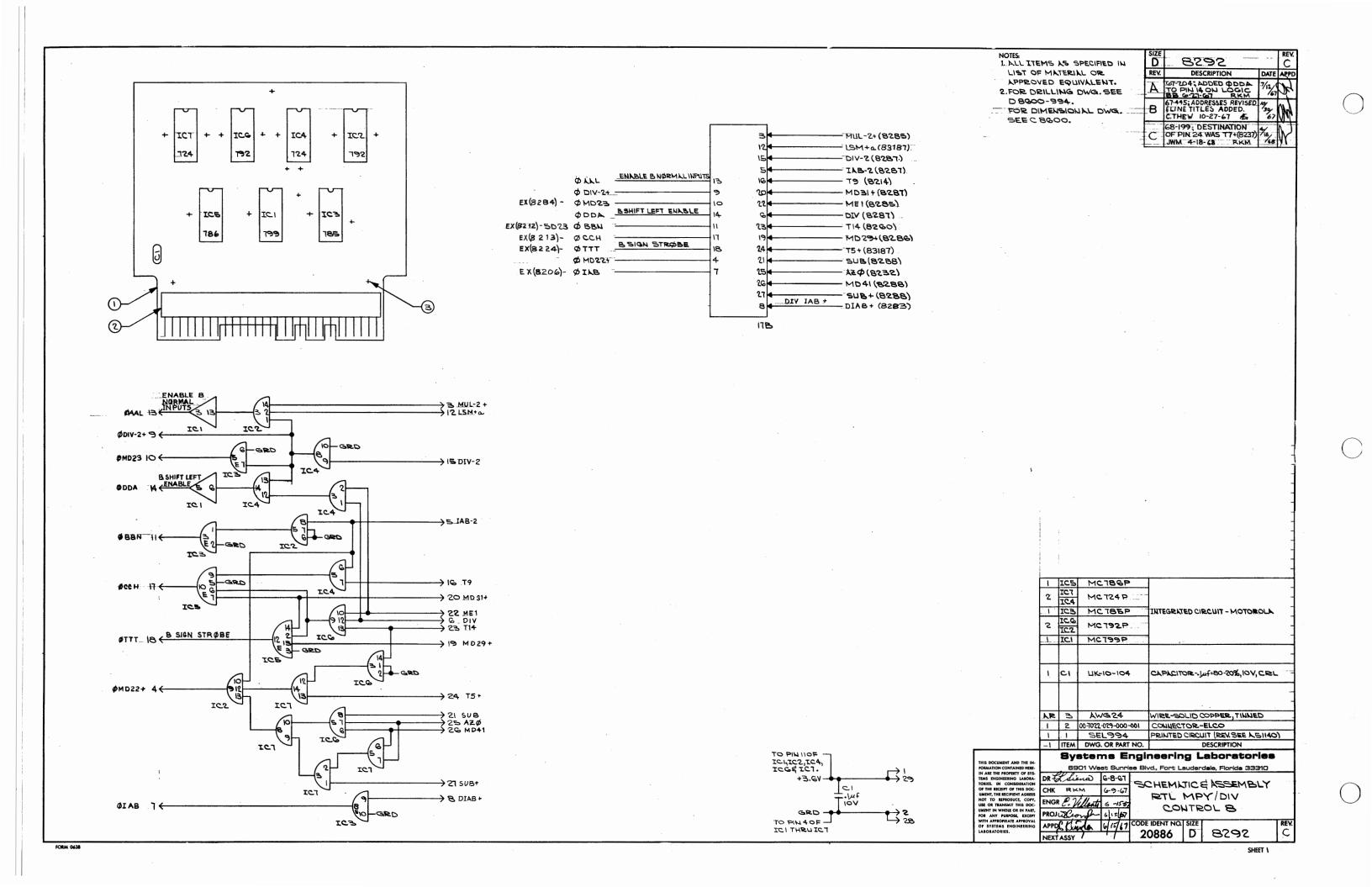
B 68-199; (8202) ON PIN 15 OF LOGIC WAS (8203) JWM 4-18-68 RKM

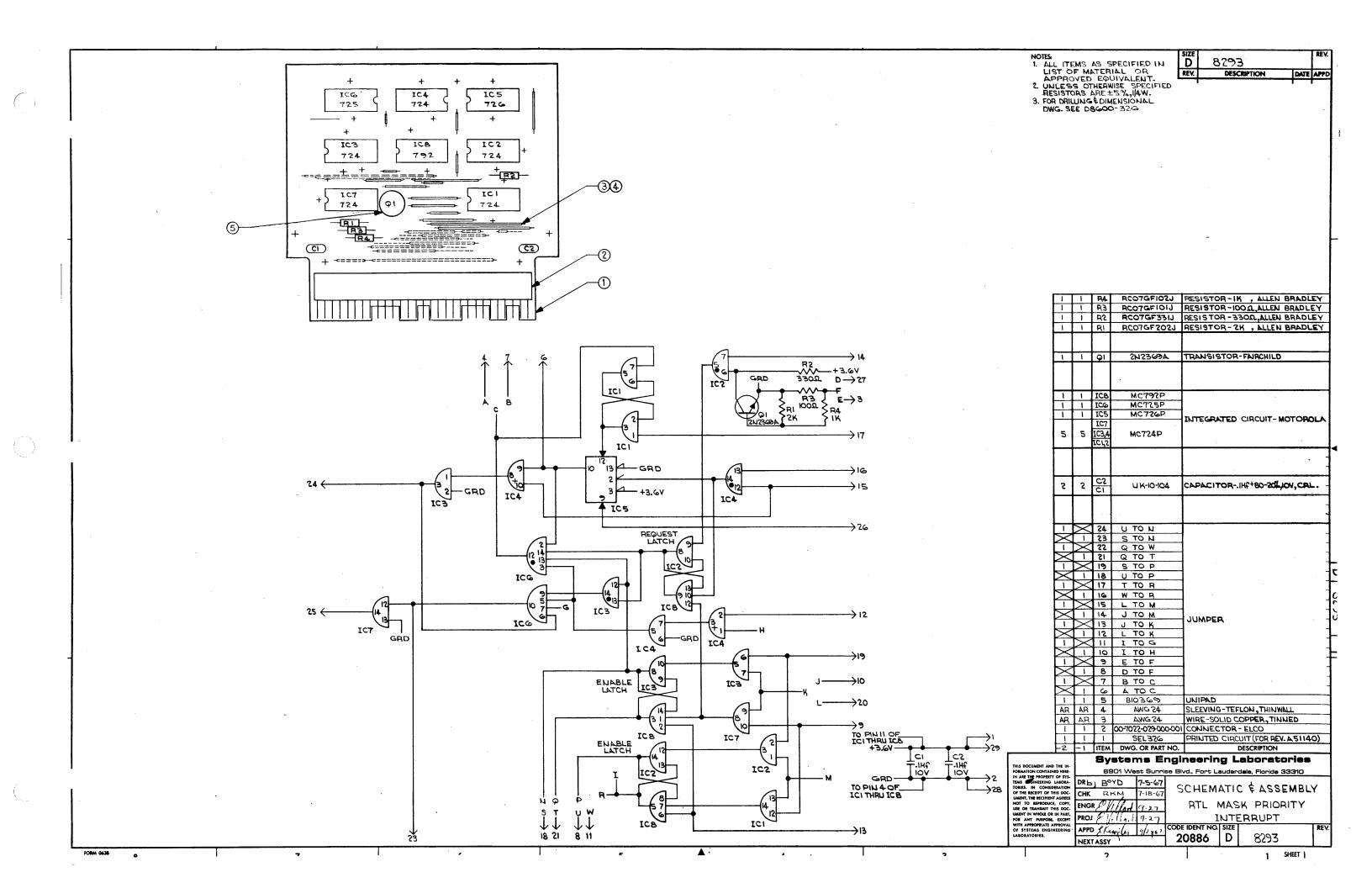
DATE APPD

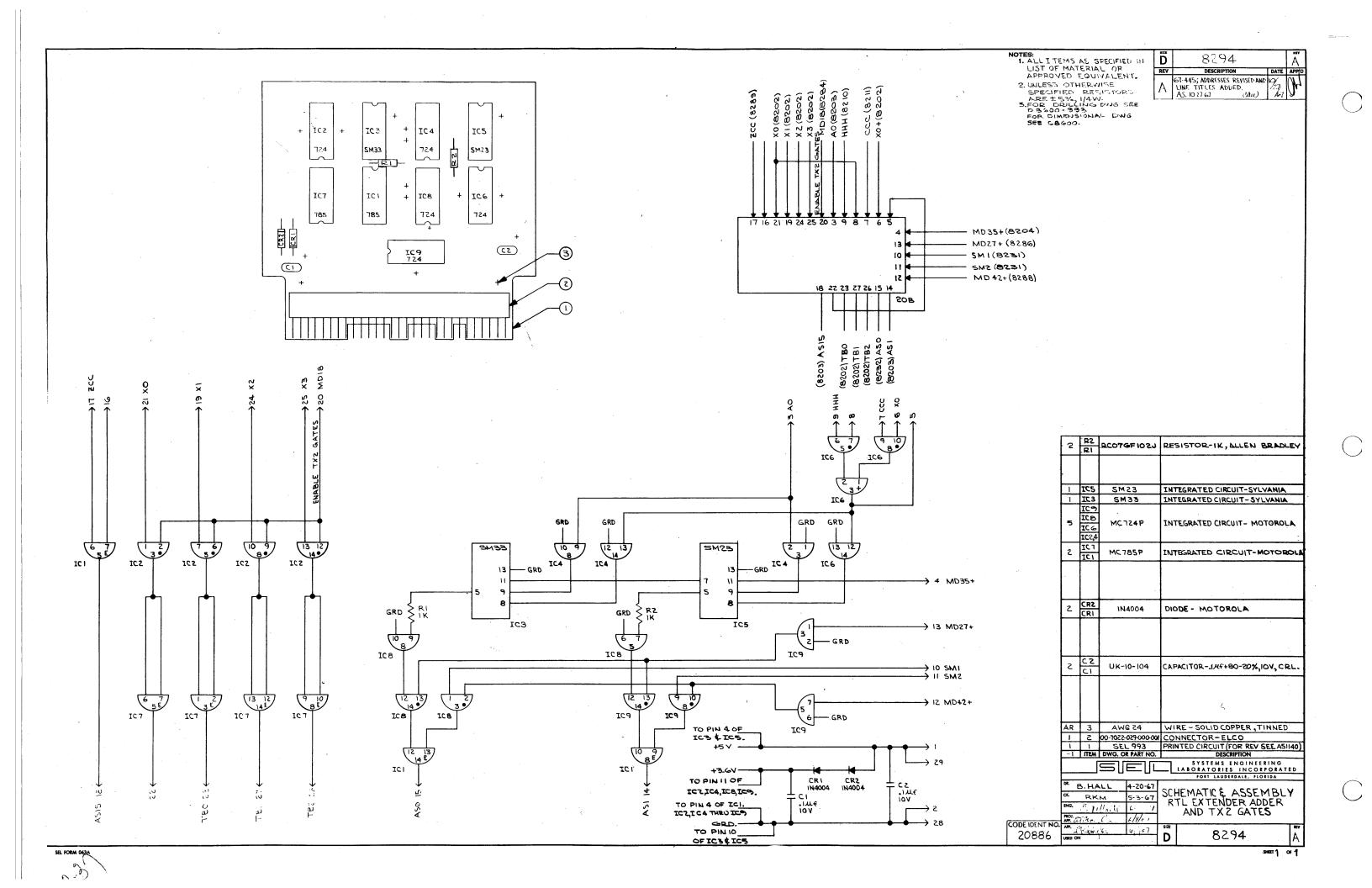
Systems Engineering Laboratories 6901 West Sunrise Blvd., Fort Lauderdale, Florida 33310 DR & Siena 5-24-67 SCHEMATICE ASSEMBLY

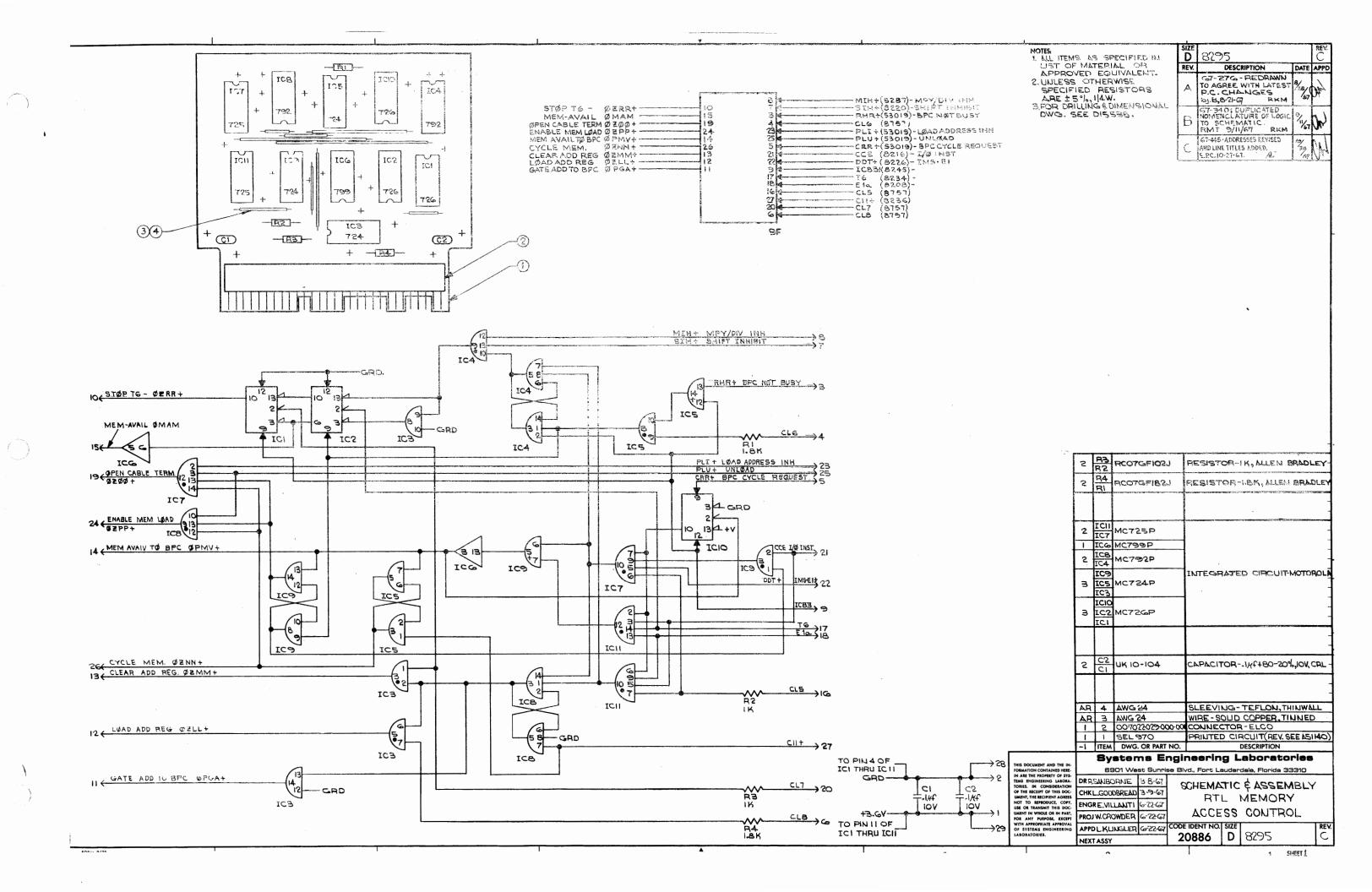
CHK RKM 6-1-67 RTL-MPY/DIV "T" XZ GATES ENGR & Villarti 6-8-67 PROJ accord 6/8/67 CODE IDENT NO. SIZE APPD SKKinh 6/8/67 20886 D 8290 В NEXT ASSY

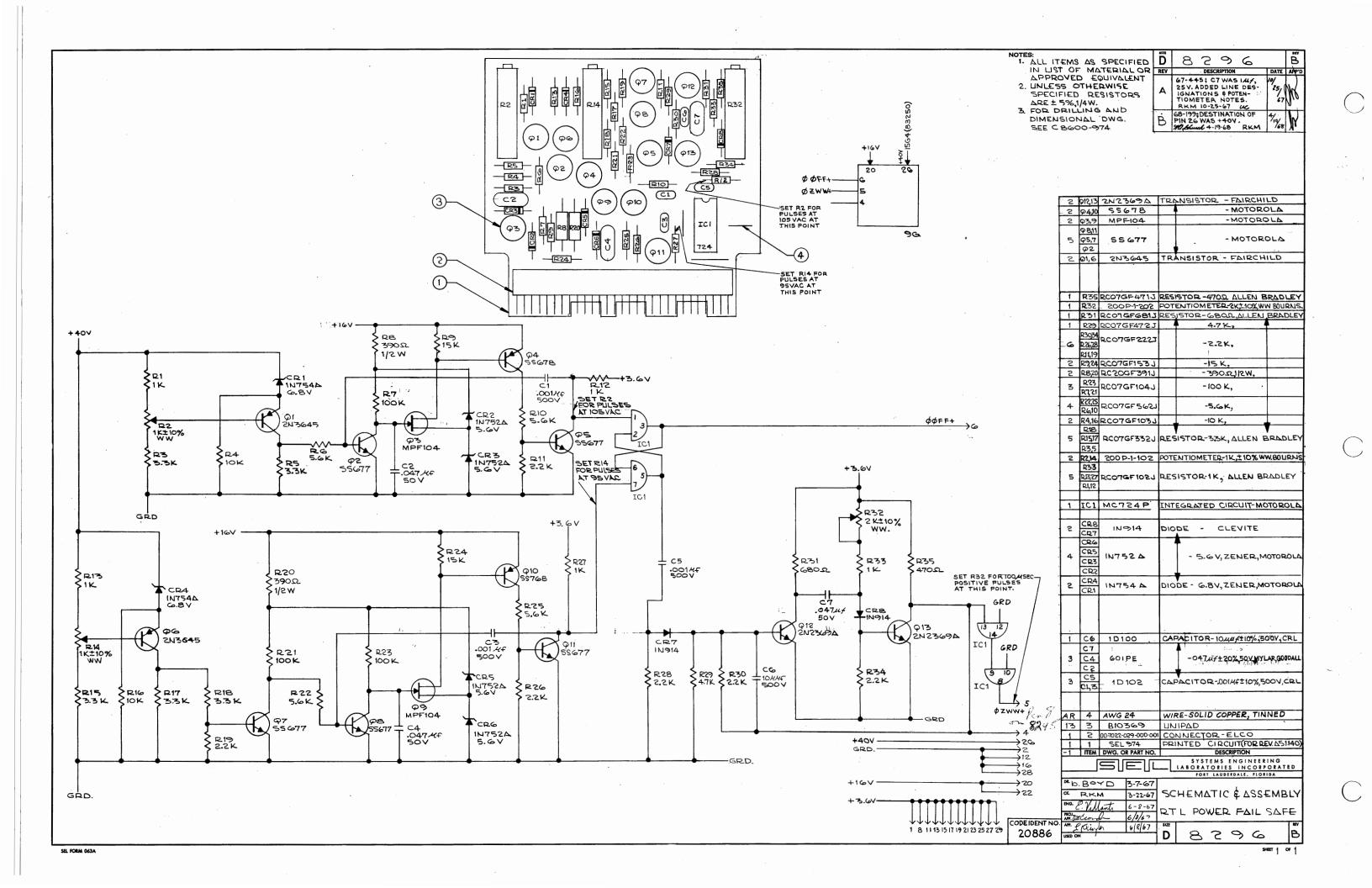


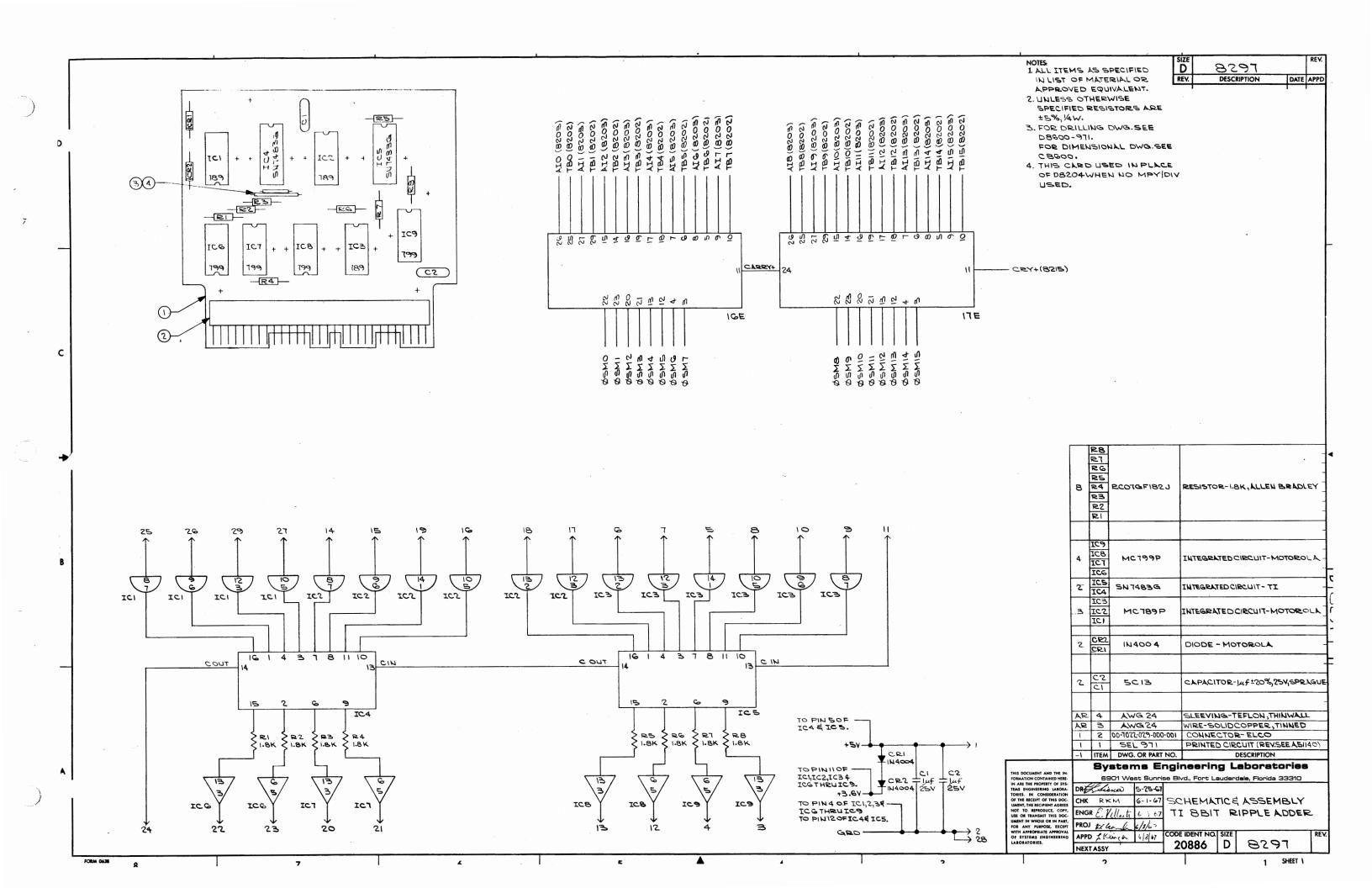


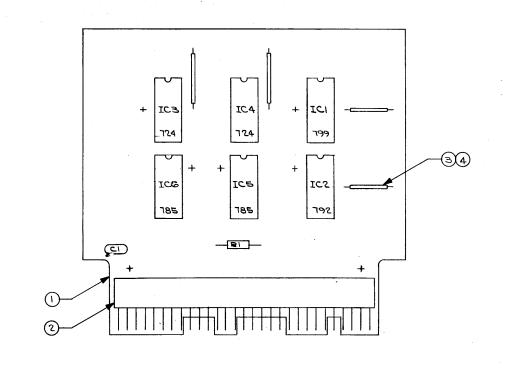


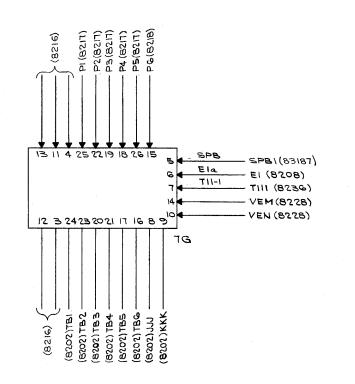












notes: 1. All Items as specified in	SIZE D	8298	-	REV. B	
LIST OF MATERIAL OR	REV.	DESCRIPTION	DATE	APPI	5
APPROVED EQUIVALENT. 2. UNLESS OTHERWISE SPECID. RESISTORS ARE ±5%, M.W.	Α	67-445; ADDRESSES REVISED AND LINE TITLES ADDED. AS. 10-27-67 CLW	18/1	W	
3. FOR DRILLING DWG.SEE D8G00-981. FOR DIMENSIONAL DWG.SEE	B	68-199; PINS 3,11,12, \$13 WERE SPARES. JWM 4-18-68 RKM	4/18/68	M	
C 8600.					

13 11 (313) ICI GATE PR TO ADDER BITS 2-6 VEM 14
GATE PR TO VEN 10
ADDER BIT 1 12 13 (e) IC4 IC4 IC3 IC 5 IC5 ICe, IC@ IC & ICE ICE) \$560r +3.67 23 50 21

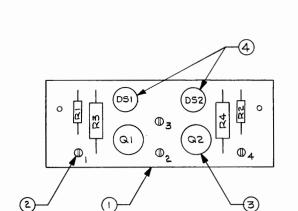
TO PIUII OF ICI THRUIC4 +3.GV	29
	- Juf 10V
TO PIN4 OF	2 >28

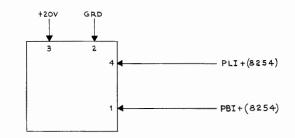
_1	RI RCOTGF5GIJ		RESISTOR-5GOD, ALLENBRADLEY
			-
			-
2	IC6 IC5	MC785P	
5	IC4	MC 724P	INTEGRATED CIRCUIT-MOTOROLA
1	ICS	ASELDW	1
1	ICI	ACT99P	1
			,
1	CI	UK-10-104	CAPACITOR-Juf+80-20%,10V, CRL
AR	4	AWG.24	SLEEVING-TEFLON, THINWALL
AR	E	AWG.24	WIRE-SOLID COPPER, TINNED
1	2	100-000-650-5501-00	CONNECTOR-ELCO
١	1	SEL 981	PRINTEDCIRCUIT (REV.SEE ASII40)
-1	ITEM	DWG, OR PART NO.	DESCRIPTION

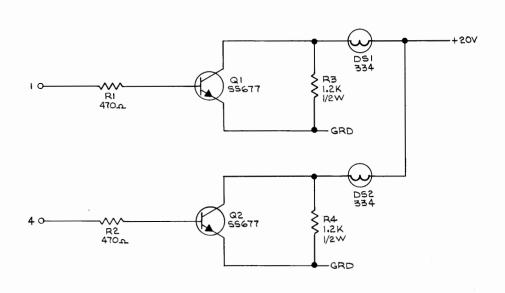
NEXT ASSY

6901 West Sunrise Blvd., Fort Lauderdale, Florida 33310 DRESiena 5-25-67 CHK RKM 6-1-67 SCHEMATICE ASSEMBLY

ENGR & Villat 6-9-67 PROJUKINA 6/8/67 RTL-SPB MAP STORE GATES 20886 D APPD Phone 6:3107 8538







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FORMATION
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TEMS ENGINE
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OF THE RECE
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2	R4 R3	RC20GF122J	RESISTOR-1.2K, 1/2W, ALLEN BRADLEY-
2	R2	RCO7GF47IJ	RESISTOR-4701, ALLEN BRADLEY
			-
2	Q2 Q1	55677	TRANSISTOR-MOTOROLA :
			-
2	DS2 DS1	334	LAMP-GENERAL ELECTRIC
			- -
			-
2	4	25-35-XPII	LAMP SOCKET-LEECRAFT
2	3	B10369	UNIPAD
4	2	1785-2	TERMINAL -CTC
1	1	SEL 329	PRINTED CIRCUIT (SEE REV. A51140)
-1	ITEM	DWG. OR PART NO.	DESCRIPTION

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OF SYSTEMS ENGINEERING
LABORATORIES.

Systems Engineering Laboratories
6901 West Sunrise Blvd., Fort Lauderdale, Florida 33310

DR C.E.M. 8/3/67 SCHEMATIC & ASSEMBLY

	DR C.E.M.	8/3/67	٥
	CHK RKM	8-4-67	\
:	ENGR & Villanti	8-15-67	
ŕ	PROJ Danie	8/15/67	
;	APPD COLLEGE	6/15/67	CODE
	NEXT ASSY		2

NOTES:

1. ALL ITEMS AS SPECIFIED
IN LIST OF MATERIAL OR
APPROVED EQUIVALENT.
2. UNLESS OTHERWISE
SPECIFIED RESISTORS
ARE ±5%, 1/4W.
3. FOR DRILLING AND
DIMENSIONAL DWG.
SEE BIGIOS

SIZE D

REV.

В

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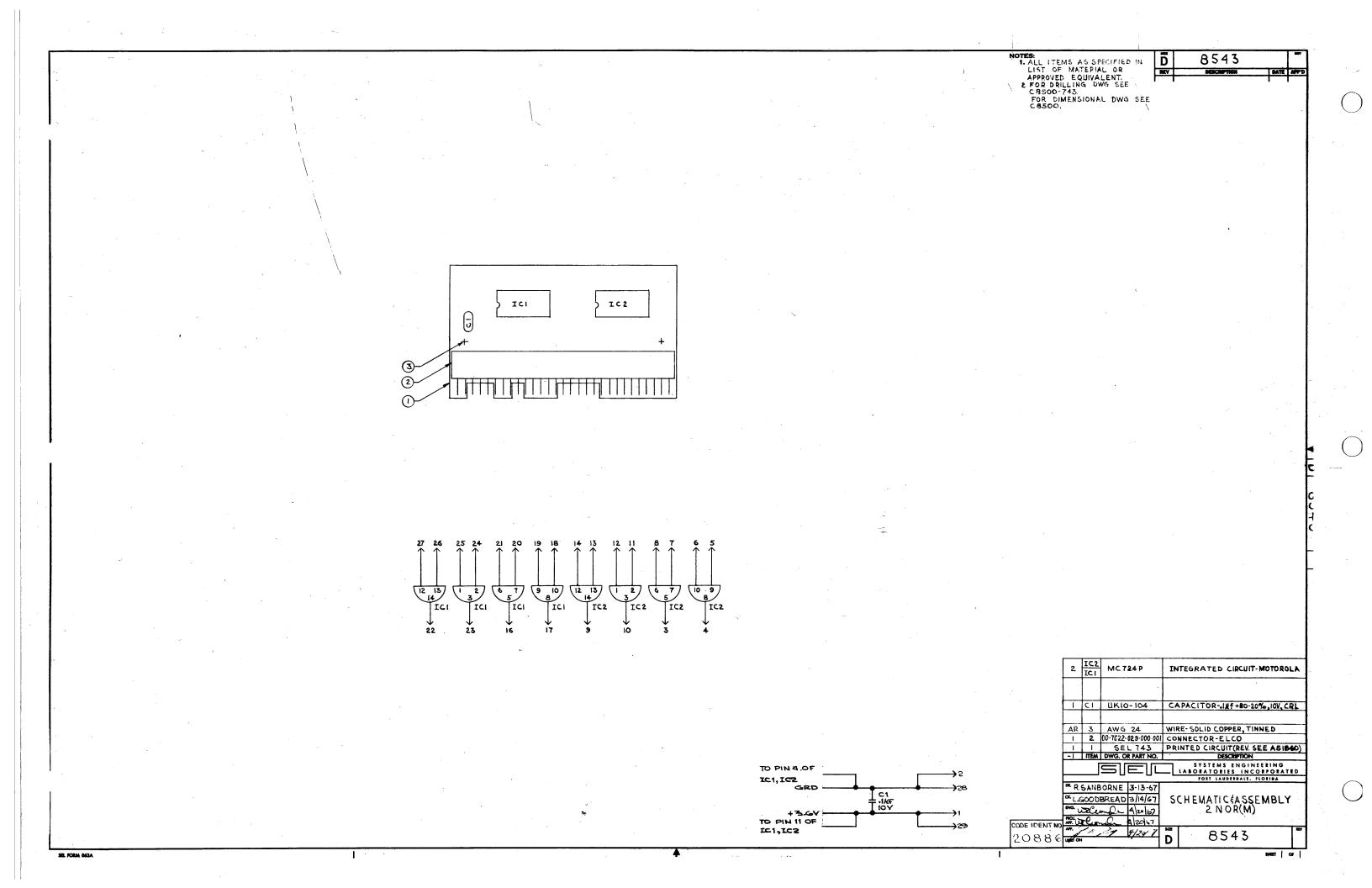
DESCRIPTION

67-391; D & D DWG NO. 10/ IN NOTE 3 WAS 815874 20/ RKM 10-18-67 UC

67-445; ADDED LOGIC. 10/25/W RKM 10-25-67 LLC 68-83; DELETED PI FROM LOGIC 1/4/268 RKM

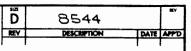
DATE APPD

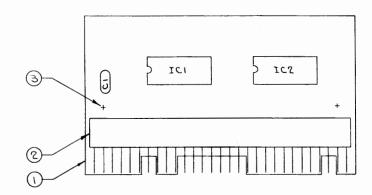
SCHEMATIC & ASSEMBLY PROGRAM PROTECT LAMP DRIVERS

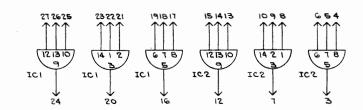


NOTES.

1. ALL ITEMS AS SPECIFIED IN D. LIST OF MATERIALS OR REV. APPROVED EQUIVALENT. 2. FOR DRILLING DWG. SEE C8500-744. FOR DIMENSIONAL DWG. SEE C8500.

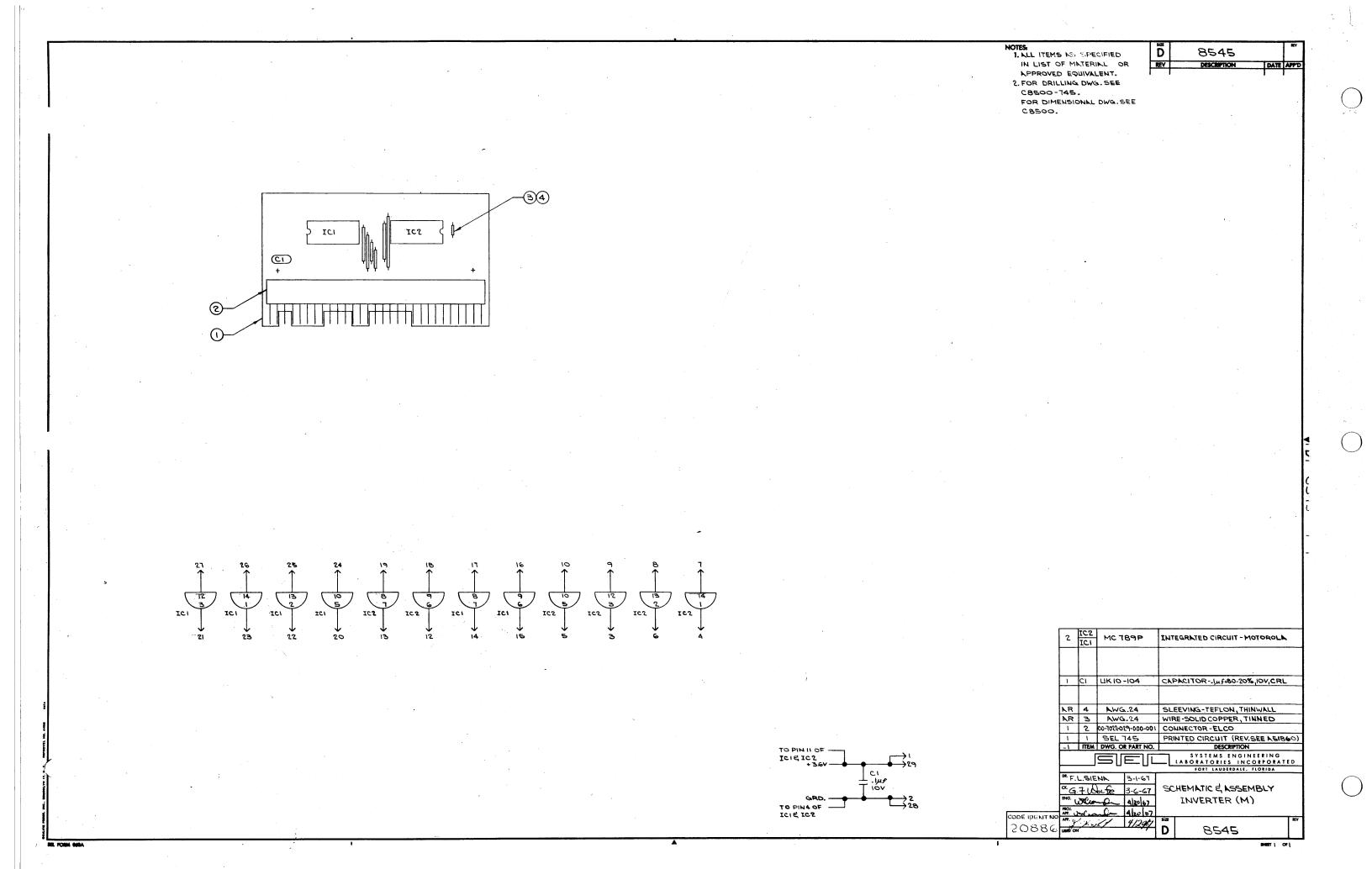






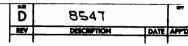
_							
	ر ر	ICI	MC 7	92P	INI	EGRATED CIRCUIT-MOTOROL	۸.
	Ī	CI	NK-10	-104	CN	PACITORJuf+80-20%, 104, CRL	
	AR	3	AWG	24	WIF	E-SOLID COPPER, TINNED	
	1	2	00-7022-0	29-000-001	CO	NECTOR-ELCO	
	1	١	SEL 7	44	PRI	NTED CIRCUIT (REV. SEE AS1860	2)
	-1	ITEM	DWG. O	R PART NO.		DESCRIPTION	
				EIL		SYSTEMS ENGINEERING LABORATORIES INCORPORATE	D
				_		FORT LAUDERDALE, FLORIDA	
	DR F.1	_SIE	NA	7-1-E			
	CK.L.(GOOD	BREAD	3/20/67	SC	HEMATIC & ASSEMBLY	
		DC 10	nolin	4/20/67		3 NOR (M)	
CODE IDENT NO.		S C	-le	4/20/67			
50886	USES OF	/2	17	4/20/1	D	8544	REV
						SHEET \ OF	· _

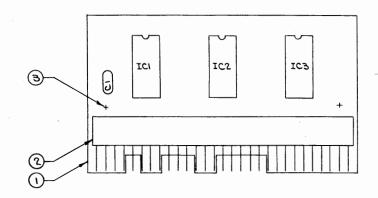
TO PINII OF	CI -!uF
TO PIN4 OF	$\xrightarrow{5}$

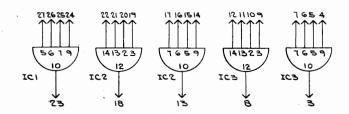


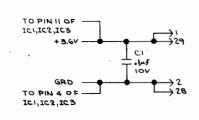
NOTES:

1. ALL ITEMS AS SPECIFIED IN
LIST OF MATERIAL OR APPROVED EQUIVALENT. 2. FOR DRILLING DWG. SEE C8500 - 747.
FOR DIMENSIONAL DWG. SEE C 8500.

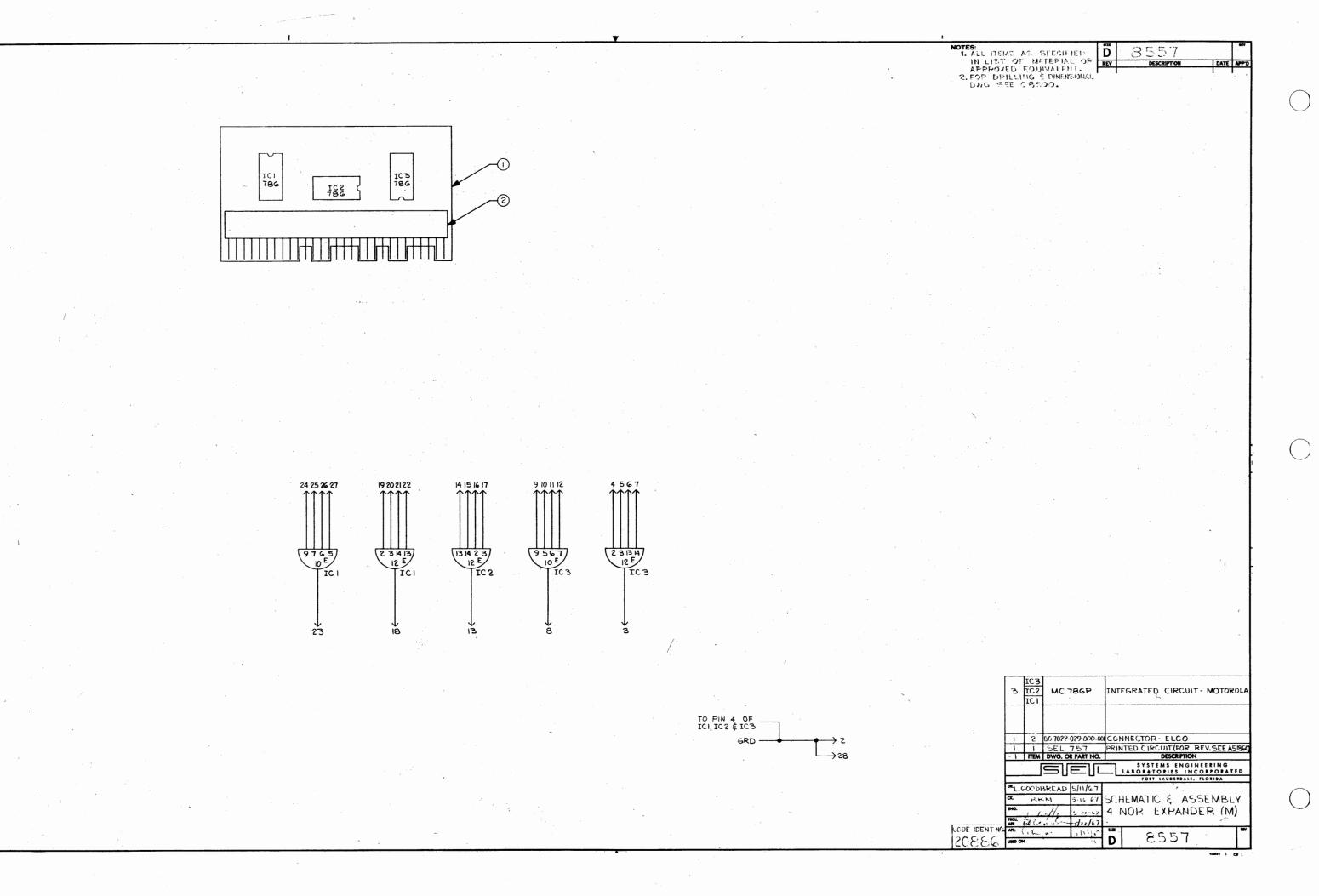


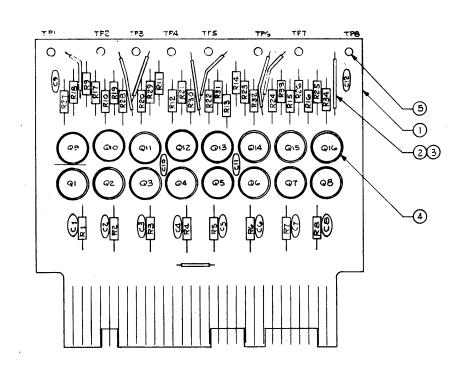


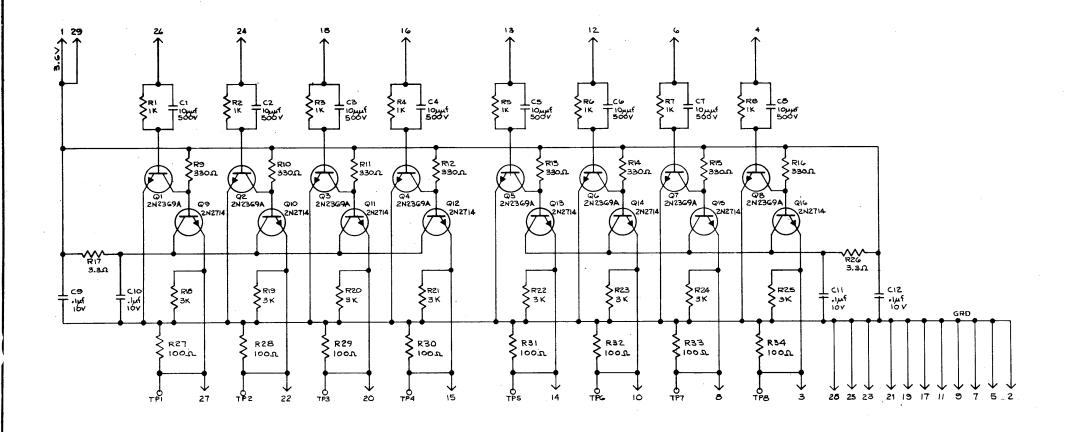




	3	103 103	мст	25P	INT	EGRATED CIRCUIT - MOTOROLA
	`	CI	ПКІО	-104	CAF	PACITORLuf+80-20%, IOV, CRL
	AR	3	AWG	24	+	RE-SOLID COPPER, TINNED
	-1	ITEM	SEL T		-	NTED CIRCUIT - (REV. SEE A 51860) DESCRIPTION
				EUL		SYSTEMS ENGINEERING LABORATORIES INCORPORATED FORT LAUDERDALE, FLORIDA
		L.SIE	NA	3-1-67 3-27-67	S	CHEMATIC & ASSEMBLY
	PROJ.	بى د	~0.	4120107		4 NOR (M)
о и тива вооо 2880S		82	3/1	4/20/69	D	8547







NOTES:

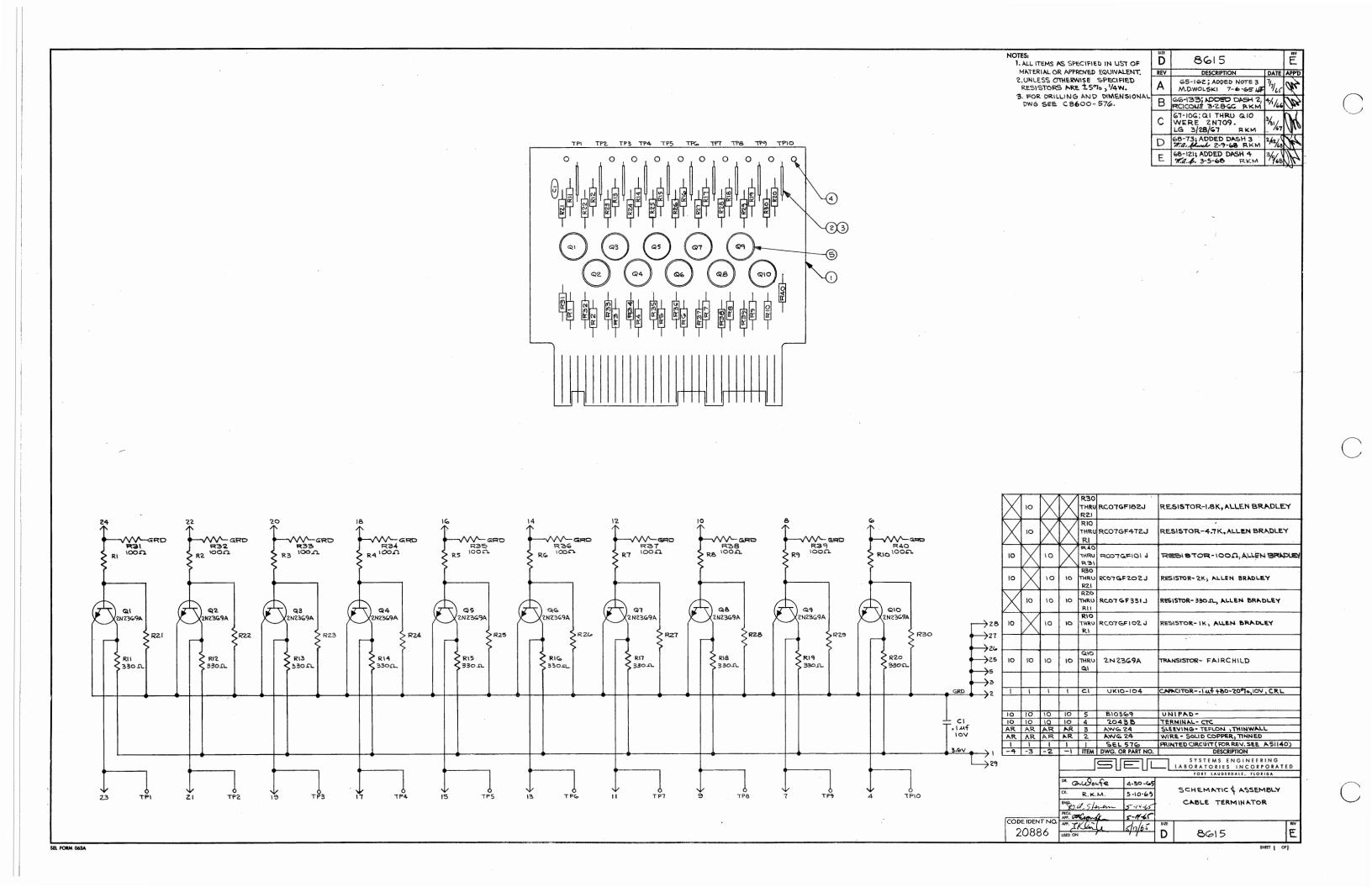
1. ALL ITEMS AS SPECIFIED IN
LIST OF MATERIALS OR
APPROVED EQUIVALENT.

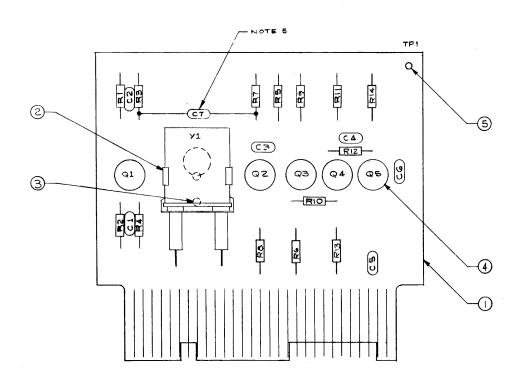
2 UNLESS OTHERVISE SPECIFIED, RESISTORS ARE \$57, 4 W. 3. FOR DRILL NO BRIMENS ON A. DAG SEE CBSCC -575

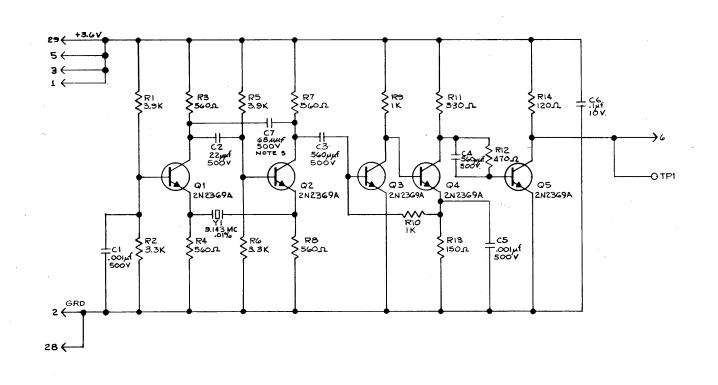
D	8614		HEV .
REV		DATE	~
		7k7/65	(M)
	6657 REVISED TO LATEST PC SEL 575C. KJ 35/66 RKM	3/4/66	an
С	GG-255, ADDED DASH 2 GWALE TIIS 166 RKM	7/19/6	IN.
D	66-276; R27 THRU R34,	^{3/2} /66	
E	67-10G; QI THRU Q8 WERE 2N709. LG 3/28/67 RKM	3/31/67	OK.

									ı			
		8	X	R34 THRU R27	RCOTGFIOIJ		RESISTOR-1000, ALLEN BEADLEY					
		в	B	R25 THRU RIS	RC07GF3o2J		RES	RESISTOR - 3K - ALLEN BRADLEY				
	·	2	2	R26 RIT	RCO7GF3R3J		RES	ESISTOR - 3.3.0 - ALLEN BRADLEY				
		8	В	14RU 14RU 189			RESISTOR - 3301 - ALLEN BRADLEY					
		8	8	R8 THRU RI			RESISTOR- IK - ALLEN BRADLEY					
		8 5 THRU 2N2714		TRANSISTOR - G.E.								
		8	٥	QB THRU Q1	2N23G9A		TRANSISTOR - FAIRCHILD					
		4	4	C12 C10 C9	UK-10-104		CAP	ACITOR Inf, ±10%, 10V - C	RL			
		8	8	C8 THRU			CAPACITOR-10/4, \$10%, 500V - CRL					
		8	8	5		43B	TERMINAL - CTC					
		IG AR	16 AR	3		0369 /G 24	UNIPAD					
		AR	AR	2		G 24	SLEEVING - TEFLON , THINWALL WIRE - SOLID COPPER , TINNED					
		1	1	1	+	-575	PRINTED CIRCUIT (FOR REV. SEE A51140)					
		-2	-1	ITEM		DWG. OR PART NO.		DESCRIPTION				
								SYSTEMS ENGINEERING				
						Ц -	—	FORT LAUDERDALE, FLORIDA	1150			
			DR.E.	J. SCH	IEICK	5-11-65	-	×HEMATIC ≠ ASSEME	21 🗸			
				R. F	REY	5-13-65 7/12/65	,	CABLE DRIVER	·-\			
٠,			PROJ.	7 14		1/2/63						
	20886		APF.	Deso	De	7/12/65	SIZE D	8614	E			

SHEET | OF







OTES:
1. ALL ITEMS AS SPECIFIED IN
LIST OF MATERIAL, OR
APPROVED EQUIVALENT.
2. UNLESS OTHERWISE SPECIFIED

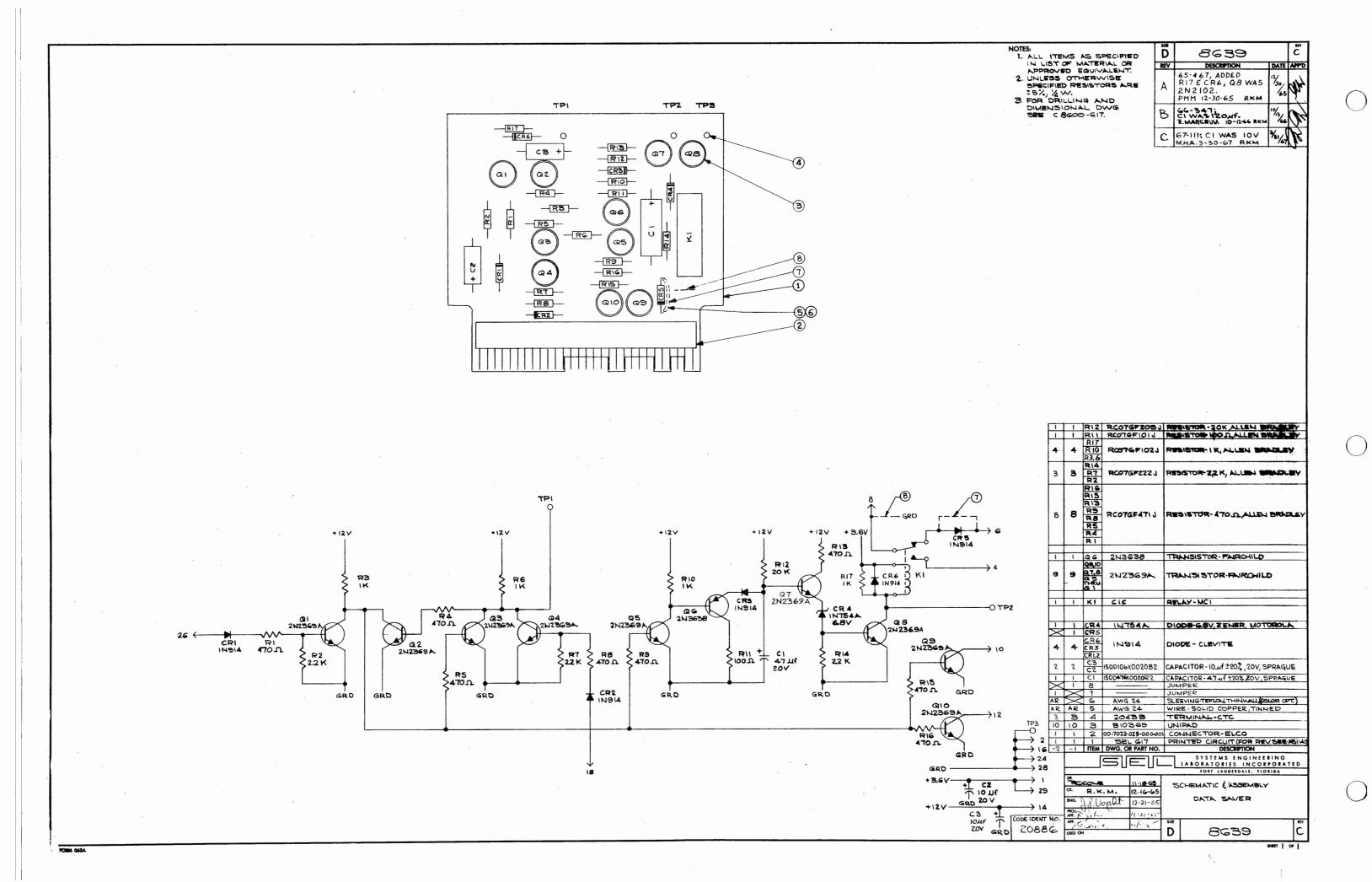
Z. UNLESS OTHERWISE SPECIFIED RESISTORS ARE 15%, WW. 3. FOR DRILLING & DIMENSIONAL DWG. SEE C8400-570.

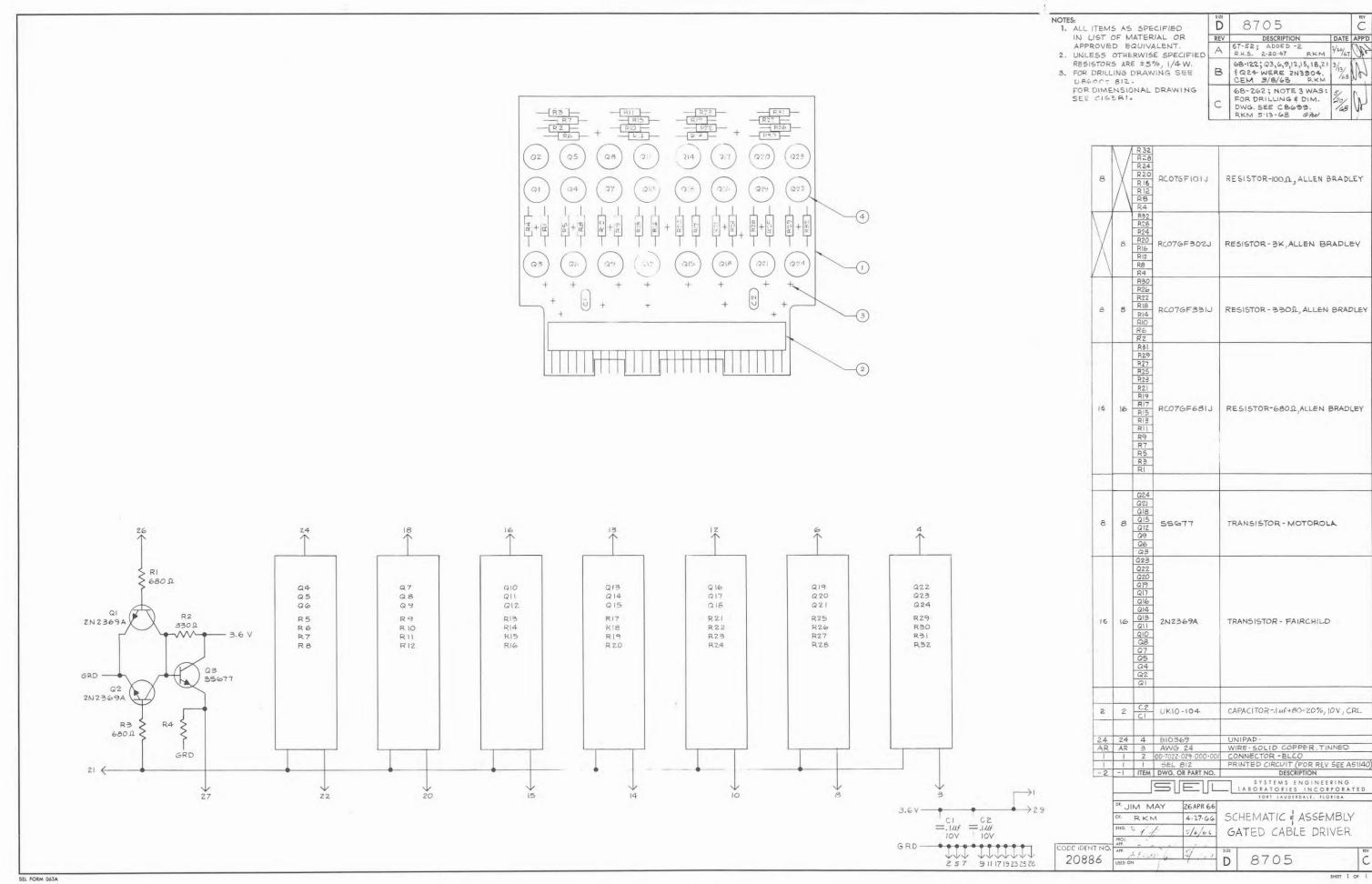
4. Y1 MAY BE SUBSTITUTED VITH G4-41, MONITOR. 5. DASH 2 FOR 5MH2 OPERATION ONLY.

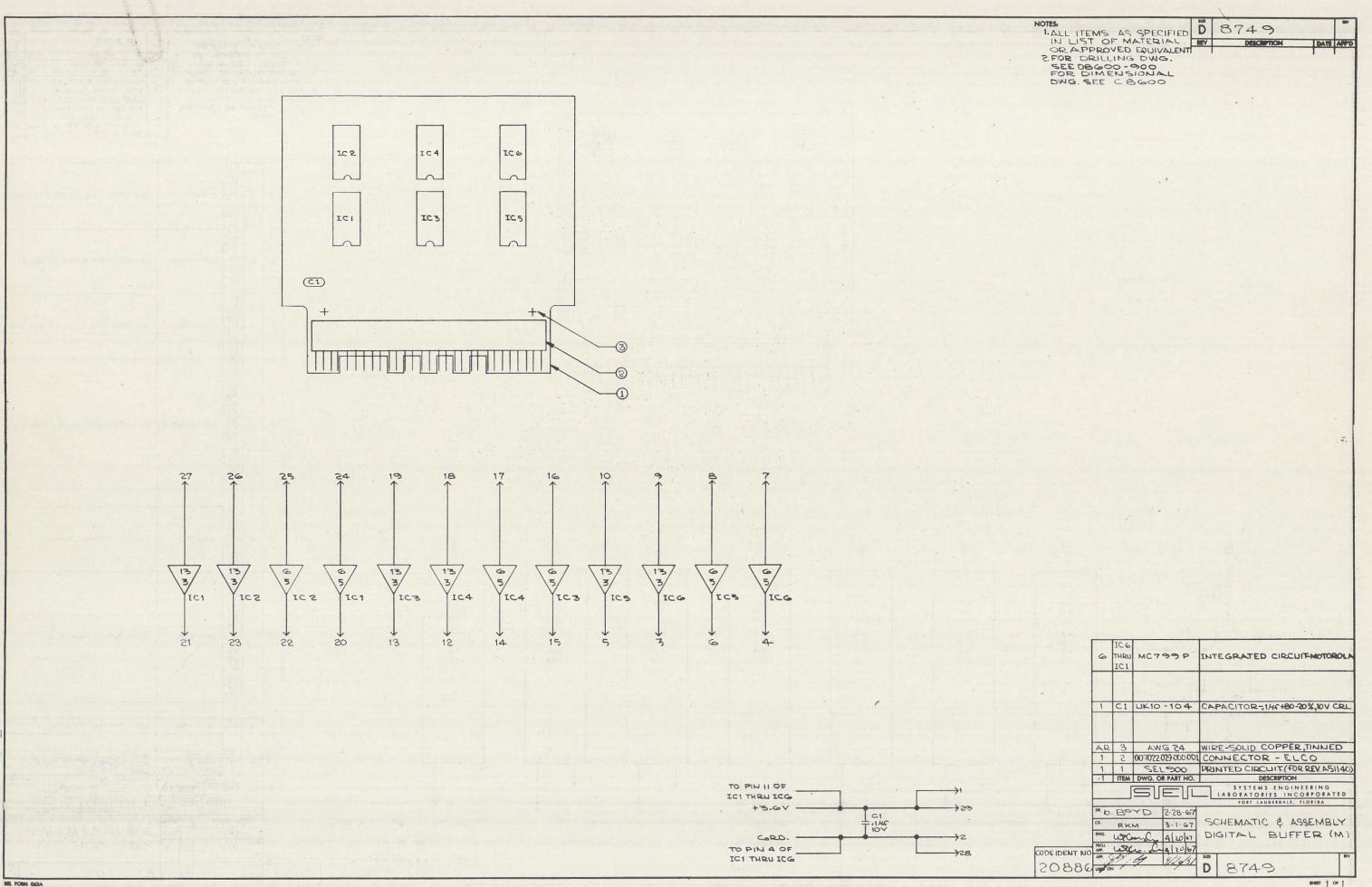
	Ď		Ę	
	REV	DESCRIPTION		APP'D
D.	Α	G5-179 ADDED NOTE 3 ELISCHEICK 7-22-65 UR?	7/22/	and
7	В	P.C. BACKGROUND	8/4/LC	W
	U	66-406; TITLE WAS IONE OSCILLATOR"		(M
	D	RKM 11-10-66 UC 67-1; ADDED DASH 2; NOTE 5. RKM 1-4-67 UC	'I ₅ 67	My
	E	GT-105; TRANSISTORS WERE 2NTO9; YI WAS 9.148 MC. LG 3/28/GT RKM	3/ ₂₁ / ₆₇	(Vy

	1	YI.	HC-G/U	NOTE14	CRY	STAL .	9.143 MC, .0	% PIEZO			
4	4	R8 R7 R4 R3	RCO7GF561J		RES	STOR-	560A - ALI	en Bradi	LEY		
2	2	R10	RCO7G	FIQZJ			1K	+			
2	2	RG R2	RCOT	s೯33ಬ			8.3K		_		
2	2	R5 R	RC07G	F3 9 2J			3.9K				
ı	1	RI4		FIZIJ			1500				
-		RI3		FISIJ		<u> </u>	1501				
1	1	RI2	RCO76	F471J		<u>† </u>	470s	<u> </u>			
1	1	RII	RCO7G	F33IJ	REIS	SISTOR-	-330T -YTT	N BRADL	ΕY		
		Q 5				- "	 		-		
5	5	THRU Q1	2N23	APBESNS		TRANSISTOR-FAIRCHILD					
1	$\overline{}$	C7	1 D-	680	CAPACITOR- 68 MUF110%, 500V, CRL						
2	2	C5	10-	1D-102		CAPACITOR 001 juf ±10%, 500Y - CRL					
2	2	C4	10-	561	CAPACITOR - 560 ft 10%, 500V - CRL						
	T	C2		220	CAPACITOR- 22,4 tio%, 500 V- CRL						
	- 1	C 6	UK-10	-104	CAPACITORInf+80%-20%,10V-CRL						
							-				
<u> </u>	<u> </u>	5	2043		TERMINAL - CTC						
5	5	4	B103		UNIPAD						
2	2	3	A87		CLIP - AUGAT						
+	1	2	8000			·		/ err	LIAC)		
-z	-1	ITEM	SEL-		PRINTED CIRCUIT (FOR REV. SEE AS1140)						
<u> </u>	-1 ITEM DWG. OR PART NO.				SYSTEMS ENGINEERING LABORATORIES INCORPORATED						
	E.J. SCHEICK 5-4-45			FORT LAUDERDALE, FLORIDA							
	CK. W. R FREY 5-11-65		- courses - correspond								
			1 OSCILLATOR								
	ENG. 0 -1-15-65			1							
NT NO	AM 1.7(AM 17-15-60			SIZE				l sev			
86	The Vinely 7/19/65		D	_ i			E				

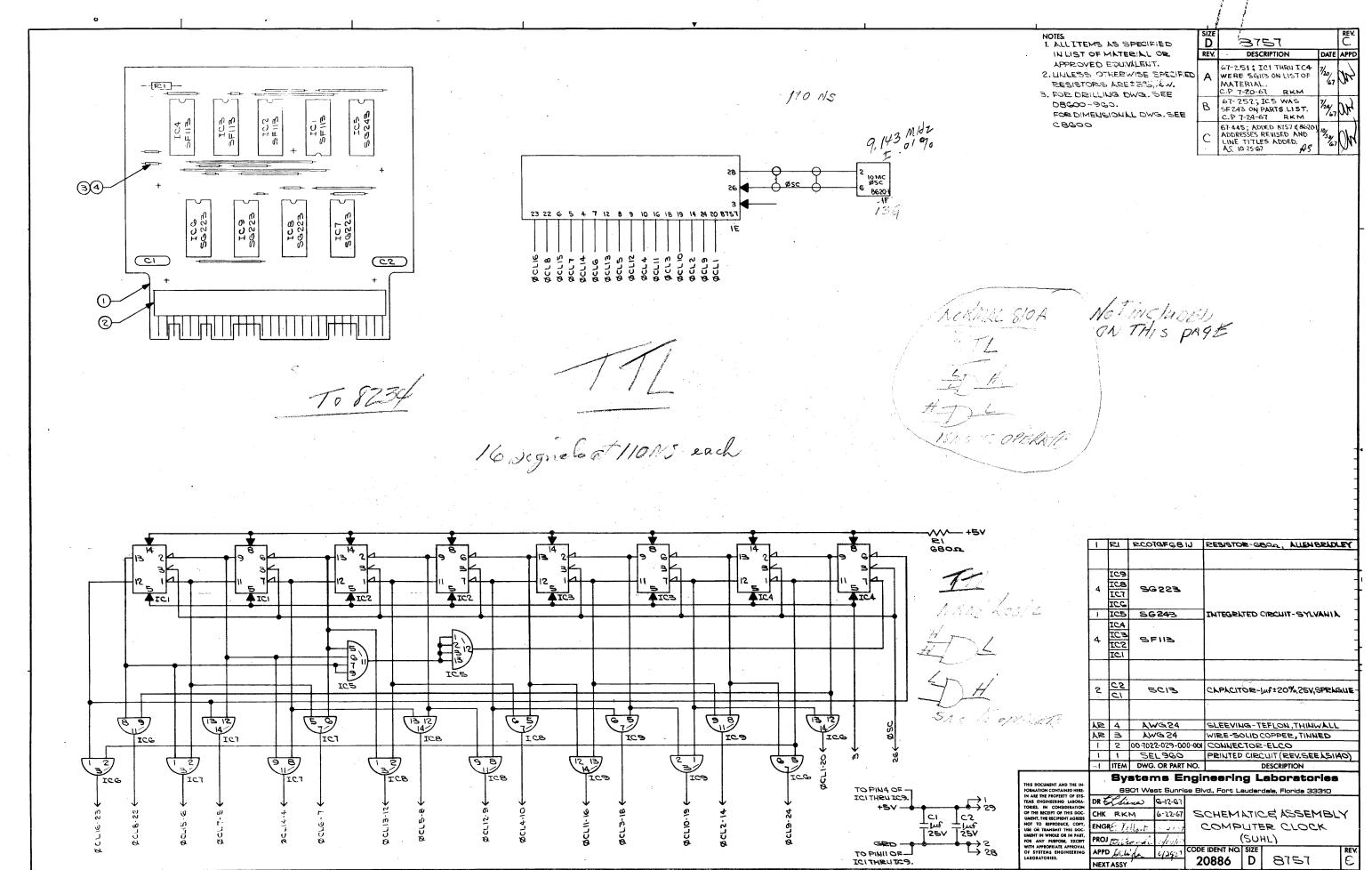
2088



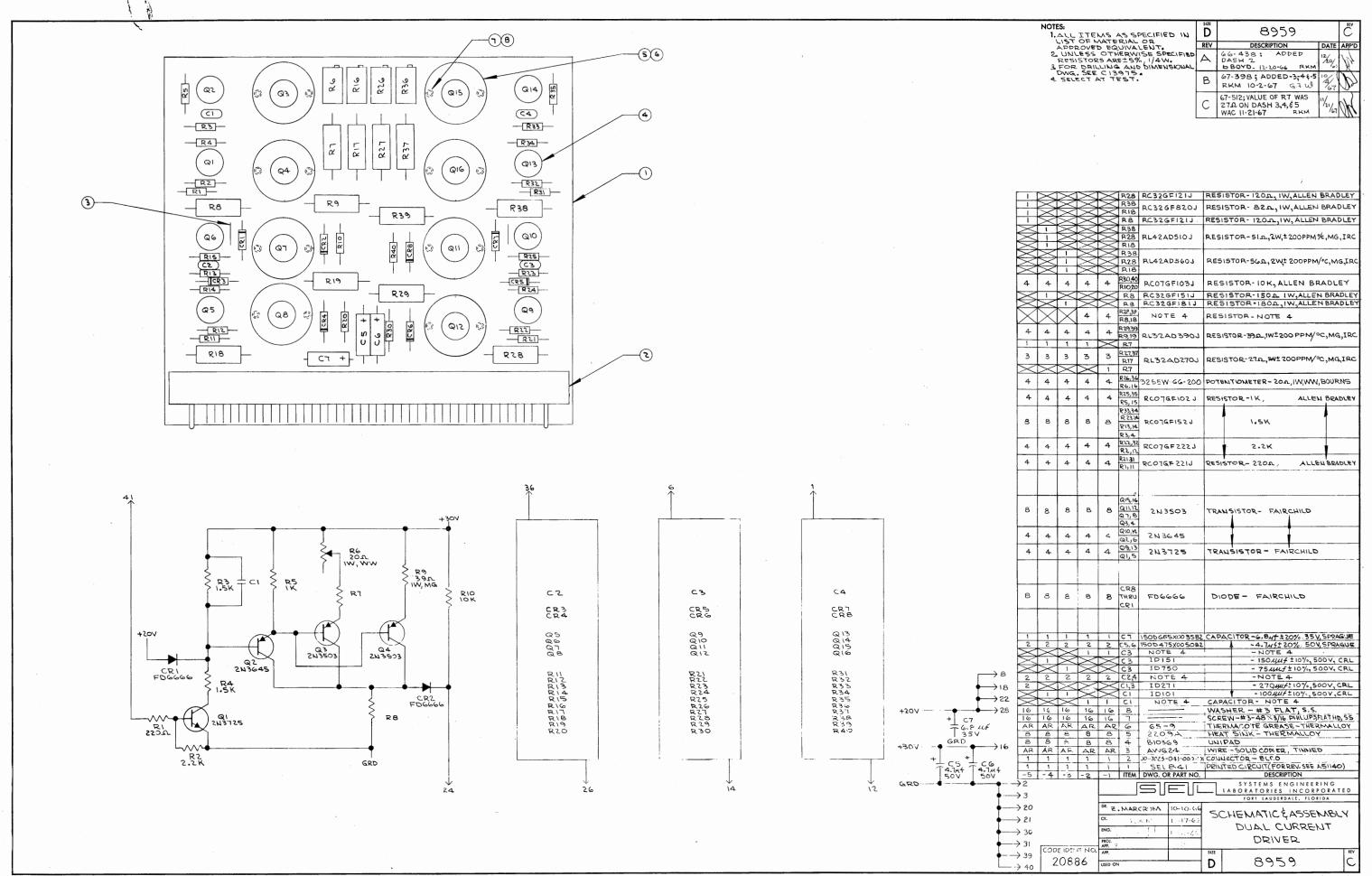


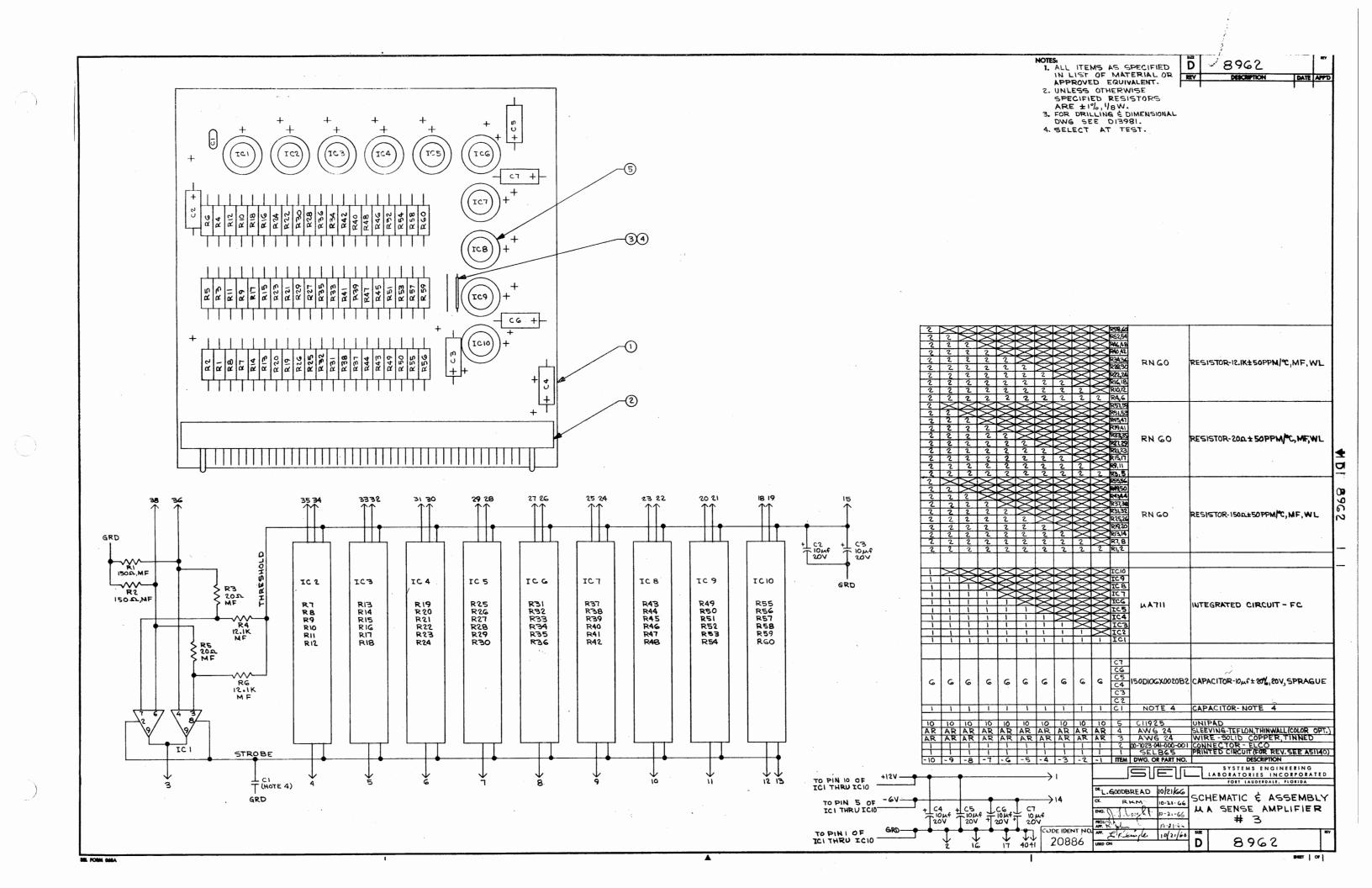


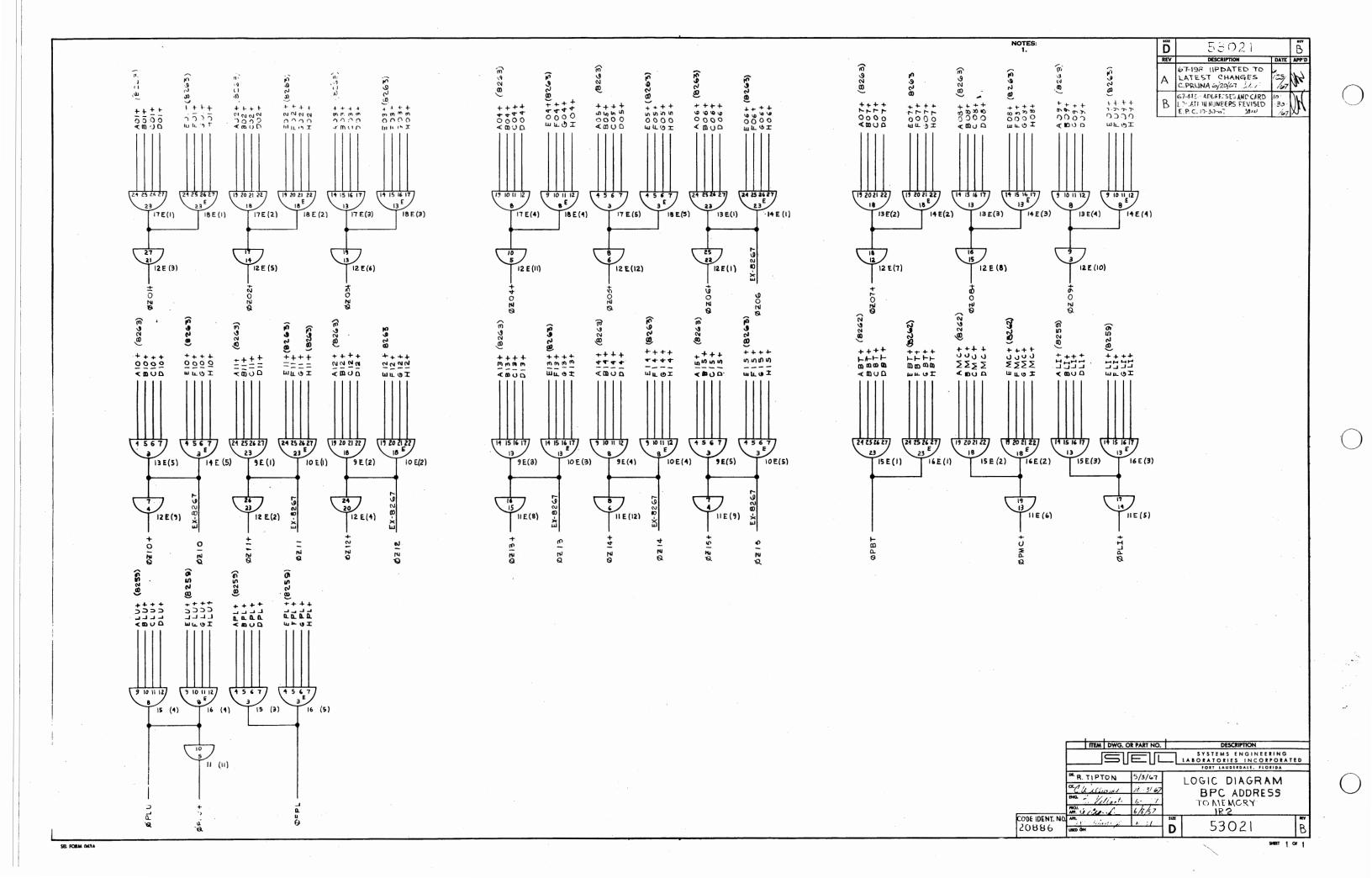
SHEET 1 OF 1

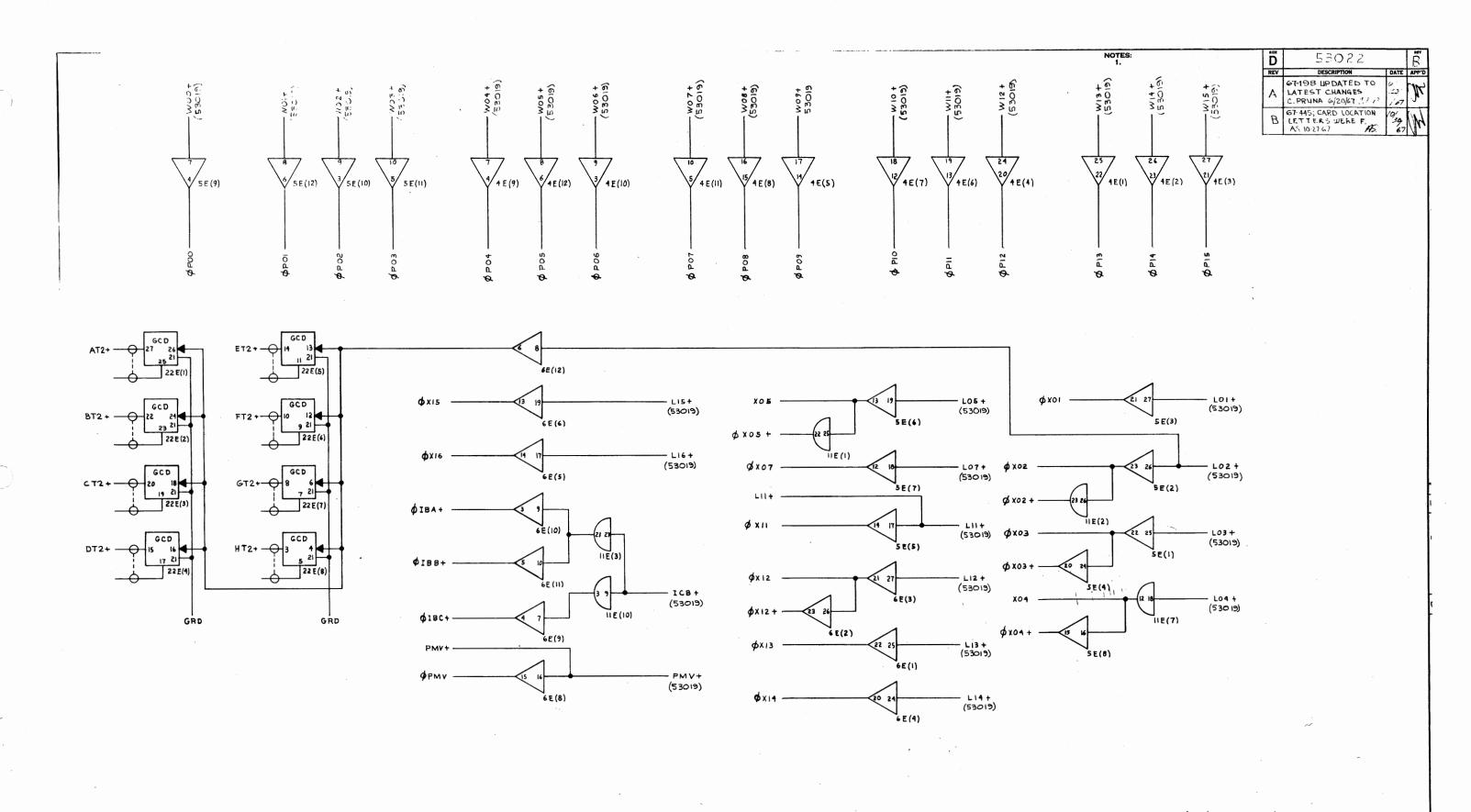


1 SHEET \









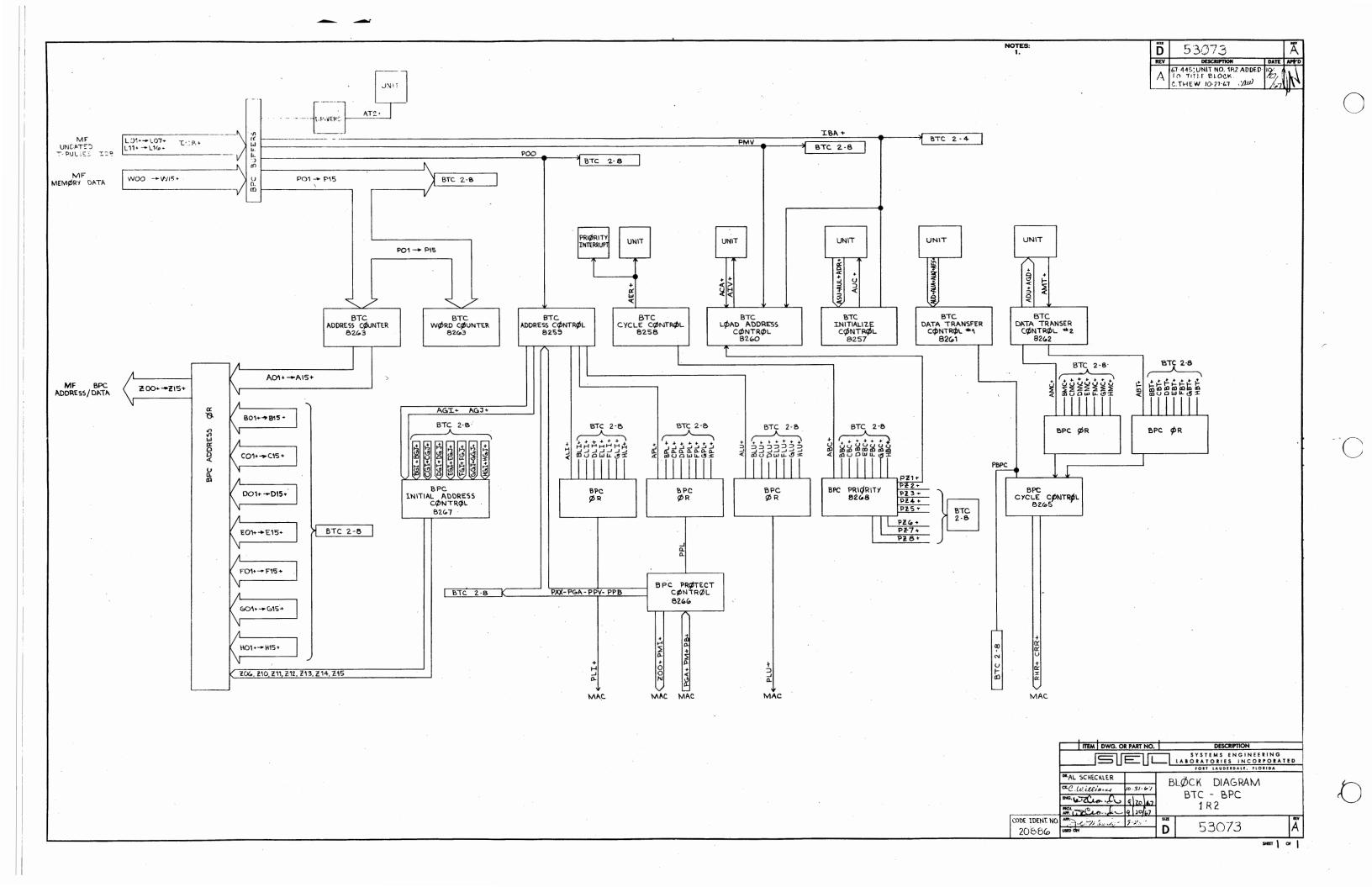
SYSTEMS ENGINEERING
LABORATORIES INCORPORATED

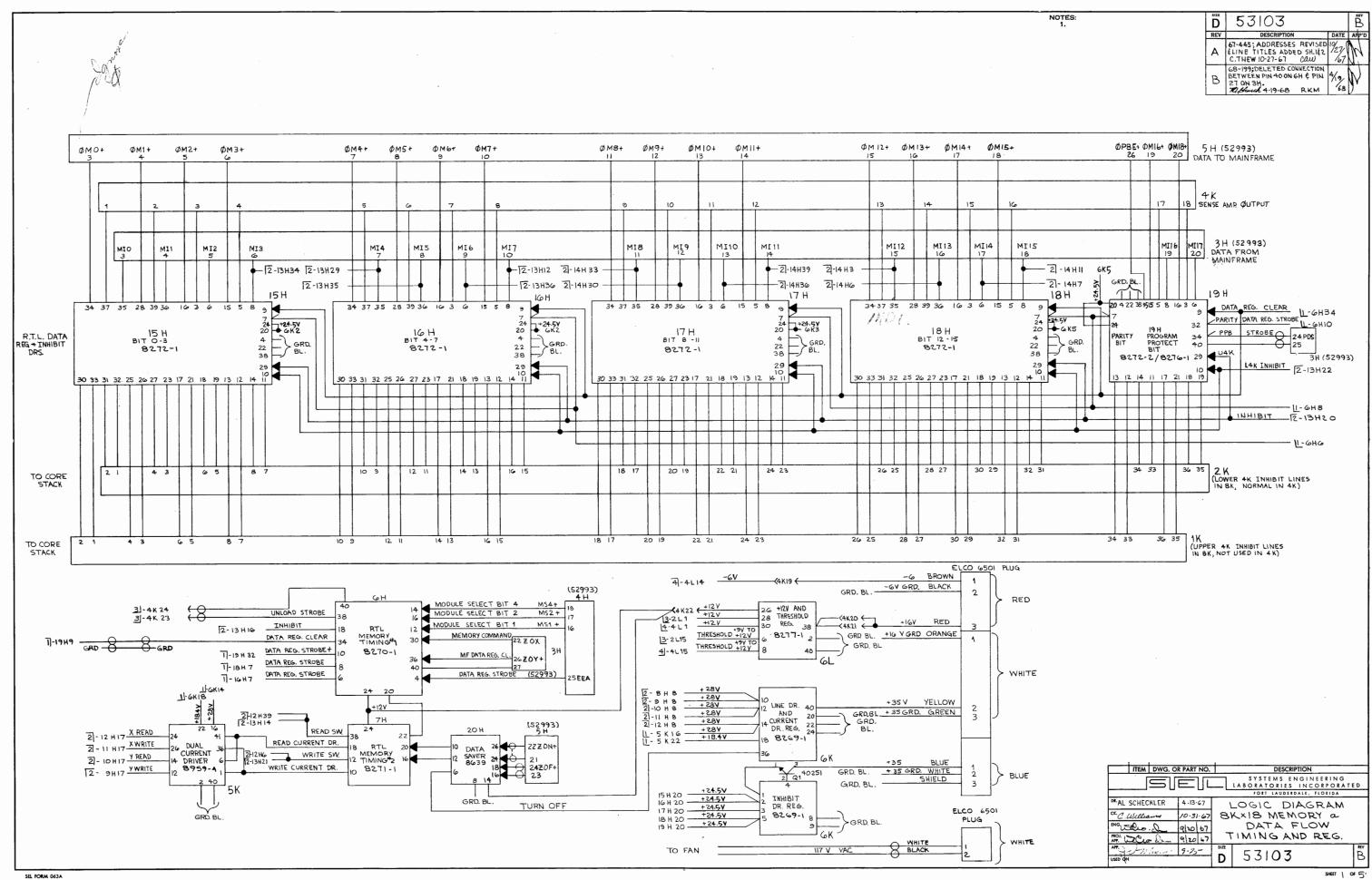
PR. TIPTON 5/3/67
CC. Williams 16 31 67
BNO. 1/1/1/1
DNO. 1/1/

↑

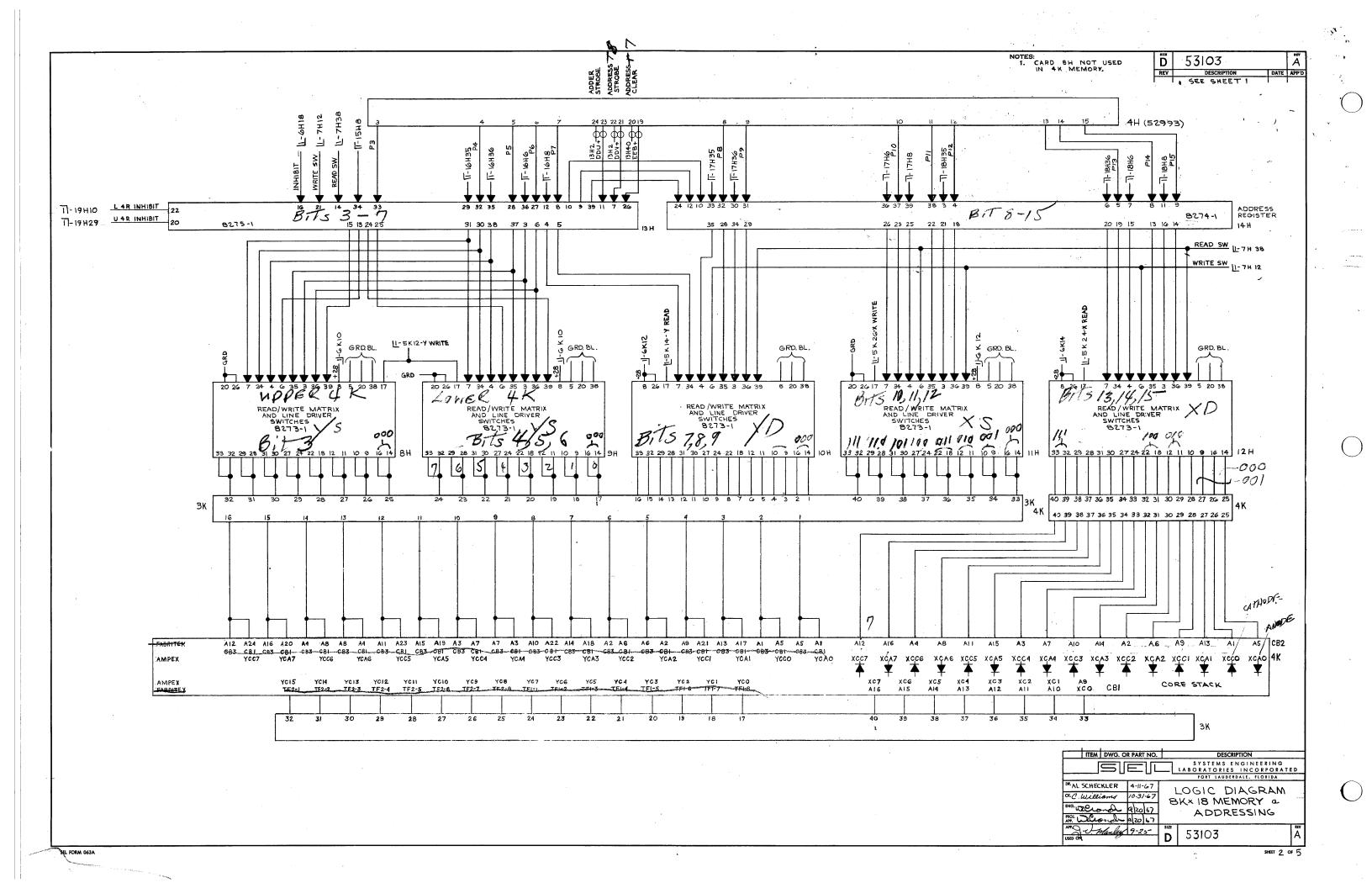
SEL FORM 063

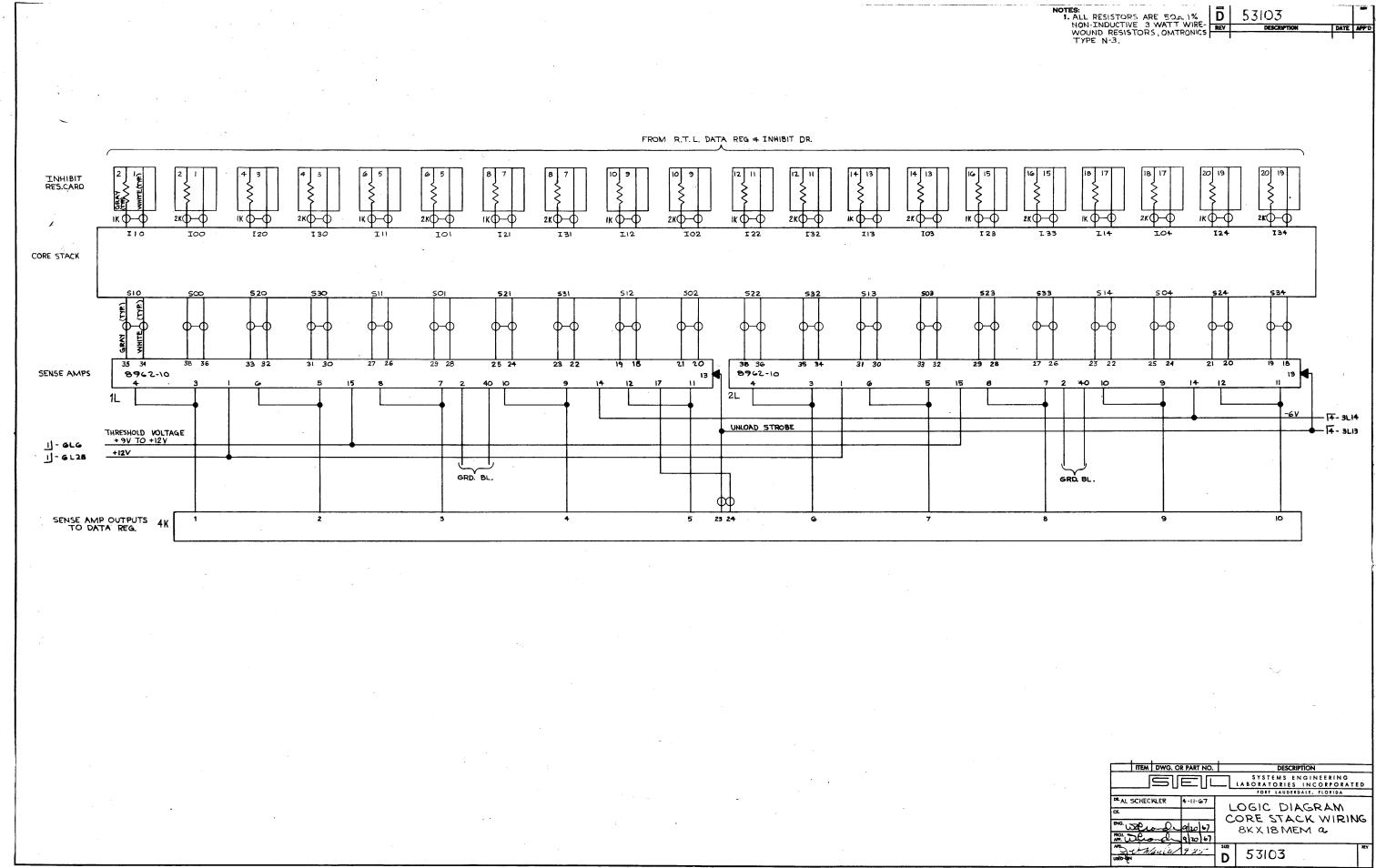
SHEET 1 O





SHEET | OF 5'





NOTES:

1. ALL RESISTORS ARE 50...
1% HOH-INDUCTIVE 3 WATT
WIRE WOUND RESISTORS,
OMTRONICS TYPE N-3. D 53103 REV DESCRIPT FROM RT.L. DATA REG - INHIBIT DR. INHIBIT RES.CARD CORE STACK 38 36 SENSE AMPS 8962-10 8962 3L 3-2114 -6V UNLOAD STROBE 1-4K19 THRESHOLD 11-6L8 +12 GRD. BL. SENSE AMP OUTPUTS 4K ITEM DWG. OR PART NO. SYSTEMS ENGINEERING
LABORATORIES INCORPORATED
FORT LAUDERDALE, FLORIDA MAL SCHECKLER 4-12-67 LOGIC DIAGRAM CORE STACK MOUZCOOL 9/20 67 WIRING BKX18 MEM Q 5310**3**

SEL FORM 063A

NOTES:

1. S09, S19, S29, \$39 ARE

1. S09, S19, S29, \$39 ARE

TERMINATED INDIVIDUALLY TO

FOUR RESISTORS (330\Omega, 1/4\W).

2. I09, I19, I29, \$139 ARE.

TERMINATED INDIVIDUALLY TO

FOUR RESISTORS (41\Omega, 1/4\W).

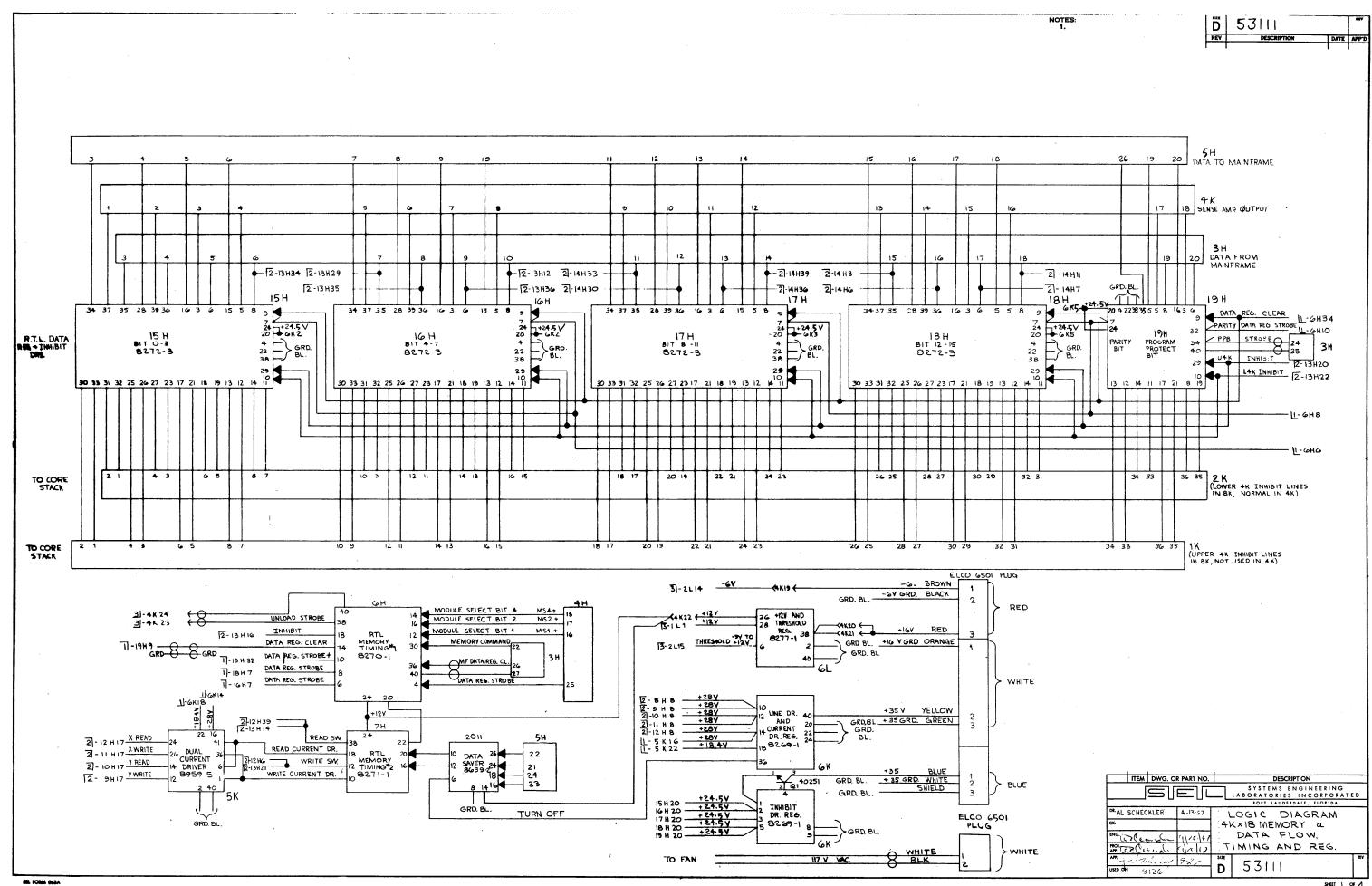
D	53103		A
REV	DESCRIPTION	DATE	APP'D
А	68-199; ADDED NOTES 1 & 2. 70 Charl 4-19-68 RKM	1/19/68	∂_{r_j}

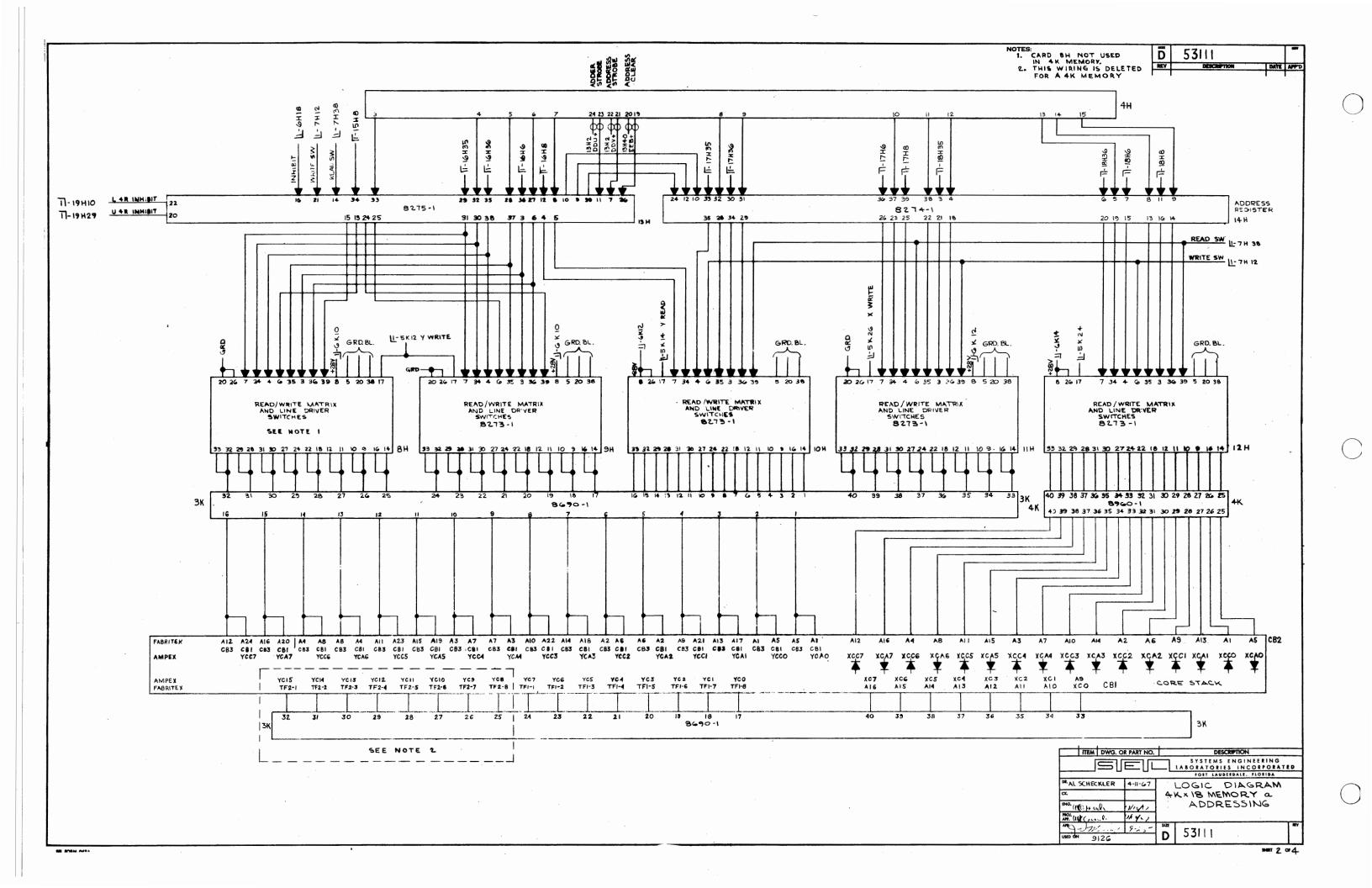
Н	25	24	23	22	21	DATA SAVER	6 R.T.L. DATA REG. (PARITY+P.B.B.)	Ø R.T.L. DATA REG.	Z R.T.L. DATA REG.	6 R.T.L. DATA REG.	й RT.L.DATA REG.	A.T.L. 8 BIT ADD, REG.	ũ R.T.L. 5 BIT ADD, REG.	で R.T.L. MATRIX + L.D.S.	= R.T.L. MATRIX+L.D.S.	0 R.T.L. MATRIX+L.D.S.	& R.T.L. MATRIX+L.D.S.	® R.T.L. MATRIX+L.D.S.	L R.T.L. TIMING # 2	6 R.T.L. TIMING # 1	MEMORY INTERFACE CONN.	P MEMORY INTERFACE CONN.	W MEMORY INTERFACE CONN,	2	1
				-		K INI						1													
				<u> </u>		(INF		DR.C	JNN.			-		soc	100							110		10	립
K				ADDI	RESS	CON	IN.					3		300											
N .				SEN	SE OI	JTPU	T + A	DD. C	ONN.			4				`									
				DUA	L CU	RREN	4T D	R.				5					\			/					
		AN		LINE	DR.	CURR	ENT	DR.,	·INH	DR.	REG.	6							ORE						FILTER
	٦	AN		SEN	SE A	AMP.						١						_							
				SENS	SE A	MP.						2								\					A R
L				SENS	SE AN	ИP.						3									`				
				SENS	1A 36	NP.						4		53	<u>ک</u> ا ٥	30				****		IS		०ऽ६	4
												5			•									,	'
				+12 V	AND	THR	ESHO	LD R	EG.			6													

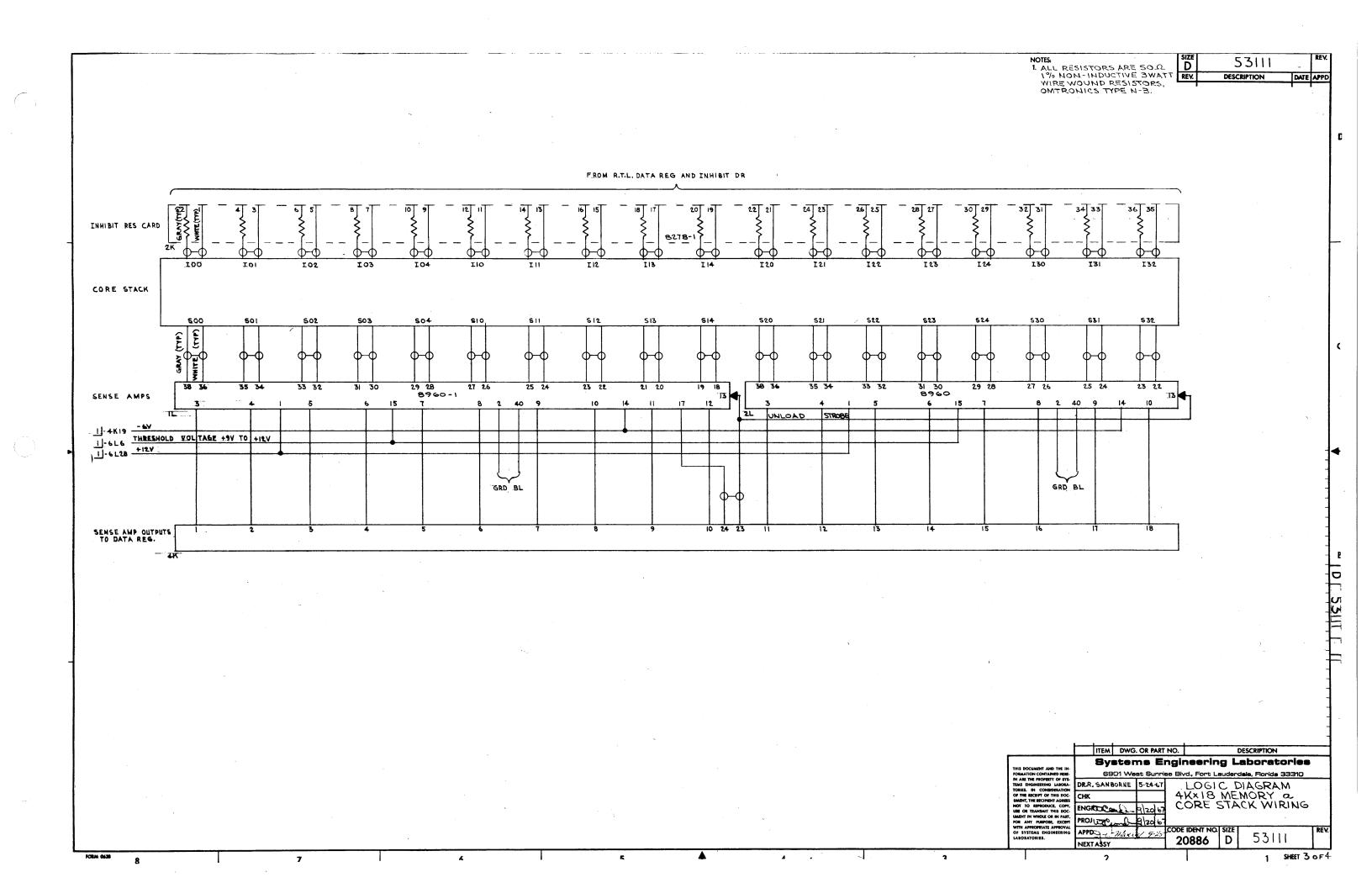
CARD	SIDE	VIEW	
-/ 111	3,00	4 (- 1 4	

CARD NAME	CARD	CARD TYPE	8K×16	8K×16 + PARITY	8K×16 +PARITY +MEMORY PROTECT
R.T.L. MEMORY TIMING #1	6н	8270-1	1	1	1
R.T.L. MEMORY TIMING #2	7H	8271-1	1	1	1
R.T.L. READ/WRITE MATRIX AND LINE DR. SW.	8H THRU 12H	8273-1	5	5	5
R.T.L. 5 BIT ADDRESS REG. +8K CONTROL	13H	8275-1	١	1	١
R.T.L. 8 BIT ADDRESS	14 H	8274-1	1)	1
R.T.L.DATA REGISTER AND INHIBIT DR.	15H THRU 18H	8272-1	4	4	4
R.T.L. DATA REGISTER AND INHIBIT DR.	19H	8272-2	0	1	0
R.T.L. PARITY PROTECT DATA REG. + INHIBIT DR.	19 H	8276-1	0	0	1
DATA SAVER	20 H	8639-2	ı	ı	1
INHIBIT RESISTOR CARD	IK AND 2K	8278-1	2	2	2
CORE STACK CONNECTOR CARD	3K AND 4K	8960-1	2	2	2
DUAL CURRENT DRIVER	5K	8959-4	1	ı	١
LINE DR. AND CURRENT DR. REG.	6K	8269-1	ı	1	١
U.A. SENSE AMP	IL THRU 3L	8962-10	3	3	3
U.A. SENSE AMP	4L	8962-2	1	0	0
U.A. SENSE AMP	4L	8962-4	0	1	0
U.A. SENSE AMP	4L	8962-6	0	0	ı
+12V AND THRESHOLD REG.	6L	8277-1	i	1	1

		1				
ITEM DWG. O	R PART NO.			DESCRIPTION		
				TEMS ENGIN		ΕD
			FOI	T LAUDERDALE, F	LORIDA	
AL SCHECKLER	5-8-67		CARD	LAYOU	T =	
cx.		υ	SAGE	CHART		
ENG. Witherden	9/20/17			a		
MOJ. W. Croude	9/20/67	8	K × 18	MEMORY		
Manie	9-25-	SIZE	F 7	0.7		REV
USED ON		D	55	103		Α







NOTES:

1. \$33 \$534 ARE TERMINATED
INDIVIDUALLY TO TWO
RESISTORS (3300,1/4W)

2. 133 \$ 134 ARE TERMINATED
INDIVIDUALLY TO TWO
RESISTORS (4701,1/4W)

4K×16

+ PARITY + MEMORY PROTECT

Ď	53111	-	B	
REV	DESCRIPTION	DATE	APP	ō
Α	68-199; ADDED NOTES 1 & 2 Milhurel 4-19-68 RKM	4/ ₁ 4/ ₆₈	0)	
В	68-309: DUAL CURRENT DR WAS 6959-5 . ADDED SENGE AMP RESISTOR TERM 83 282-1. J. PINEDO. 6-7-68 W	6/1/68	\mathcal{A}_{1}	
	REV	REV DESCRIPTION (88-199; ADDED NOTES 1 & 2 MADELLA 19-68 RKM (88-309: DUAL CURRENT DR WAS 8959-5 ADDED SENSE AMP RESISTOR TERM 83282-1.	REV DESCRIPTION DATE A 68-199; ADDED NOTES 1 62 102 102 103 104 105 105 105 105 105 105 105 105 105 105	REV DESCRIPTION DATE APP A 68-199; ADDED NOTES 1 62 1162 1162 1162 1162 1162 1162 1162

Н						DATA SAVER	R.T.L. DATA REG. (PARITY+ P.P.B.)	R.T.L. DATA REG.	R.T.L. DATA REG.	R.T.L. DATA REG.	RT.L.DATA REG.	R.T.L. 8 BIT ADD, REG.	R.T.L. 5 BIT ADD, REG.	R.T.L. MATRIX +L.D.S.	R.T.L. MATRIX+L.D.S.	R.T.L. MATRIX+L.D.S.	R.T.L. MATRIX+L.D.S.	NOT USED IN 4K MEMORY	R.T.L. TIMING # 2	R.T.L. TIMING # 1	MEMORY INTERFACE CONN.	MEMORY INTERFACE CONN.	MEMORY INTERFACE CONN.		
	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	,
				тои	USE	N O.	4K	MEM	ORY			١	,												
				LOW	ER 41	(IN	IIBIT	DR.CC	NNC.			2			100	-						I	10		립
				ADD	RESS	CON	IN.					3		500								/		510	
K				SENS	SE OL	JTPU	T + A[DD. CC	ONN.			4						-			/		-		
				DUA	L CUR	RENT	DR.					5								/					
	_	'AN		LINE	DR.,c	URREI	NT DR	,+IN	нівіт	DR.RE	G.	G						`	COR						ER R
	'	A14		SEN	SE A	AMP.						١						/							FILTER
				SENS	SE A	MP.				-		2					/								Z Z
L												3			١,	/									
												4		530		30							051	950	L
												5				,5									
				+12 V	AND	THR	ESHO	LD R	EG.			G													
				• • • • • • • • • • • • • • • • • • • •				C/	ARD	511	DE	VIEV	V												

			L	l	· · · · · · · · · · · · · · · · · · ·
R.T.L. MEMORY TIMING #2	7H	8271-1	١	1	1
R.T.L. READ/WRITE MATRIX AND LINE DR. SW.	9H THRU 12H	8273-1	4	4	4
R.T.L. 5 BIT ADDRESS REG. +8K CONTROL	13H	8275-1	١	١	١
R.T.L. 8 BIT ADDRESS	14 H	8274-1	1)	1
R.T.L.DATA REGISTER AND INHIBIT DR.	15H THRU 18H	8272-3	4	4	4
R.T.L. DATA REGISTER AND INHIBIT DR.	19H	8272-2	0	ł	0
R.T.L. PARITY PROTECT DATA REG. + INHIBIT DR.	1914	8276-1	0	0	. 1
DATA SAVER	20 H	8639-2	١	1	١
INHIBIT RESISTOR CARD	гк ,	8278-1	1	١	1
CORE STACK CONNECTOR CARD	3K AND 4K	8960-1	2	2	2
DUAL CURRENT DRIVER	5K	8959-4	1 .	1	١
LINE DR. AND CURRENT DR. REG.	6K	8269-1	ı	1	١
U.A. SENSE AMP	1L	8962-10	1	t	1
U.A. SENSE AMP	SL	8962-8	0	0	١
U.A. SENSE AMP	2L	8962-7	0	١	0
U.A. SENSE AMP	2L	8962-6	١	0	0
+ 12 V AND THRESHOLD REG.	6L	8277-1	ı	ŀ	1 '

CARD

LOCATION

6H

4K

83282-1

CARD

TYPE

8270-1

4 K × 16 4 K × 16

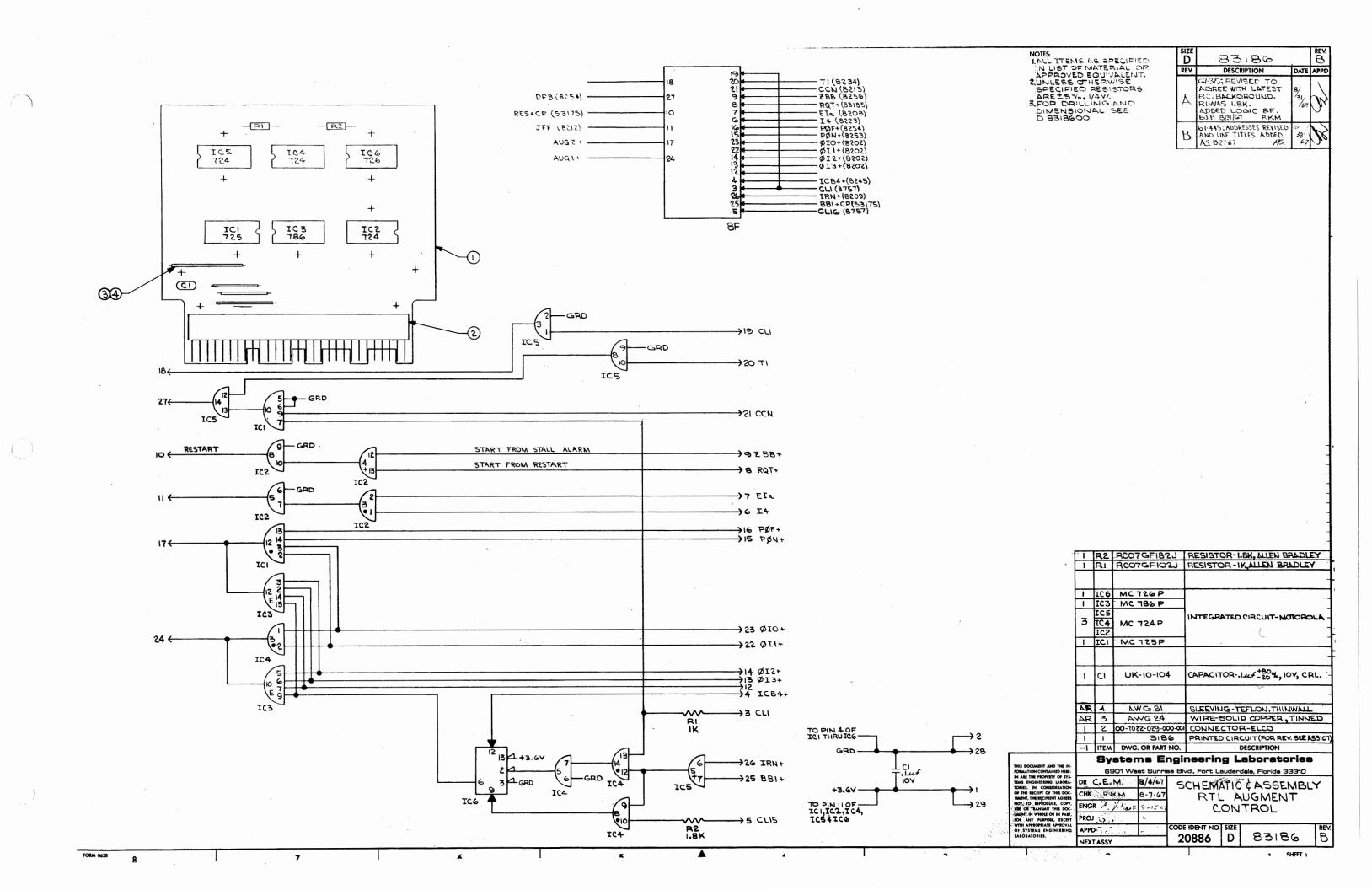
+ PARITY

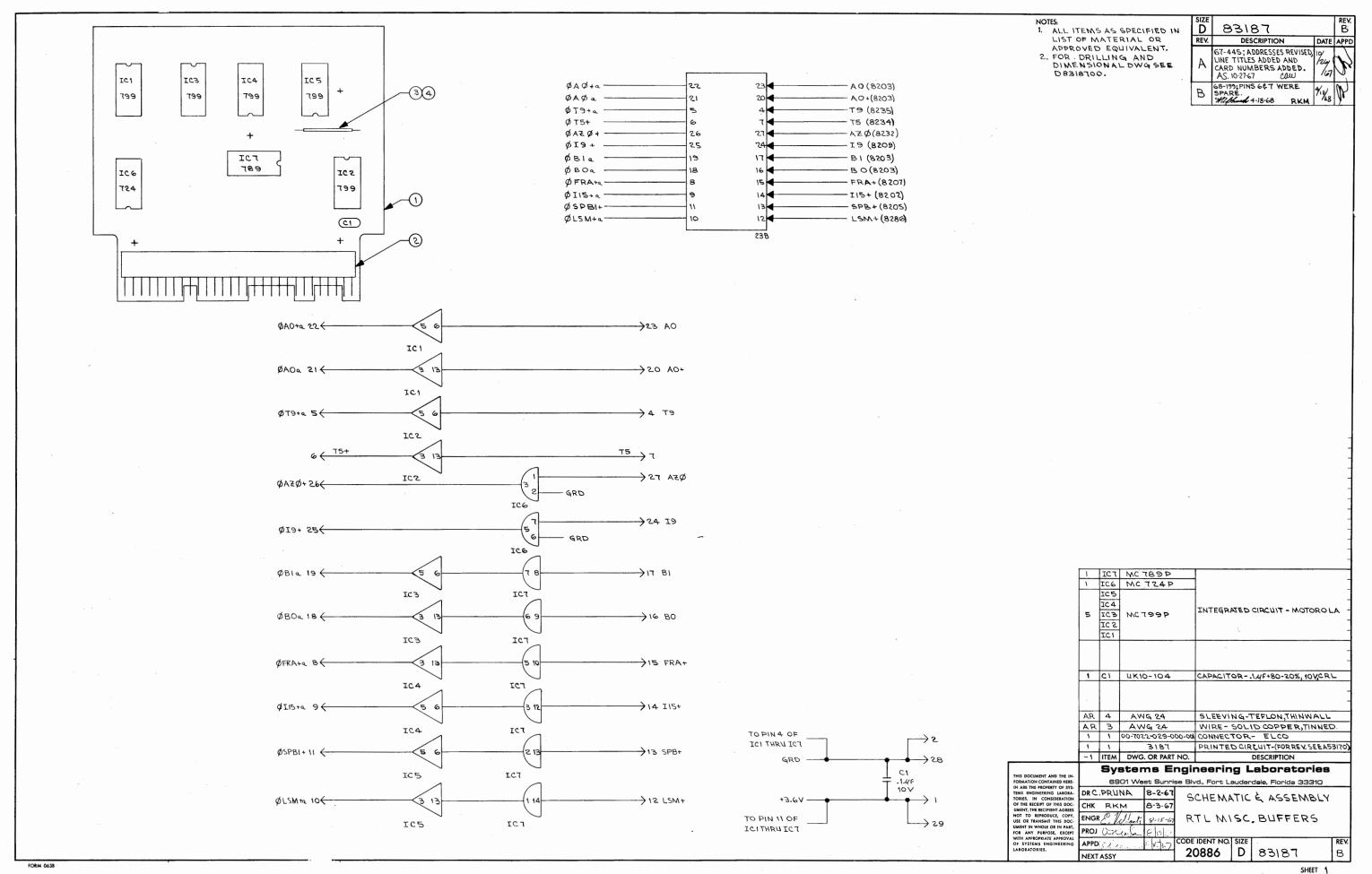
CARD NAME

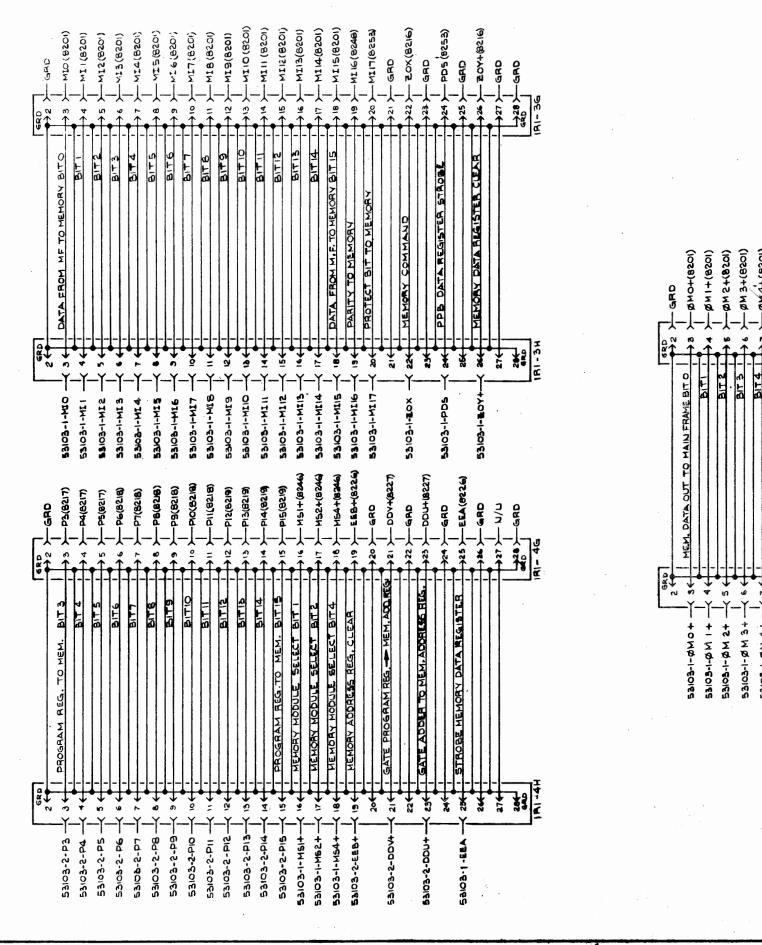
R.T.L. MEMORY TIMING #1

SENSE AMP RESISTOR TERMINATOR

1 1					
ITEM DWG. O	R PART NO.			DESCRIPTION	
				MS ENGINEERING	E D
			FORT L	AUDERDALE, FLORIDA	
MAL SCHECKLER	5-8-67		CARD	LAYOUT &	_
α.		_	USAG	E CHART	
ENG BCionQu	9/20 67				-
Mai Weloom	4/2067		4K×18	MEMORY a	
MA Jalas	5-25-	SIZE) .
USED ON 9126		D	5	3111	R







GRD GRD	╼╎╼╎╼╎╼╎╼╎╼╎╼╎╼╎╼ ╺ ┆╼┆╼┆╼┆╼┆╼┆╼┆╼┆		22
MEM, DATA OUT TO MAIN FRAME BITO BITI BITE BITE	BIT 5 BIT 6 BIT 7 BIT 8 BIT 9 BIT 9 BIT 11	BITIE BITIE BITIE MEM DATA OUT TO MAIN FRAME BIT IS PARITY BIT OUT	TURN MEMORY ON TURN MEMORY OFF PON OR WITH POF
53103-1-ØMO+	53103-1-ØM 6+	(- <u>\</u> __\-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\-\	53103-1-20N + 224 53103-1-20F + 24 53103-1-PBE + 264 682 1R1 - 5 H

NOTES:

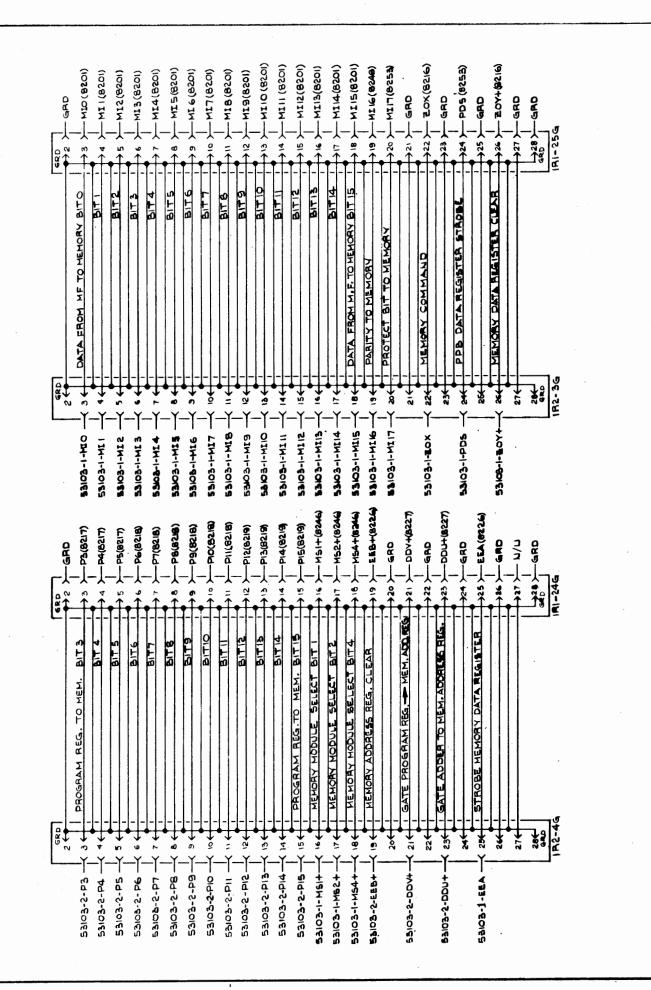
ITEM DWG. O	R PART NO.		DESCRIPTION	
		司	SYSTEMS ENGINEERING LABORATORIES INCORPORAT	ŧ D
			FORT LAUBERDALE, PLORIDA	
" J. PINEDO	1-7-1	1.1	IRING DIAGRAM	
a. C. William	10.31-67		TERCONNECTING CAR	1
ENG. V. C. C.C.	1-/21/1	12	EMORY MODULE # T	
PROL.	duke	М	EMORT MODULE # 1	
When a	1. 1	202	52993	1
USED ON		D	25222	IA

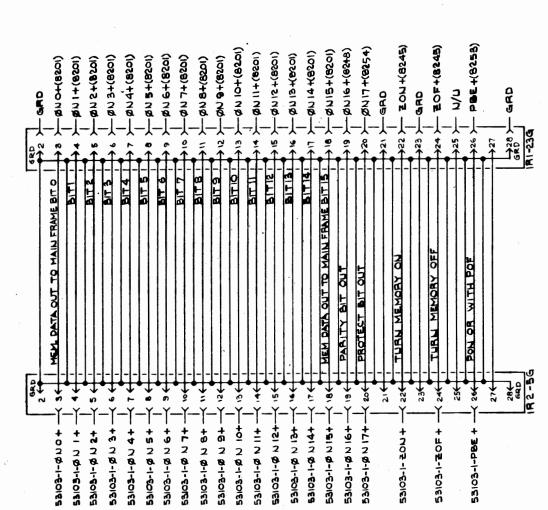
52993

DESCRIPTION DATE APPO 67-445:UPDATED TO LATEST IO CHANGES. ADDED SHEET 2 J. PINEDU.IO-27-67 ML C7

D

REV





ITEM DWG. O	e PART NO	 	DESCRIPTION	
			SYSTEMS ENGINEERII LABORATORIES INCORPO FORT LAUSERBALE, FLORID	RATED
J. PINEDO	16-27 47		IRING DIAGRAM	
BNG., I(i)	1.1/0.1	IN.	TERCONNECTING OF	YBLE
APP.	11 1	ν.α.	52993	Ā

52993

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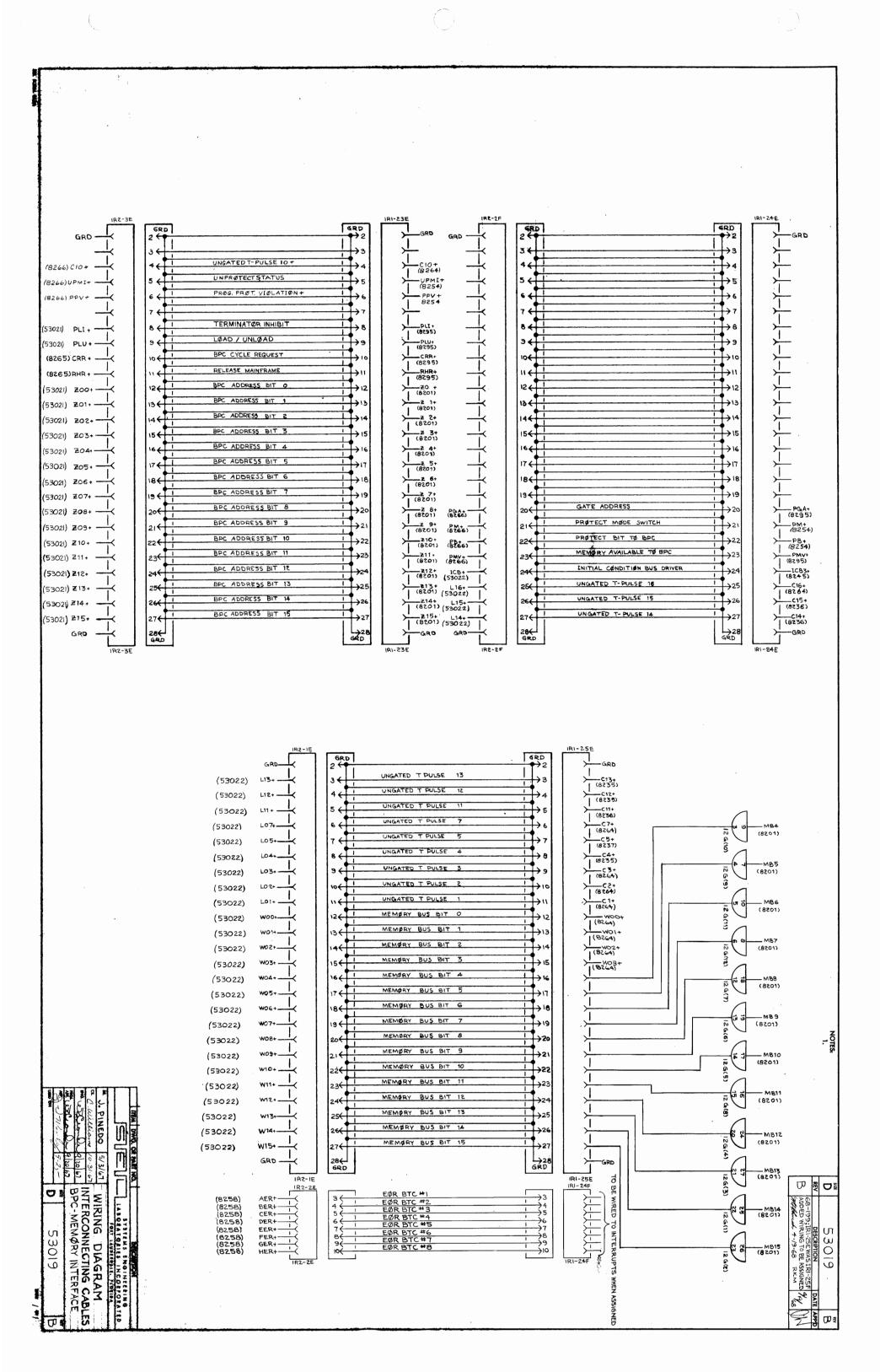
REV DESCRIPTION

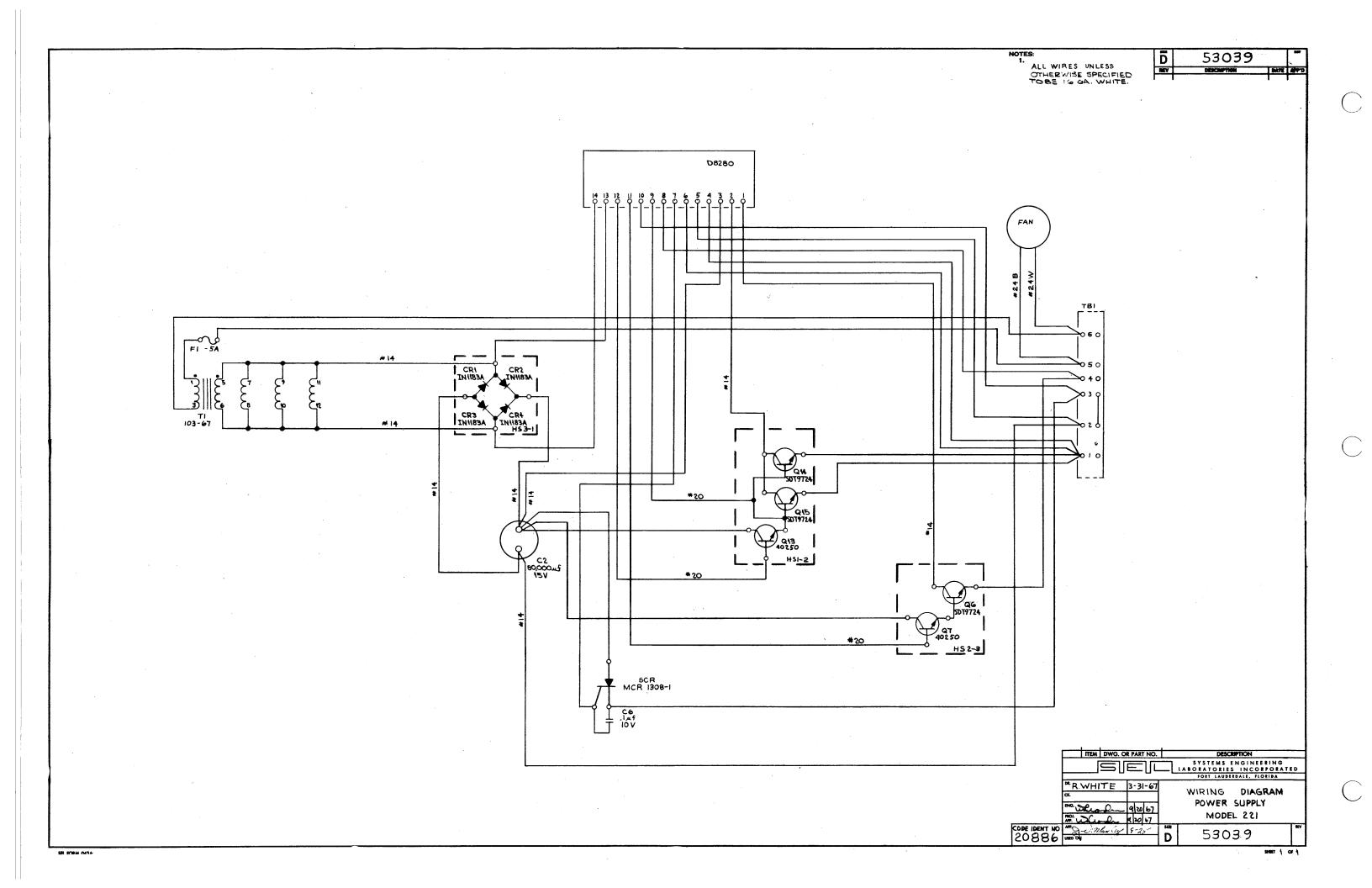
67-445: THIS SHEET

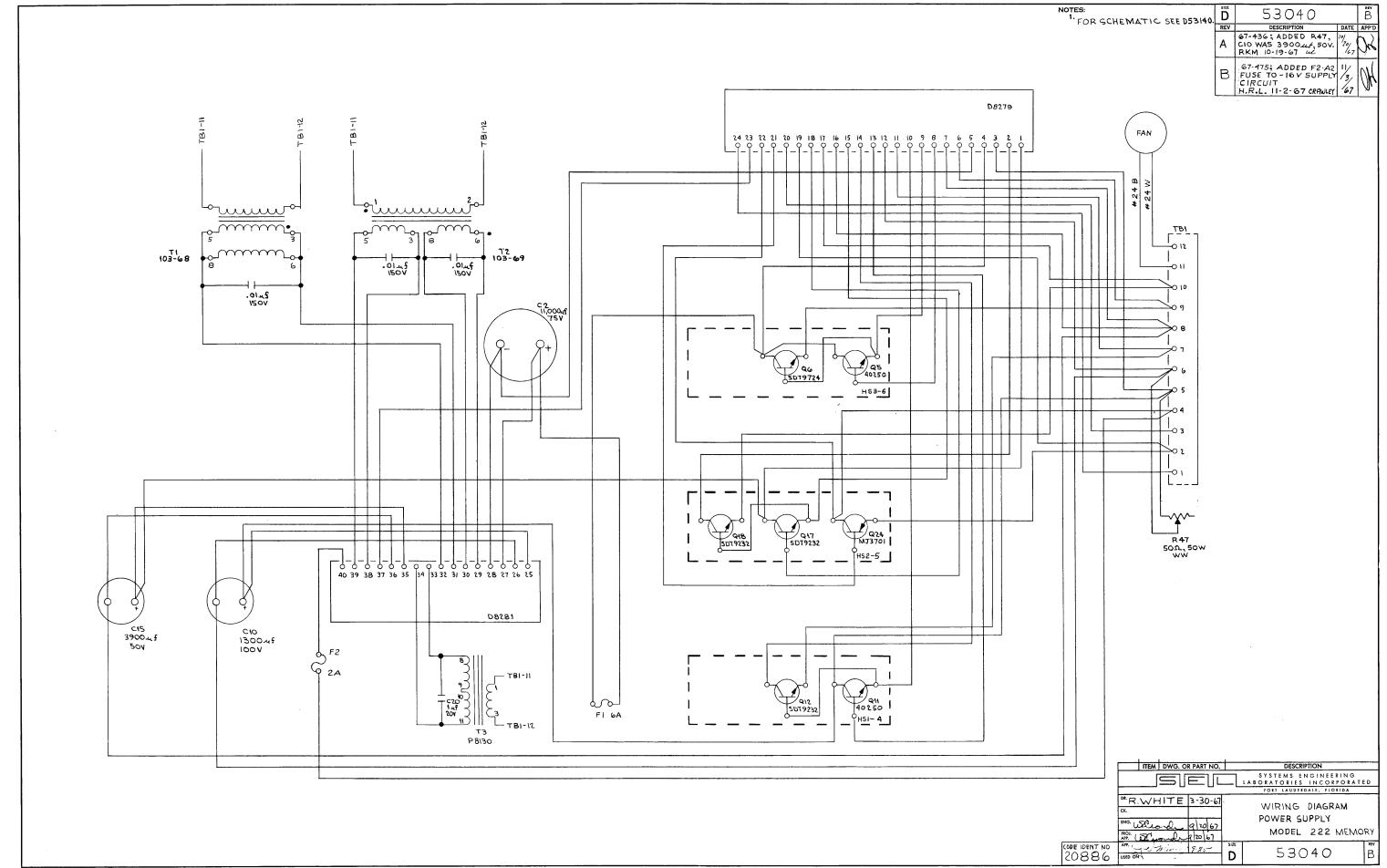
ADDED
J.PINEDO.10-27-67 M.L.

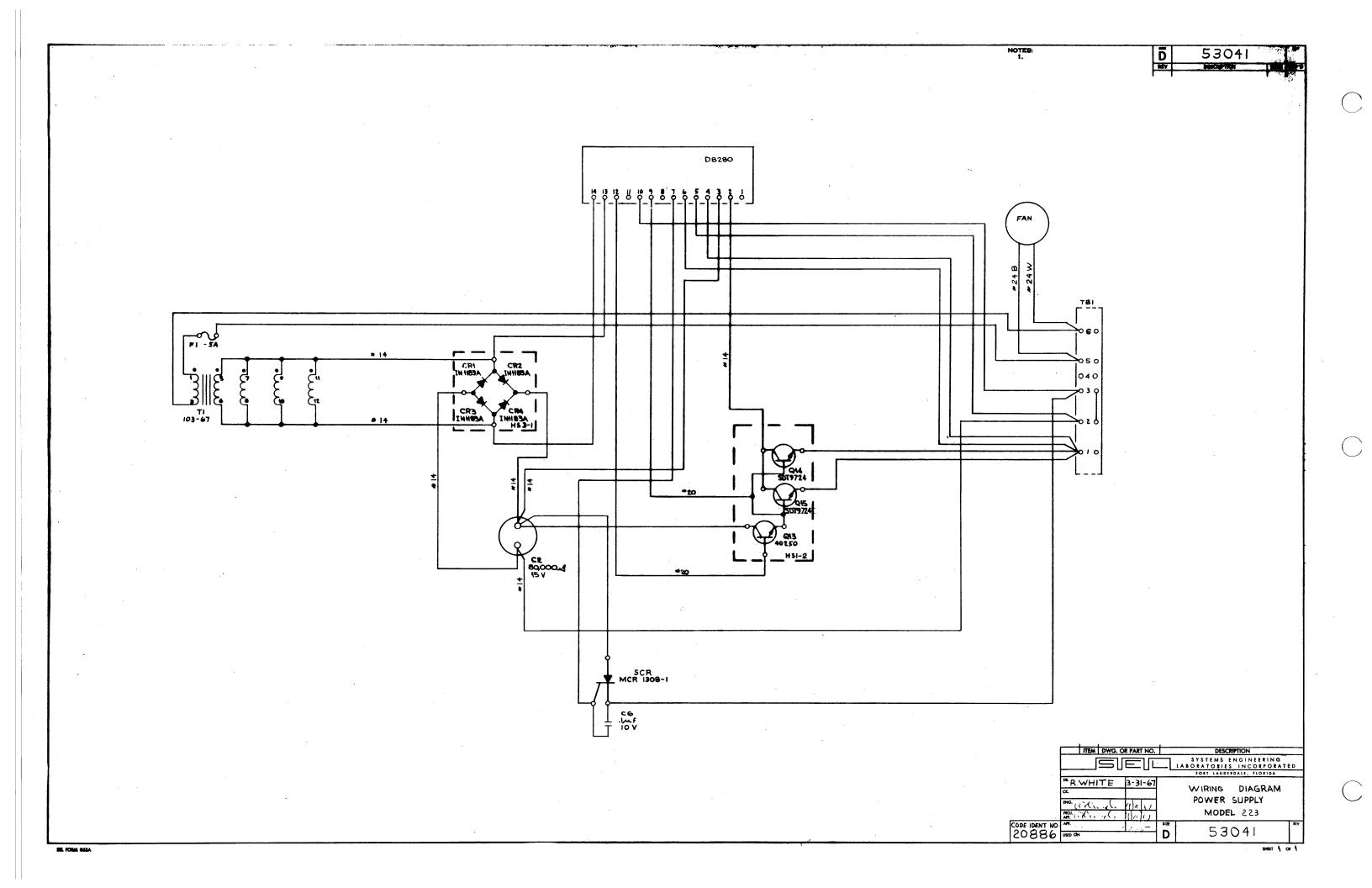
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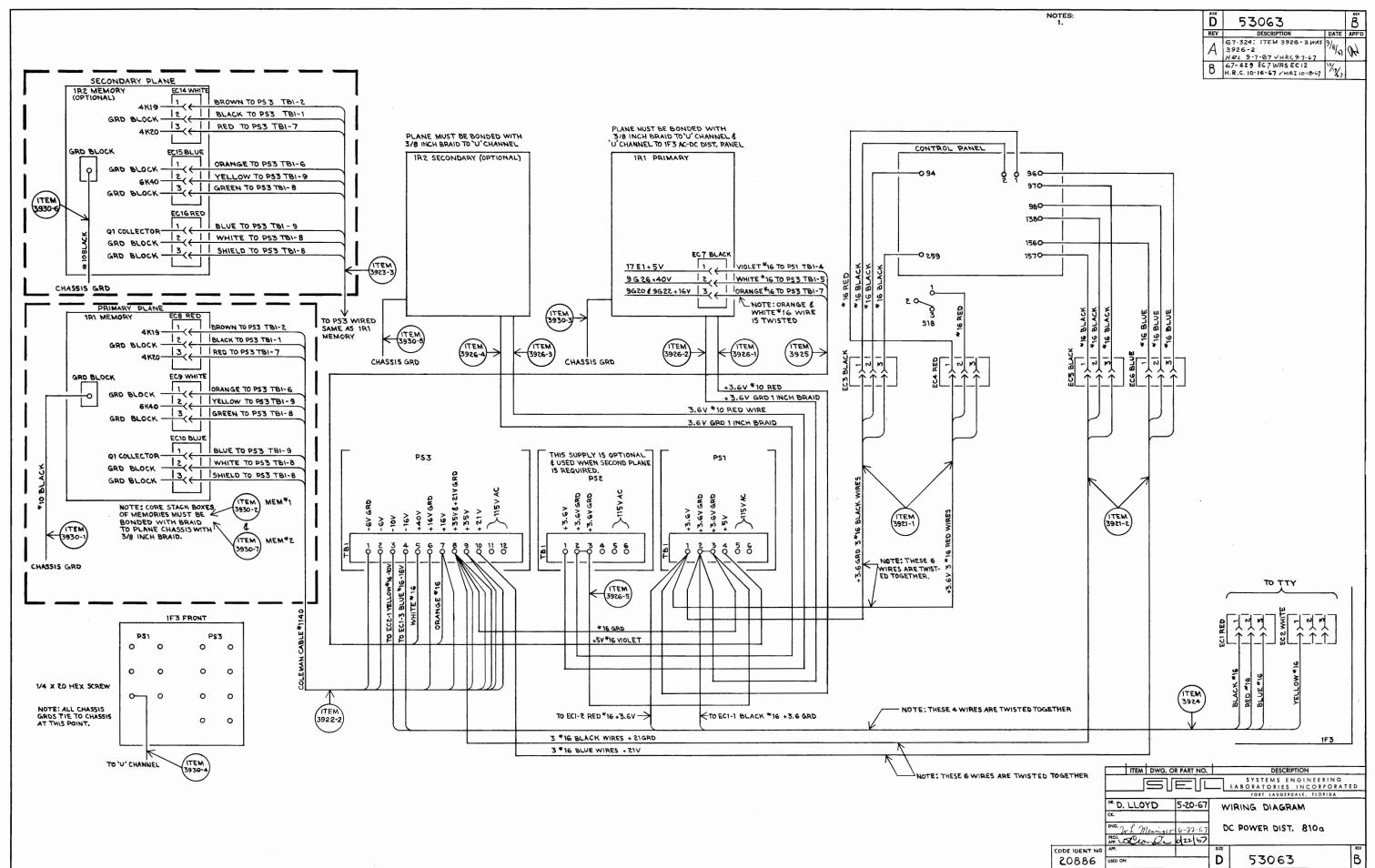
& FORM MAN

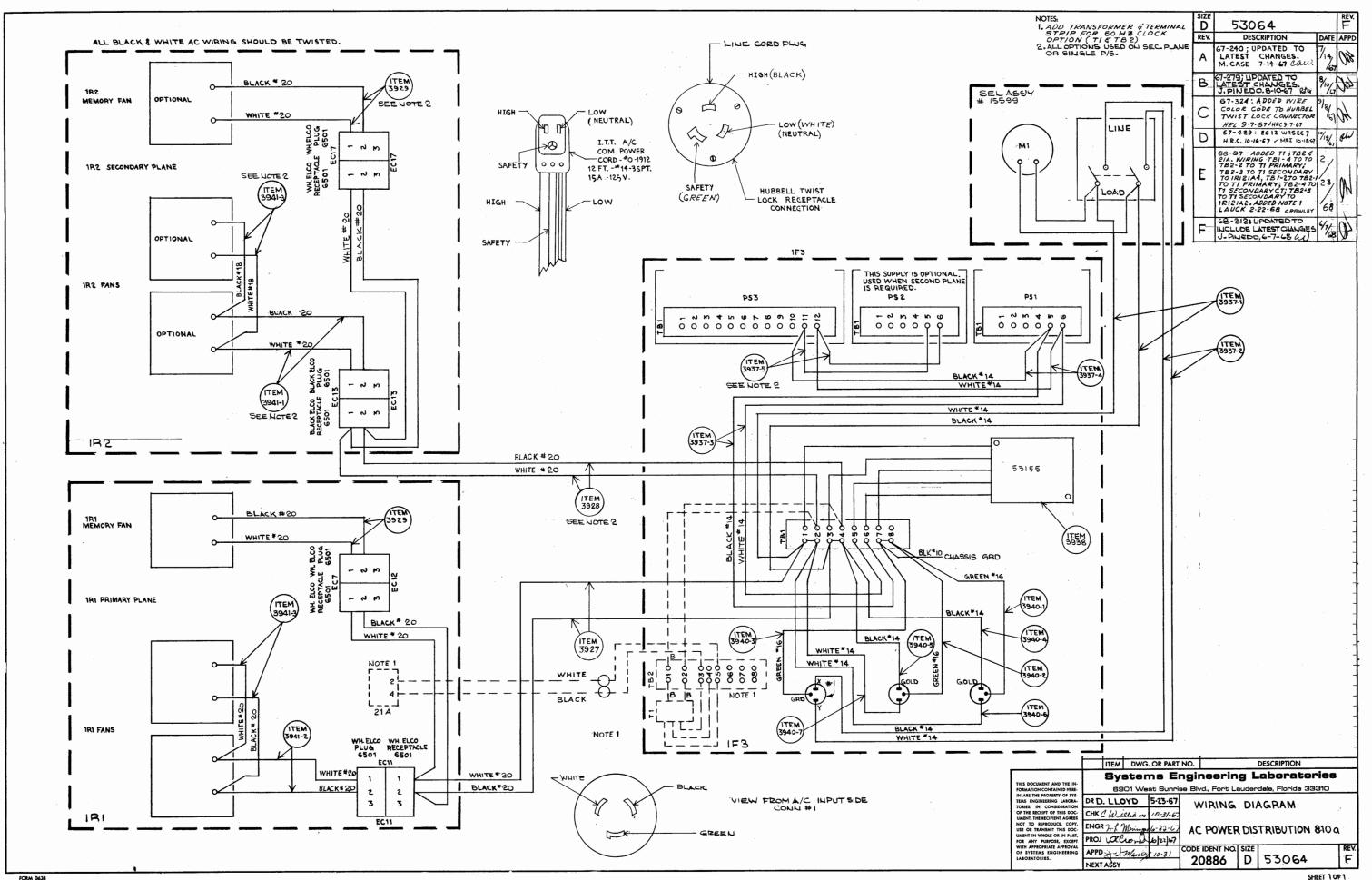












NOTES: 1. #22 RED/BLACK TWISTED SOLID WIRE.

D	Ď	53072			
	REV	DESCRIPTION	DATE	APP'D	
	۷	67-198 UPDATED TO LATEST CHANGES C.PRUNA 6-19-67 CQW.	6/23/	Olg	
	В	68-199; P12 WAS P14, P13 WAS P15, & P14 WAS P16 Was thunk 4.19.68 RKM	4/9/8	()\(\frac{1}{3}\)	

BTC *1 P3	BTC #2 PIZ	BTC*3 PI3	BTC [#] 4 Pl4
Ø (8257) ASU+	φ BSU+ (10) (φcsu+	ØDSU+ (101)
GRD. (201 UNIT CONNECTED	Ø BUL + (102)	φcul+ (201 (102 (102 (102 (102 (102 (102 (102	φDUL+ (102)
GRD (2024 IN/ØUT	ØBDR+ (2024)	ϕ CDR + $\langle 202\langle 1 \rangle \langle 103\langle 1 $	ØDDR+ (103 (
ØRD (203 UNIT D/H	Ø BUD+ 2034	\$\frac{\zero}{\zero} \frac{\zero}{\zero} \frac	φDUD+ (203)
GRD. O (204 UNIT D/A	Ø BUA+ (105)	ØCUA + (204)	ØDUA+ (105)
Ø(82GI) AUQ + CONCENTION OF CONCENTRATE OF CONCENTR	Ø BUQ+ 2055	φcuq + (205)	\$DUQ+ \(\frac{\chi_{\chi\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi_{\chi_{\chi_{\chi_{\chi\tiny{\chi_{\chi_{\chi\tiny{\chi_{\chi\tiny{\chi_{\chi\tiny{\chi\tiny{\chi_{\chi_{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tin\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny{\chi\tiny\tiny{\chi\tiny{\chi\tiny{\chi\tiny\tin\tiny{\chi\tin\tii\tiny{\chi\tiny\tin\tin}\chin\chi\tiny\tin\tin}\chi\tiny\tin}\chinti\tint}\chi\tin\chi
Ø(8260) ACA + (1074 TCWA	φ BCA+	ØCCA+ (107)	ØDCA+ (107)
Ø(8262) AMT + (1084 MAX RATE	φ BMT+ (207)	φCMT+ (207)	\$\frac{1}{207}\cdot \frac{1}{207}\cdot \frac{1}{207
(53022) AT2 +	BT2+	CT2+ (208)	DT2+ (109)
(8261) AFS + (1104 FAST UNIT	ØBFS+ (110)	ØCFS+ (110 < 1	ØDFS+ (110)
\(\)	(11)		\(\begin{align*}
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(8262) ADU + (301 BTC D/H	BDU + (301 (401 (401 (401 (401 (401 (401 (401 (4	CDU+ (301 (DDU + (30) (1
(8262) AGP+ 302< STC GATE DATA IN	BGP+ (302<	CGP+	DGP +
(8257) AUC + 303 DISC ØNNECT	Buc + (303(403)	CUC+ (303 </th <th>DUC+ (303<!--</th--></th>	DUC+ (303 </th
(8260) AIV + (304 INST. RCVD	BIV+ (304<)	CIV+	DIV +
(8258) AER +	BER+ (405)	CER + (305 (1)	DER+ (305)
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→ ⟨307⟨- → ⟨407⟨-	407	\(\frac{1}{307} \) \(\frac{1}{307} \)	
(40 0)	(308 	(308)	<308 (1) 408 (1) 408 (1)
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- (3 (- - - - - - - - -	\(\) \(\	(3)1 (4)1 (4)1 (4)1 (4)1 (4)1 (4)1 (4)1 (4	- <311
412	412	4/2	412
<u> </u>	None des des vend	Name and Asset	

	ITEM DWG. OR PART NO.			. DESCRIPTION			
				SYSTEMS ENGINEERING LABORATORIES INCORPORATED			
				FORT LAUDERDALE, FLORIDA			
	MAL SCHECKLER	AL SCHECKLER 4-26-67		WIRING DIAGRAM	и		
	CX.		BTC TO UNITED 3				
	ENG. & Villents.	6/8/67					
	M. Calcond	6/18/67		TOR			
CODE IDENT NO.	APP.		SIZE	53072	IEV D		
20886	USED ON 9126		D	35072	D		

NOTES: 1. #22 RED/BLACK TWISTED SOLID WIRE. 53072 D DESCRIPTION DATE APP'D LATEST CHANGES 68-199; PIS WAS PIT, PIG WAS PIB, PIT WAS PI9, \$ PIB WAS P20 M. Bruck 4.19.68 RKM 68 ITEM DWG. OR PART NO. DESCRIPTION SYSTEMS ENGINEERING LABORATORIES INCORPORATED DR.AL SCHECKLER 4-26-67

			BTC#7				BTC*
	ØGSU+-	0	(0)		ØHSU+ -	0	(101
	-	-	(201		-	0	(201
	\$GUL+ -	0	(102		Ø HUL+ -	0	102
	_	-	(202		_	0	- < 202
	ØGDR + -	0	(03(\$ HDR+ -	-0	(103
	_	-	(203		5	-	203
	ØGUD+ -	0	(104		Ø HUD+ -	0	104
	_	-5	(204(to the same of the	_	-	(204
	ØGUA+ -	0	(105		Ø HUA + -	0	(105
	-	7	205		-	7	(205
	øguq+ -	0	(106	The section	Ø HUQ+-	0	106
	P GOQ +	7	1 (206		- HOG .	3	1 206
,	¢GCA+ −	0	(107		ØHCA+-	0	(107
	WOCA+	7	207		PHCA.	7	(207
	don't -	0	108		Øнмт+-	0	(108
	ф GМТ+ -	7			SHIMIT	7	208
		0	(208		LITO	0	109
	GT2+ -	7			HT2+-	7	
		0	(209		4	0	1 (209
The second	ØGFS+ -	Y	110		ØHFS+	7	1(110
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		9	(113		2	Y	1(113
	-	0	213			0	213
	GDU+	9	(301		HDU+	9	(301
	-	0	(401		-	0	401
	GGP+ -	9	⟨302⟨	The state of the s	HGP+	9	302
	-	0	(402		-	0	402
	GUC+ -	-	(303 ←	The same of the sa	HUC + -	9	₹303
		0	(403		_	0	403
	GIV+ -	0	(304←		HIV+ -	0	- 304
	-	-	404		-	0	404
	GER+ -	0	(305		HER+ -	-0	(305
	-	-	(405	125.0	-	-	405
	-	0	(306		-	0	- 306
	-	-	(406		12 11 11	0	406
		0	(307€		<u> </u>	0	307
	_	-	(407		-	-	407
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		0	310			0	(310
		7	(410		4 3 3 1	5	(410
	3	0	(311 (_	0	(311
		7	(411		4 -	7	(411
		0	312			0	(312
		X	1412			7	412

WIRING DIAGRAM BTC TO UNIT# 5,6,7,8 ENG. & Villant 100 POLITICAL 6/8/67

CODE IDENT NO APP.

20886 USED ON 9126 50 PINCONNECTOR 53072 D

BTC#5

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(408) (309) € (310 €

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(311 (

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302 BTC GATE DATA IN

303 DISCONNECT

304 INST. RCVD

208←

Ø(8257) ESU +

\$\phi(8257) EUL+

Ø(8257) EDR+

Ø(8261) EUD+

Ø (8261) EUA+ GRD.

\$ (8261) EUQ+

Ø(8260) ECA+

Ø(8262) EMT+

(53022) ET2+

(8262) EDU +

(8262) EGP+

(8257) EUC+

(8260) EIV +

(8258) EER+

GRD.

 (8261) EFS +

GRD.

(IOI (INITIALIZE

CIO2 UNIT CONNECTED

UNIT D/H

UNIT DA

1006 UNIT DATA REQUEST

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MAX RATE

103 IN/ØUT

BTC #6 PI6

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(2016

1/1024

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-< N3 ←

(2064

-K108

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ØFCA+

1F3-P1 1F3-P1 713 CI5(8225) 812> ⊖ GRD. UNIT DATA ACCEPTED → ØUDA + COMMAND INST CEU+18216) < 118€ → GRD →øusR+ O TEST INST. TIN.+ TEU(8216) UNIT TEST RETURN →øutr DATA INST. DIN+ H17(8217) 2 MASTER CLEAR 00 -ICB(**8232**) -I/Ø+18202 DATA BIT 15 IN/OUT 708) --- D15+ (8201) O DATA BIT 14 IN/OUT 707)806) DATA BIT 13 IN/OUT D13+ (8201) - D12+ (8201) → GRD. DATA BIT 11 IN/OUT)804) - D11+(8201) ⊖^{GRD.} DATA BIT 10 IN/OUT - D10+(8201) DATA BIT 9 IN/OUT - D9+(8201) ⊖ GRD. DATA BIT 8 IN/OUT - D8+(8201) UNIT NO. BIT 16 -MACH. BIT II U16+ ØIII1 COMPUTER DATA ACCEPTED CDA+ (S058)+111 CDA(8225) UNIT NO BIT8-MACH BIT 12 U8+ ØII12 COMPUTER DATA HERE CDH+ CD € CDH18225) (2028)+21[-UNIT NO BIT4 - MACH BIT 13 5 6 7 → T8+T 16 (TIMING) ·GGA(8216) (2028)+211)210] ⊖ GRD. UNIT NO. BIT 2 -MACH. BIT 14 ion(WAIT FLAG (5058)+411 UNIT NO. BIT 1-MACH. BIT 15 → GRD Ø1115 INPUT INSTRUCTION GRD. -CCB(8216) (5058)+711 DATA BIT 7 IN/OUT (1058)+7D PARITY BIT FROM UNIT >607> | GRD. DATA BIT & IN/OUT DG+(8201) NOTES: PARITY BIT TO UNIT LOP+(8248) O GRD CODE IDENT. NO. 20886 DATA BIT 5 IN/OUT - D5+(8201) PARITY CHECK REQUEST PRU+(8248) MR. SCHECKLER 4

GC, WELLIAMS

PRO JOHNSON DE CONTROL

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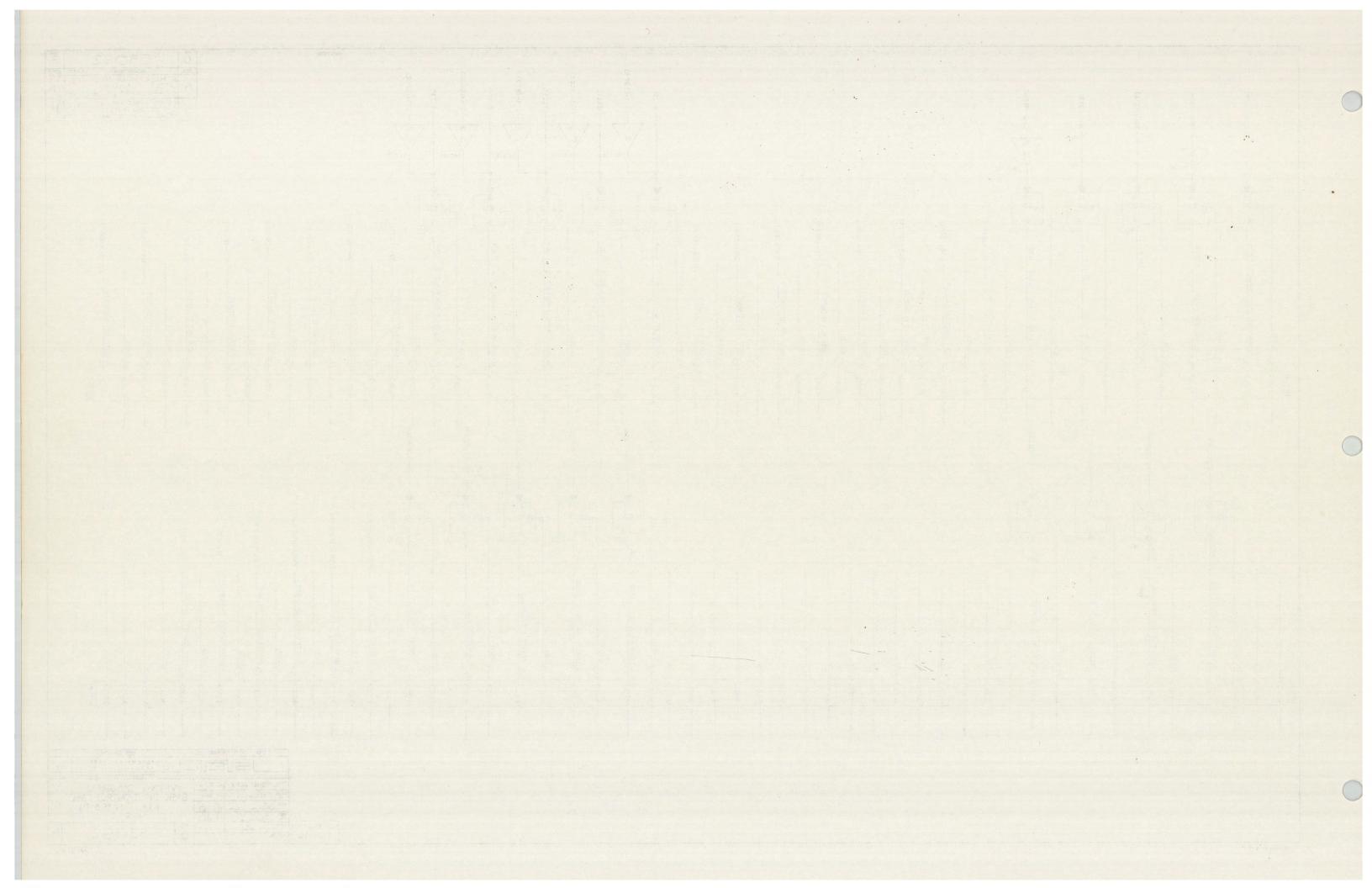
MRO DE → GRD. DATA BIT 4 IN/OUT PARITY ERROR TO UNIT PEU + (8248) ⊖ GRD. M DWG, OR PART NO. DATA BIT 3 IN/OUT - D3+(8201) UNIT OUTPUT INTERRUPT (8242) 4-27-67
10-31-67
120 67
120 67 504> OGRD.)203> DATA BIT 2 IN/OUT σ WIRING DIAGRAM
BASIC INPUT/OUTPUT CABLE
104 PIN 1F3-P1
AMIP UNIT INPUT INTERRUPT (8242) - D2+(8201) O § >503> DESCRIPTION

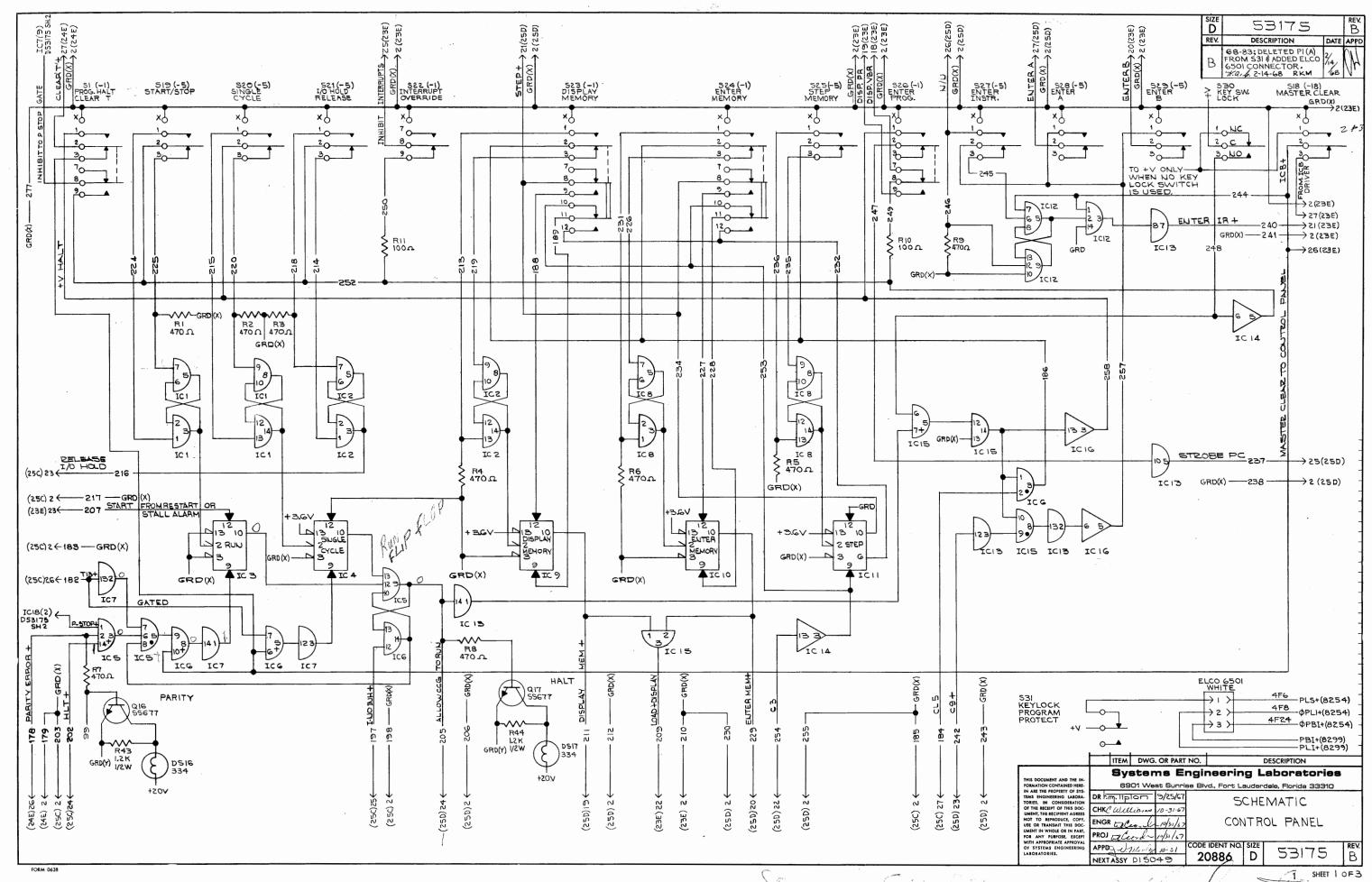
OBSCRIPTION

67-445; UPDATED TO
LATEST CHANGES,
IC.THEW 10-27-67 Q 68-199; ADDED 370mg CAP TO GRD FROM 10C(1) ⊖ GRD. >602> DATA BIT 1 IN/OUT 3092 ⊖GRD. DATA BITO IN/OUT 1F3-P1 1F3-P1 SHEET 37

<u>___</u>

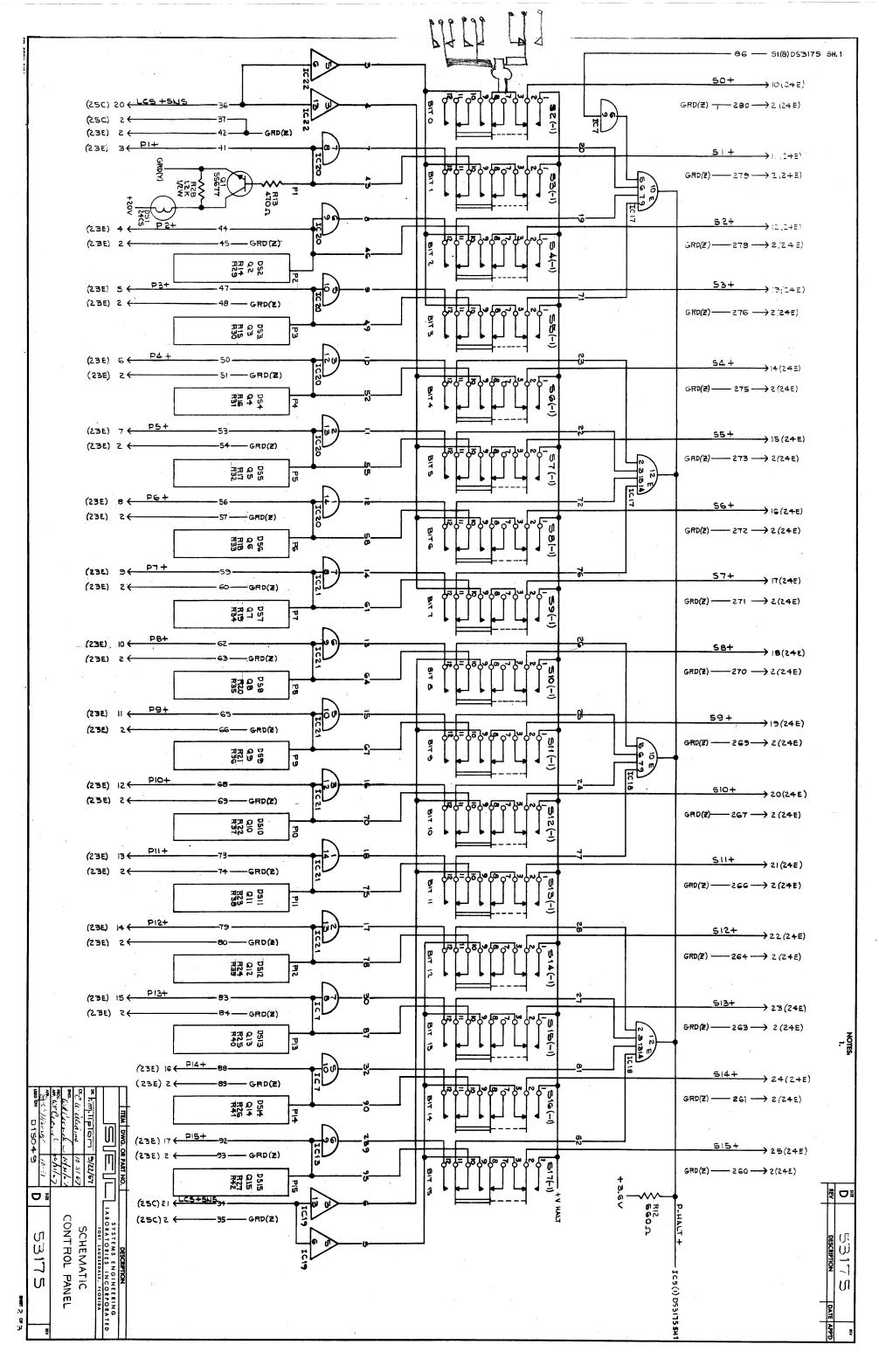
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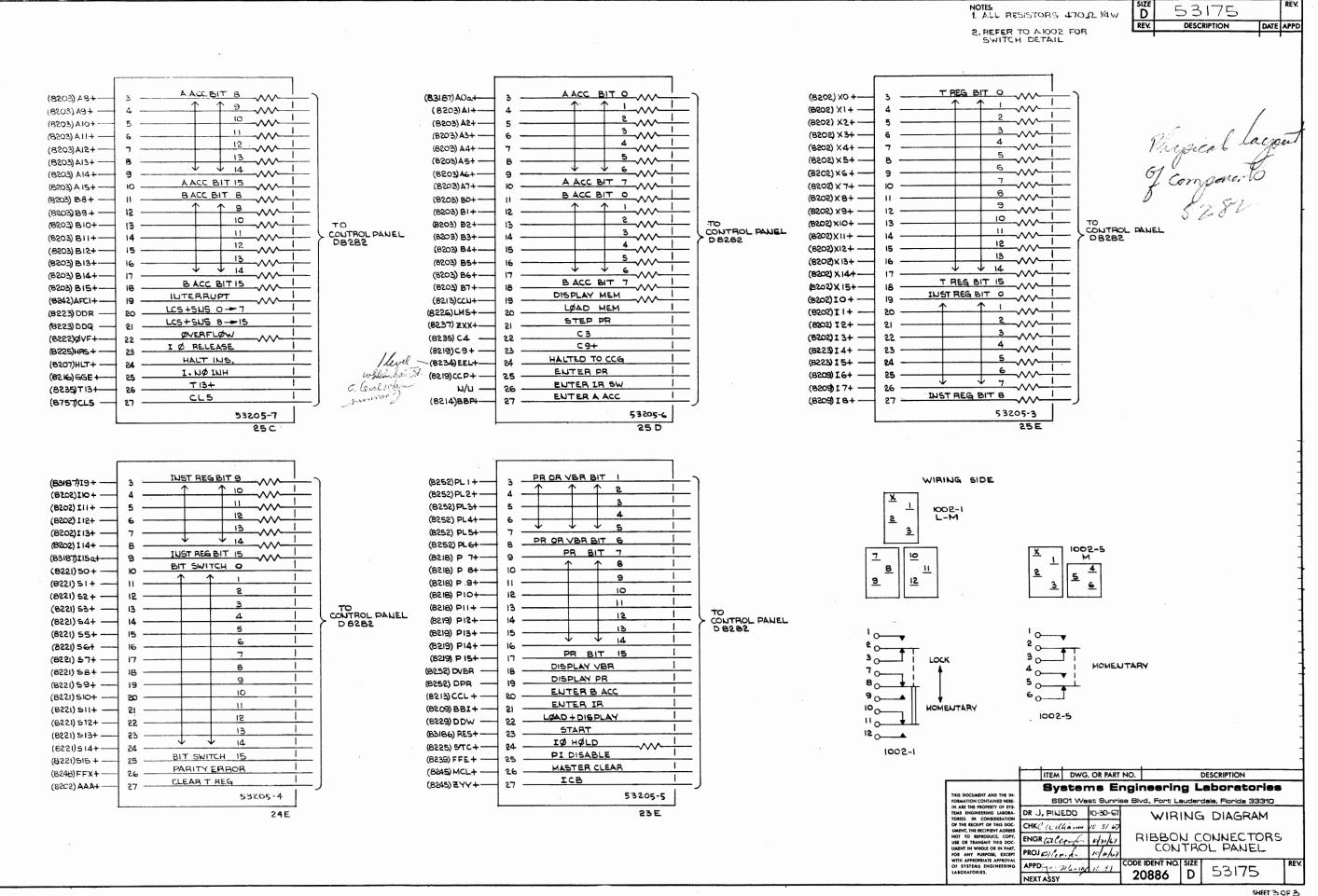




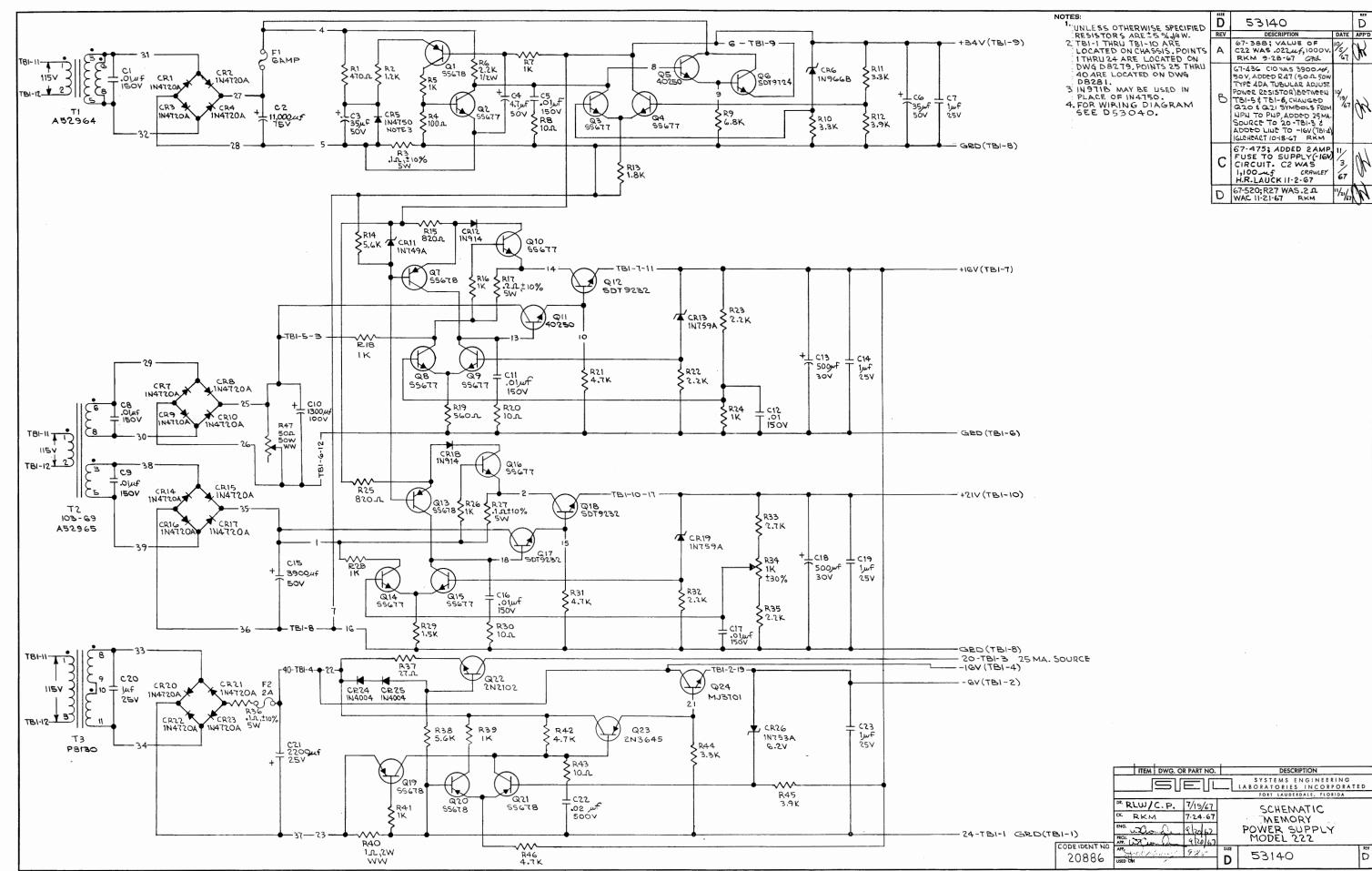
SCE_

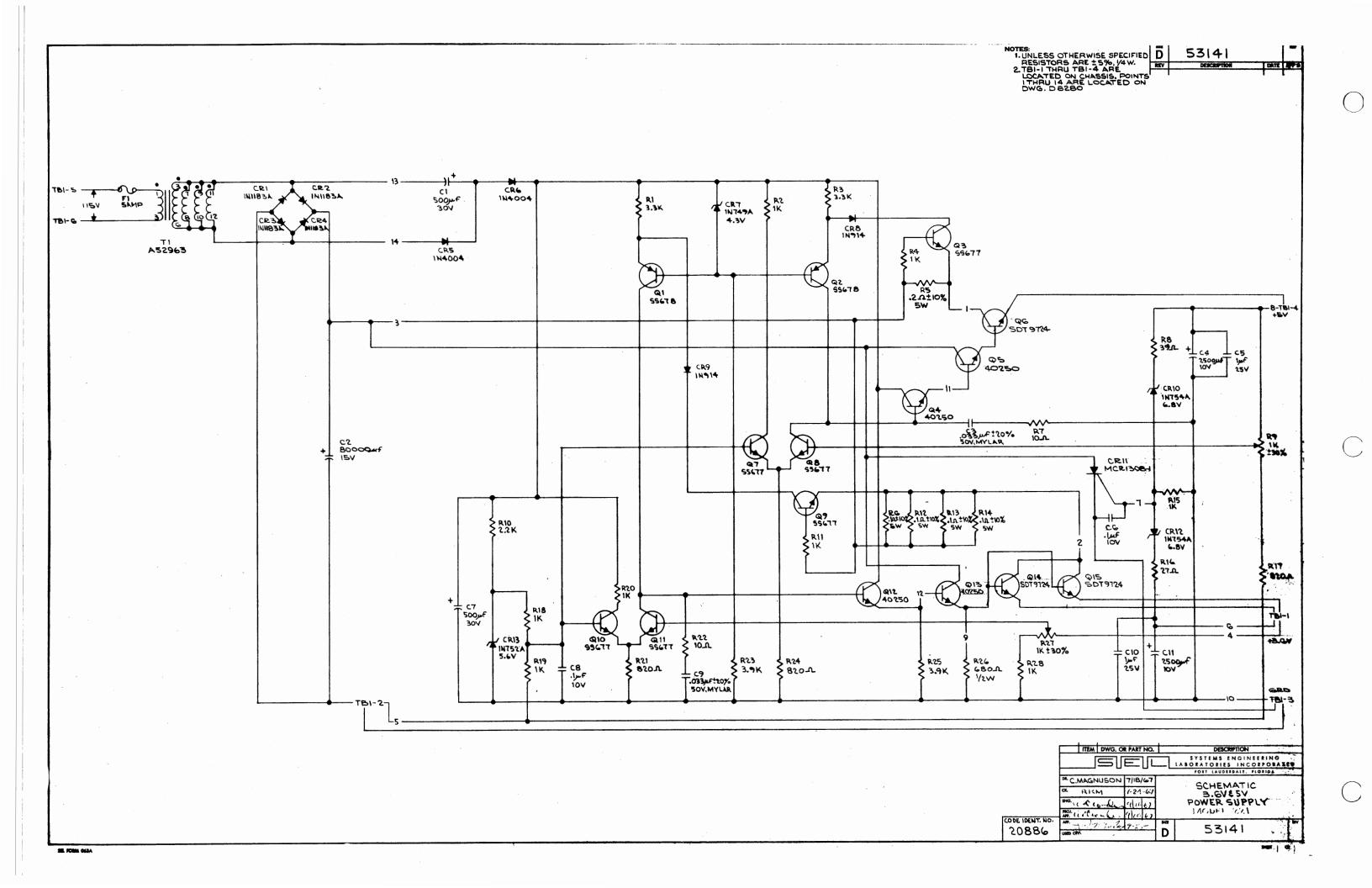
2 er parto la cotton





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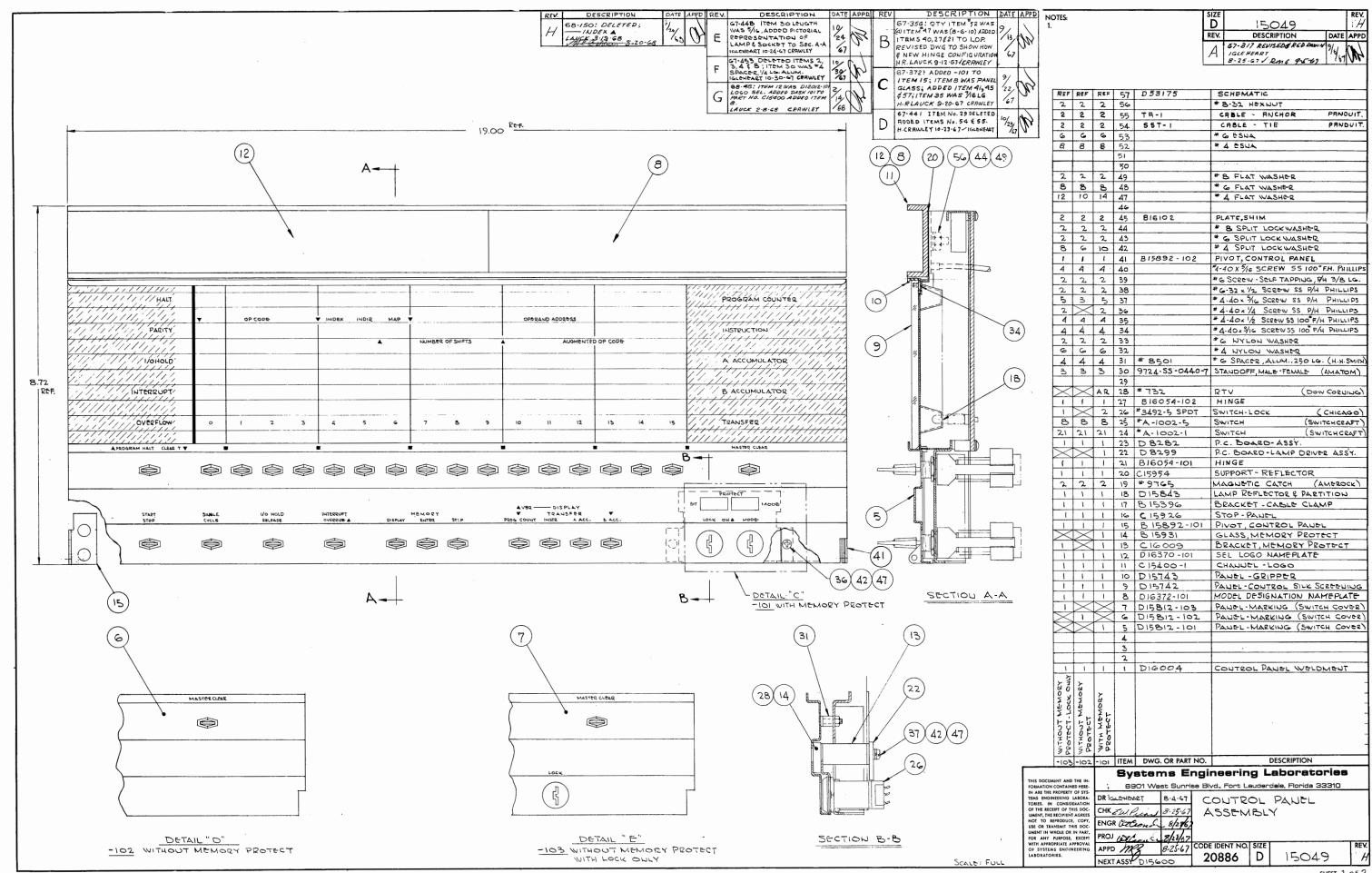


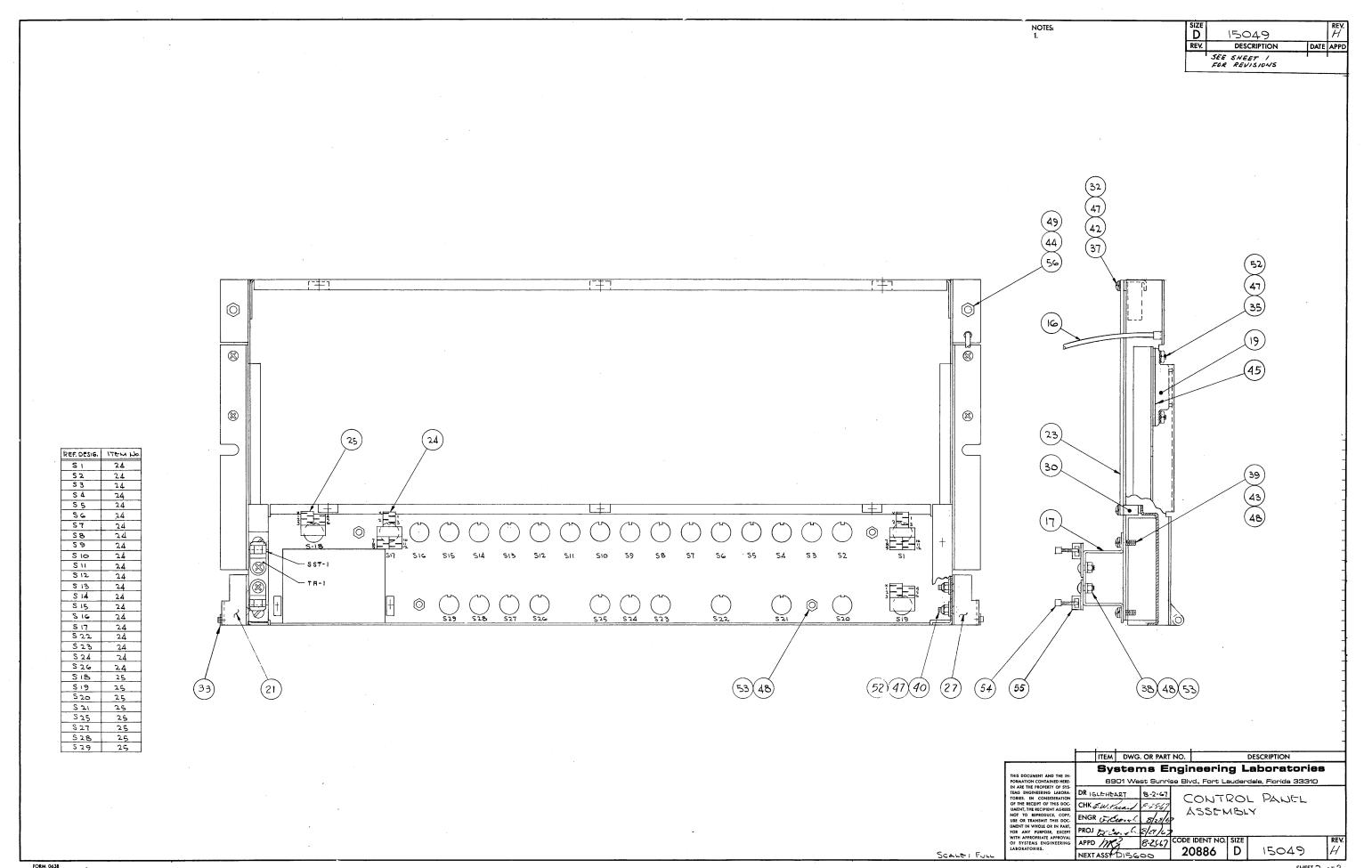


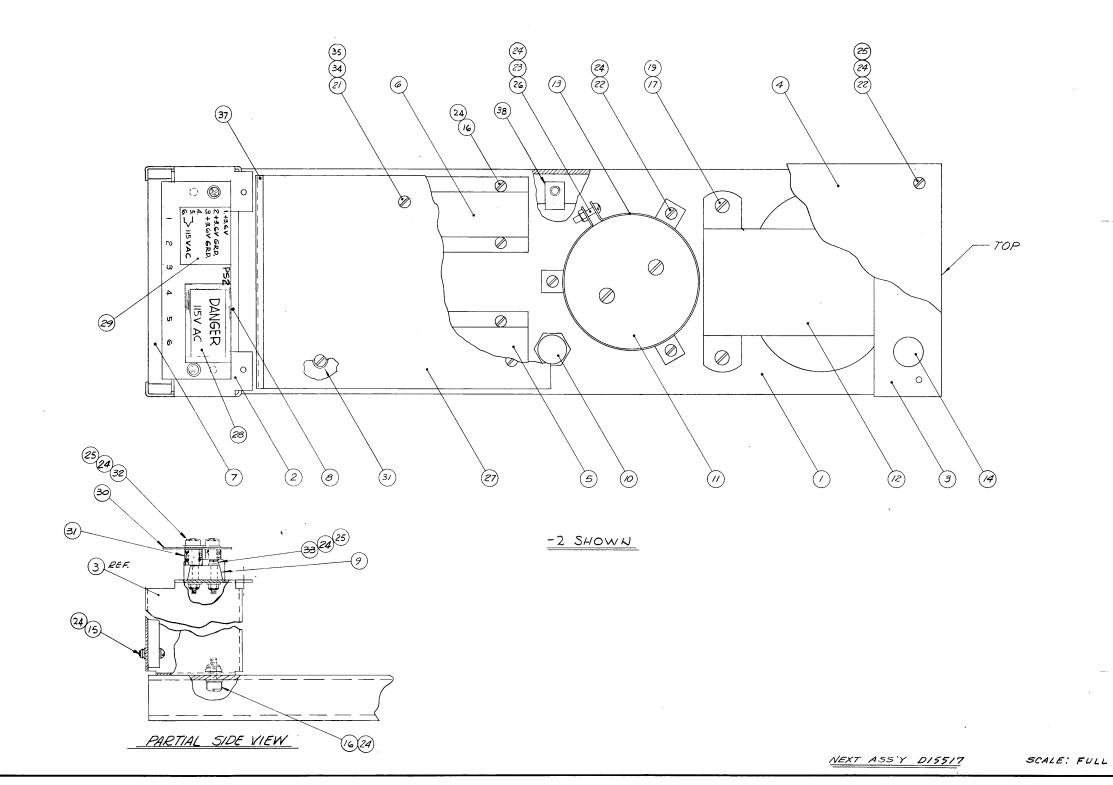
NOTES:
f. UNLESS OTHERWISE SPECIFIED D
RESISTORS ARE ± 5 %, 1/4 W.
2.TBI-1 THRU TBI-4 ARE.
LOCATED ON CHASSIS.
POINTS I THRU 4 ARE.
LOCATED ON DWG D8280 53142 DESCRIPTION DATE APPO CR6 IN4004 Ćί 500μf 30∨ INII83A CR2 R16 27_2 TB1-5 AEBIINI CR 12 IN 754A 6.8V T FI ŞRΙ CR3 CR4 INII 83A 3.3K TB1-6 **~~~** RI5 IK TI 852963 CR5 10V IN4004 CR 11 MCR 1308-1 R5 .1.0.±10% 5W 60000 MF CR9 IN914 RII \$ RIO Q9 5**56**77 Q1 55678 CR7 IN749A 4.3V +1 T500Mf 30V \$ R I B \$ R20 Q15 SDT 9724 Q14 L 40250 Q13 40250 Q10 55677 CR13 IN752A 5.6V TBI-1-6 QII 55677 \$ R23 \$ 3.9K \$1K \$ 10 m ≷R 25 3.9K \$820 T \$R28 \$680₹ \$856 10V V2W LC10 124 25V 2500*u*f ± C9 .033.41±20% 50V, MYLAR -ю-тві-з GRD TBI-2 -5-ITEM DWG. OR PART NO. DESCRIPTION Systems Engineering Laboratories THIS DOCUMENT AND THE INFORMATION CONTAINED RESIN ARE THE PROPERTY OF STSTHAS IS INCHERENCE LAGORATORIES, IN CONSIDERATION
OF THE RECEIFF OF THIS DOCLUMENT, THE RECEIFIERT AGRESS
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USE OF TRANSMITT THIS DOCLUMENT, THE WOOD,
OF MANY PROPOSE, EXCEPT
WITH APPROPRIATE APPROVAL
OF SYSTEMS REGISEERING
LABORATORIES. 6901 West Sunrise Blvd., Fort Lauderdale, Florida 33310 DRC.MAGNUSON 7/19/67 SCHEMATIC CHK RKM 7-24-67 3.67 POWER SUPPLY
MODEL 223
CODE IDENT NO. SIZE
20886 D 53142 ENGR WECCO PROJUTE Q 9/20/67 APPD TIME 1925

FORM 0638





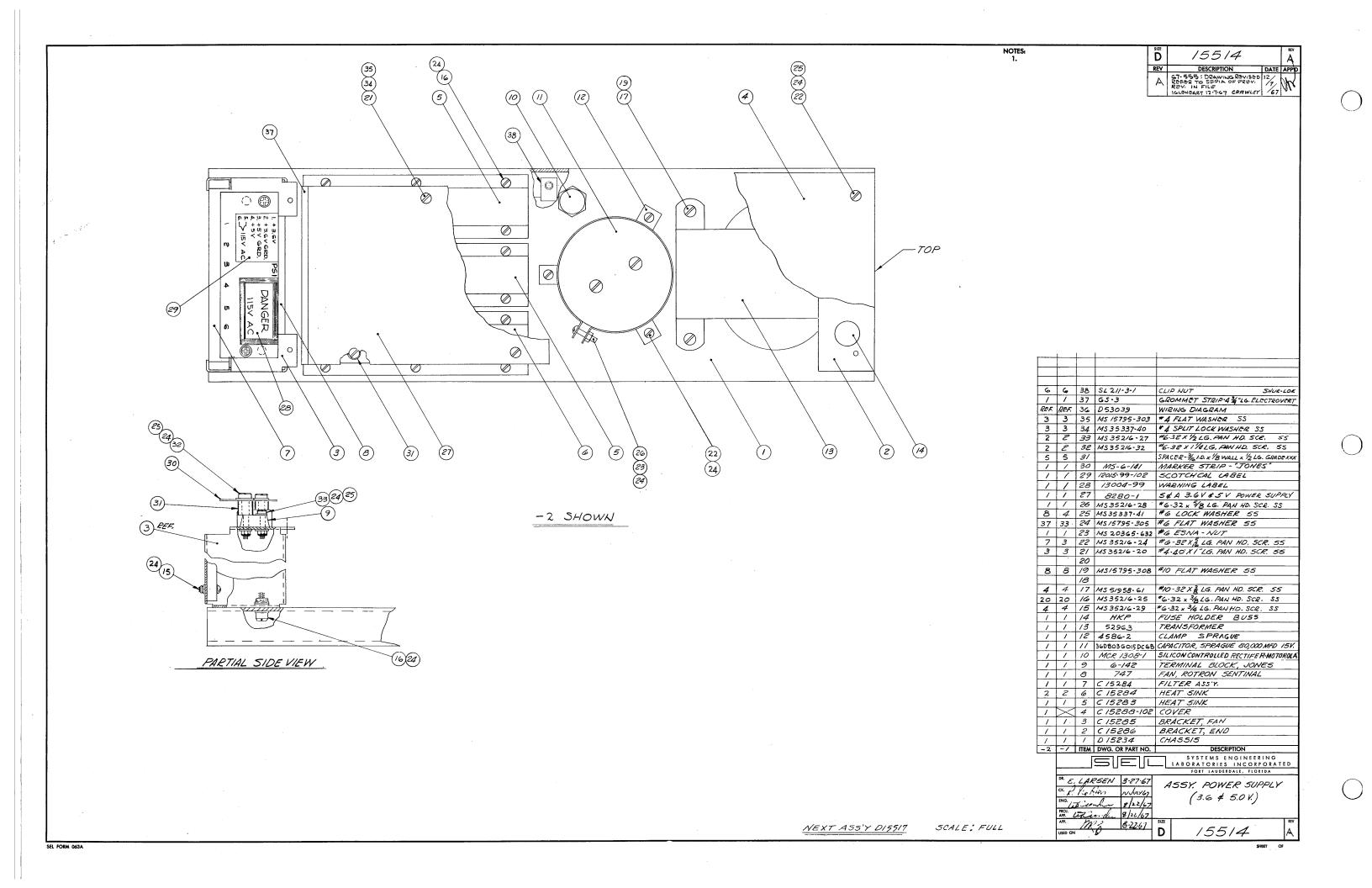


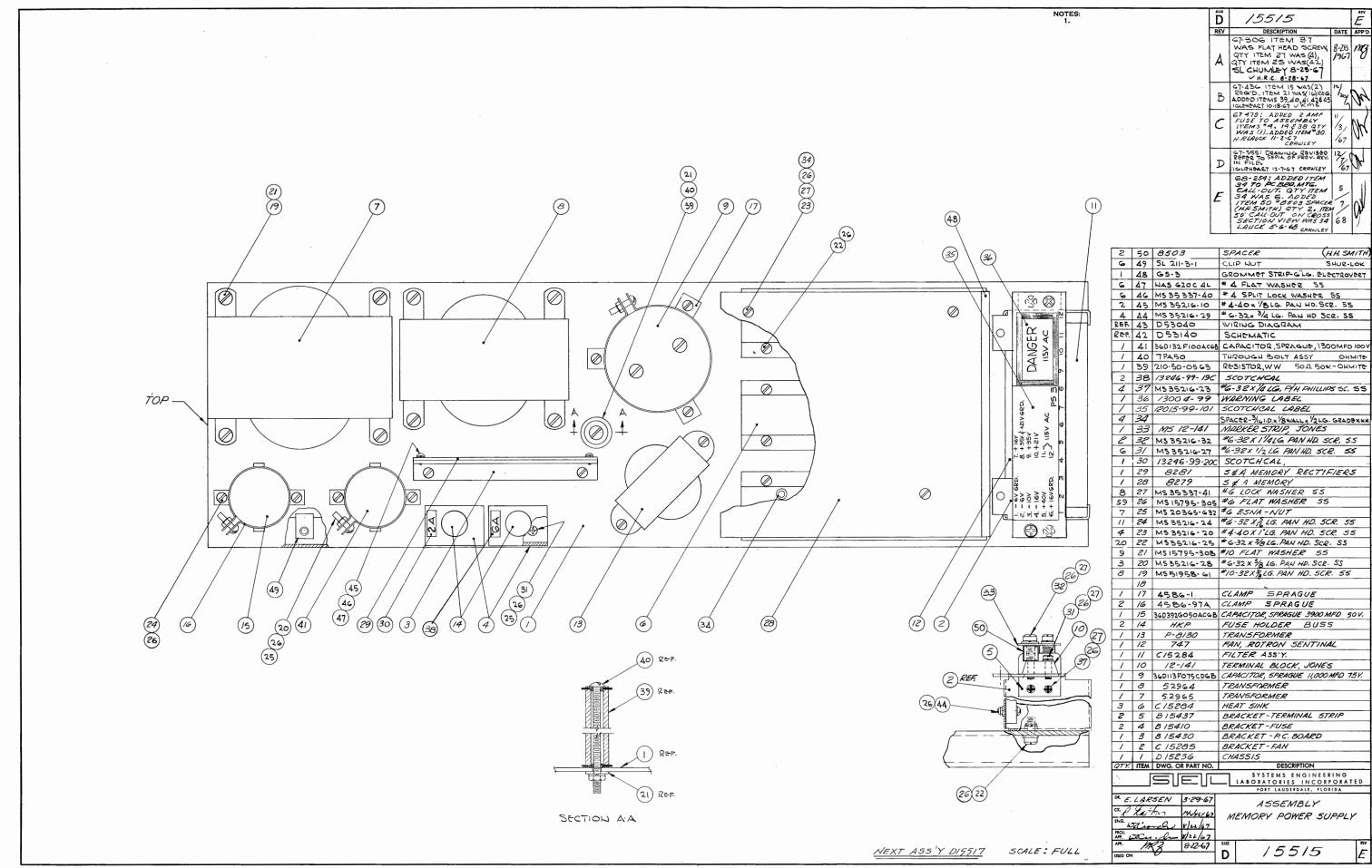


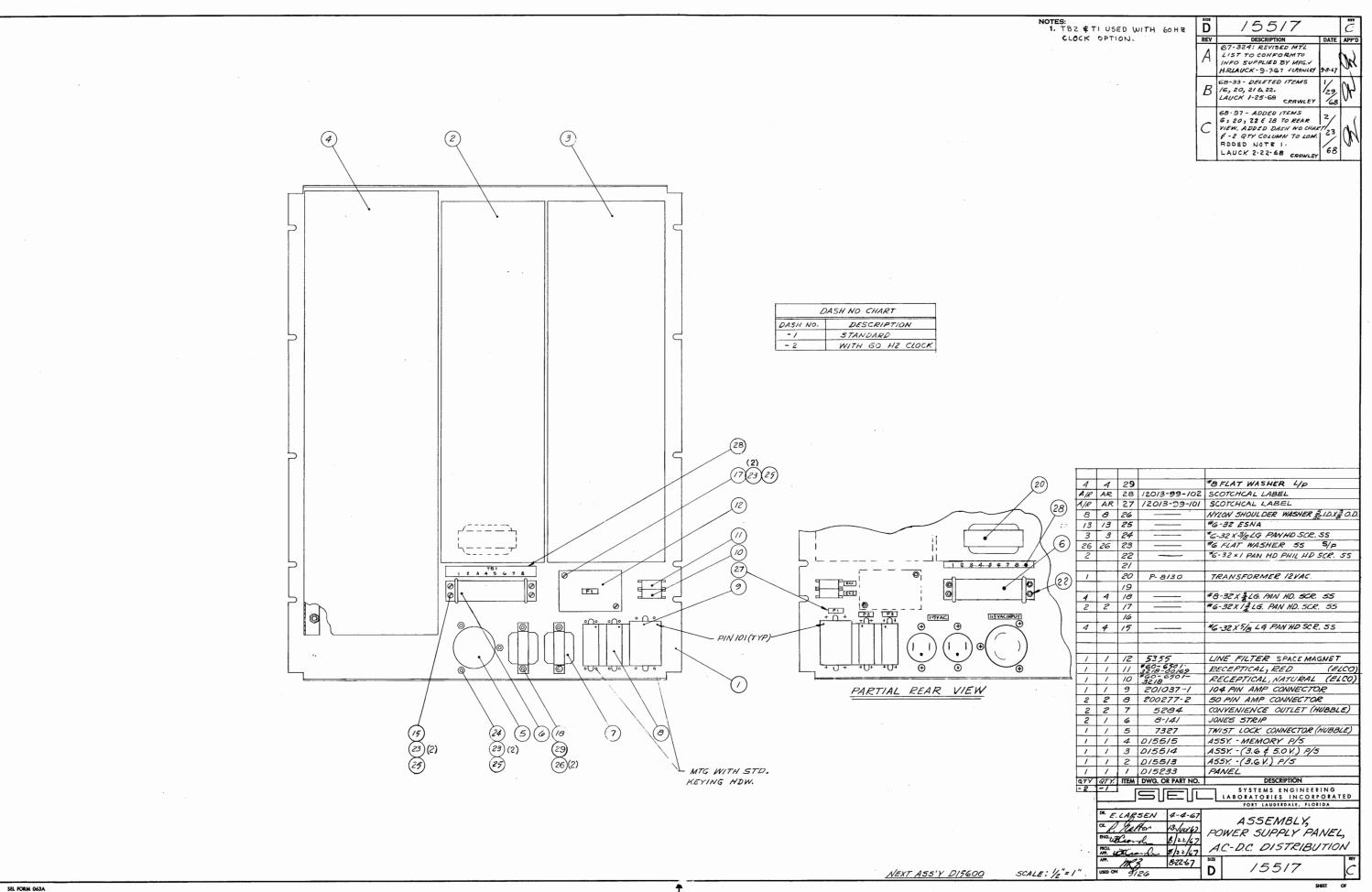
G	ی	38	5L 2//-3-/	CLIP NUT SHUR-LOK
١	Τ,	37	GS-3	GROMMET STRIF 4 % "LG. ELECTROVERT
Rr F	Rt-F.	36	D53041	WIRING DIAGRAM
3	ሳ	35	MS/5795-303	#4 FLAT WASHER SS
3	3	34	MS35337-40	*4 SPLIT LOCK WASHER SS
2	2	33	M5352/6-27	#6-32 X 1/2 LG. PAN HO. SCR. 55
2	2	32	MS 352/6-32	*6-32 X1/4 LG. PAN. HD. SCR. 55
2	2	3/		SPACER- 36 ID.X & WALL X 1/2 LG. GRADEXXX
1	1	30	M5-6-141	MARKER STRIP, JONES
1	/	29	12015-99-103	SCOTCHCAL LABEL
1	/	28	13004-99	WARNING LABEL
/	/	27	8280-2	S&A 3.6V&5V POWER SUPPLY
1	1	26	M5352/6-28	*G-32 x 1/8 LG. PAN HO. SCREW SS
8	4	25	MS35337-41	#6 LOCK WASHER 55
37	33.	24	M5/5795-305	#6 FLAT WASHER 55
1	1		MS 20365-632	#6.ESNA-NUT
7	3	22	MS 352/6-24	#6-32 X \$6 LG. PAN HD. SCR. 55
3	3	2/	MS352/6-20	#4-40 X 1"LG PAN HD. SCR. 55
_		20		
8	8		MS /5795-308	#10 FLAT WASHER 55
		18	7,7,000	
4	4		MS 5/958-6/	#10-32 X & LG PAN HD. SCR. 55
	20		MS 352/6-25	*6-32 x 3/2 LG. PAN HO. SCR SS
4	4	15	MS 352/6-29	"6-32x3/4 LG. PAN HD SCR. SS
1	7	14	HKP	FUSE HOLDER BUSS
7	1	13		CLAMP SPRAGUE
<u> </u>	/	12	52963	TRANSFORMER
7	/	77		CAPACITOR, SPRAGUE BO,000 MFD 15%
<i>-</i>	/	10	MCE-1308-1	SILICON CONTROLLED RECTIFIER-MOTOROLA
7	/	9	6-142	TERMINAL BLOCK, JONES
-/	/	8	747	FAN, ROTRON SENTINAL
7	/		C 15284	FILTER ASS'Y.
 	/	6	C 15284	HEAT SINK
7	,	5	C /5283	HEAT SINK
7		4	C15288-101	COVER
7	$\overline{}$	3	C15286	BRACKET
-	<i>'</i>	2	C/5285	BRACKET-FAN
 	/	7	015235	CHASSIS
-2	-/	_	DWG. OR PART NO.	DESCRIPTION
~		۱,,,,,,		SYSTEMS ENGINEERING
				LABORATORIES INCORPORATED
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		. <i>LAK</i>	PSEN 3-28-67	ASSV DOWER SUDDIV
	CK.	/le/	Hom JULY67	ASSY POWER SUPPLY
	ENG.	E Co	nde 8/22/67	(3.6 V.)
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	APP.	m	3 8-22-67	SIZE REV
	USED ON	/// _\	10.000	D 15513 A
				700/0

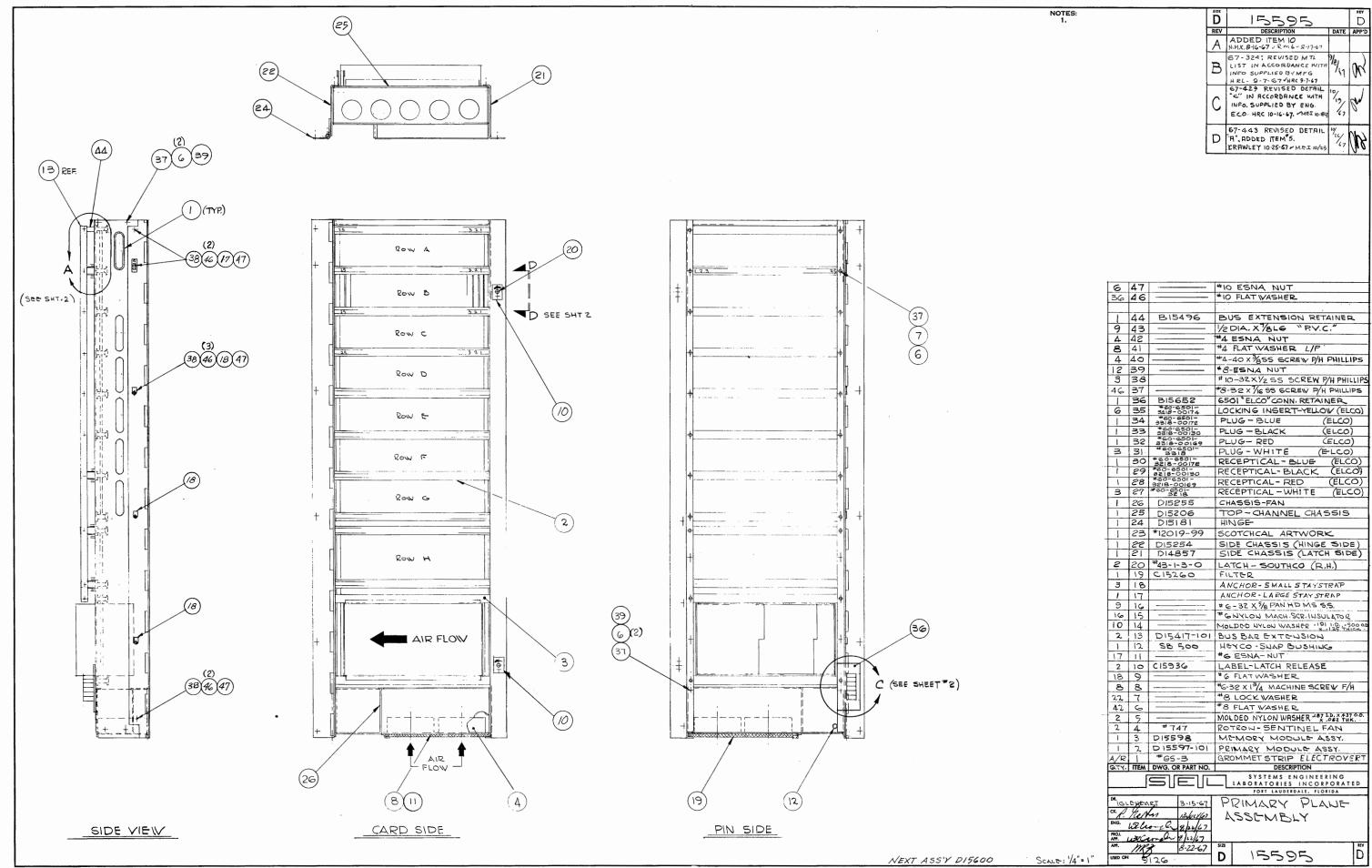
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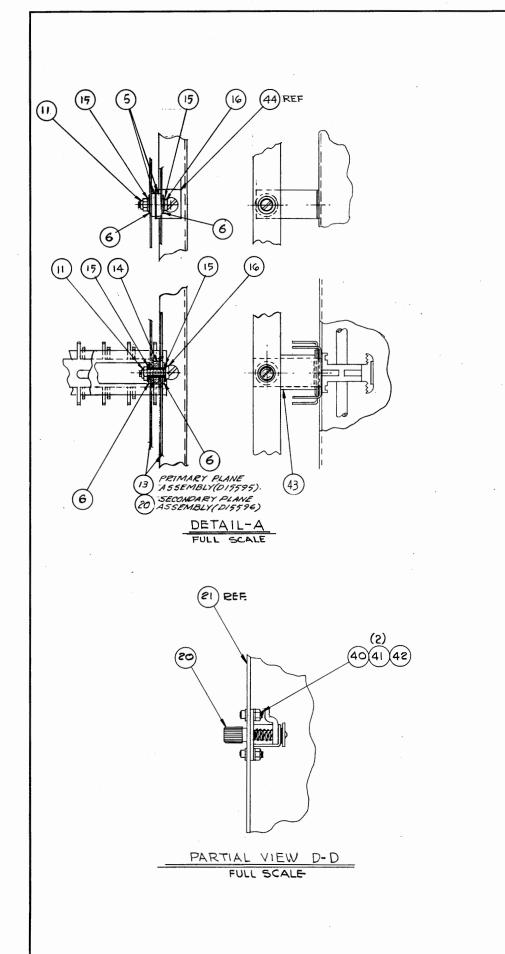
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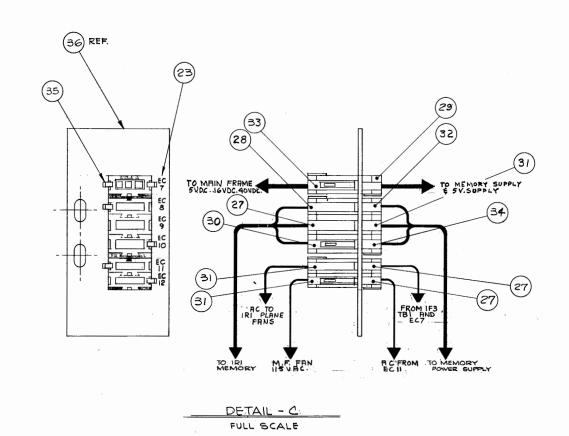










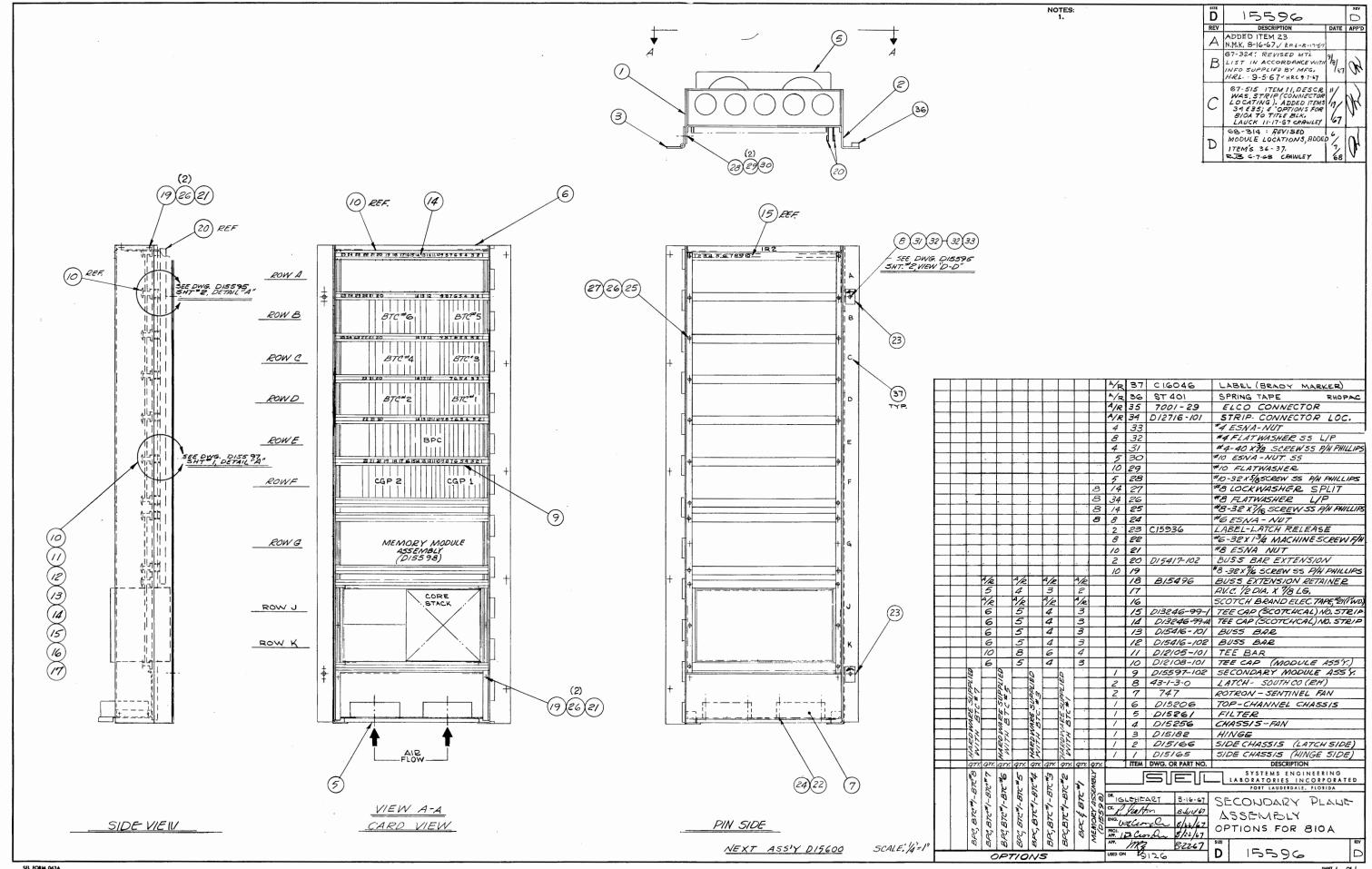


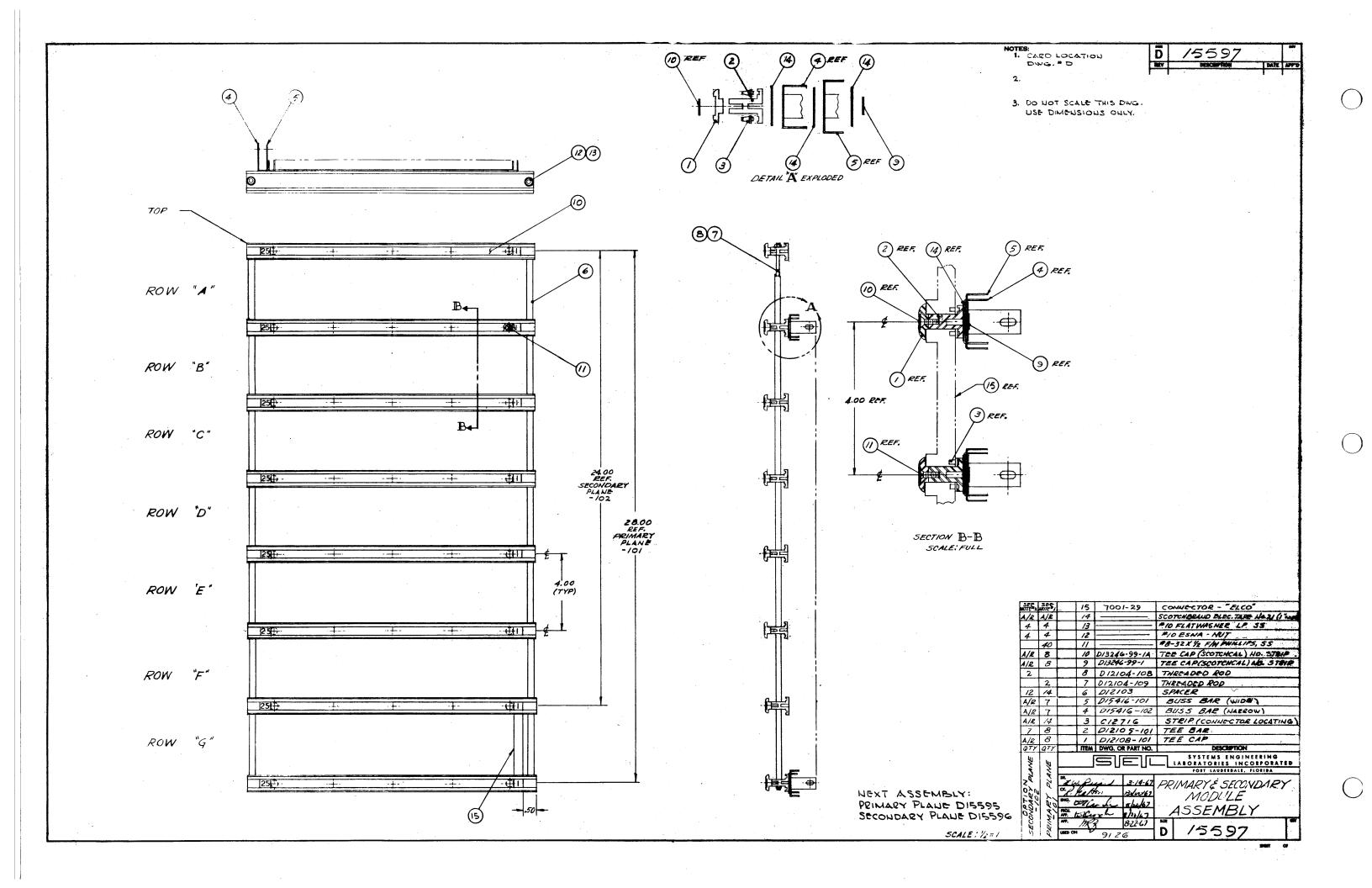
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		LABORATORIES	INCORPORATIONS	E D
DE GLENTART 4.13 CC PLATEN 13-W BNG. DCLOWL 82: ROJECUL 82:	1467 A	RIMARY		
MF. MR 2 8-22	3-67 S128	1559	<u> </u>	D REV

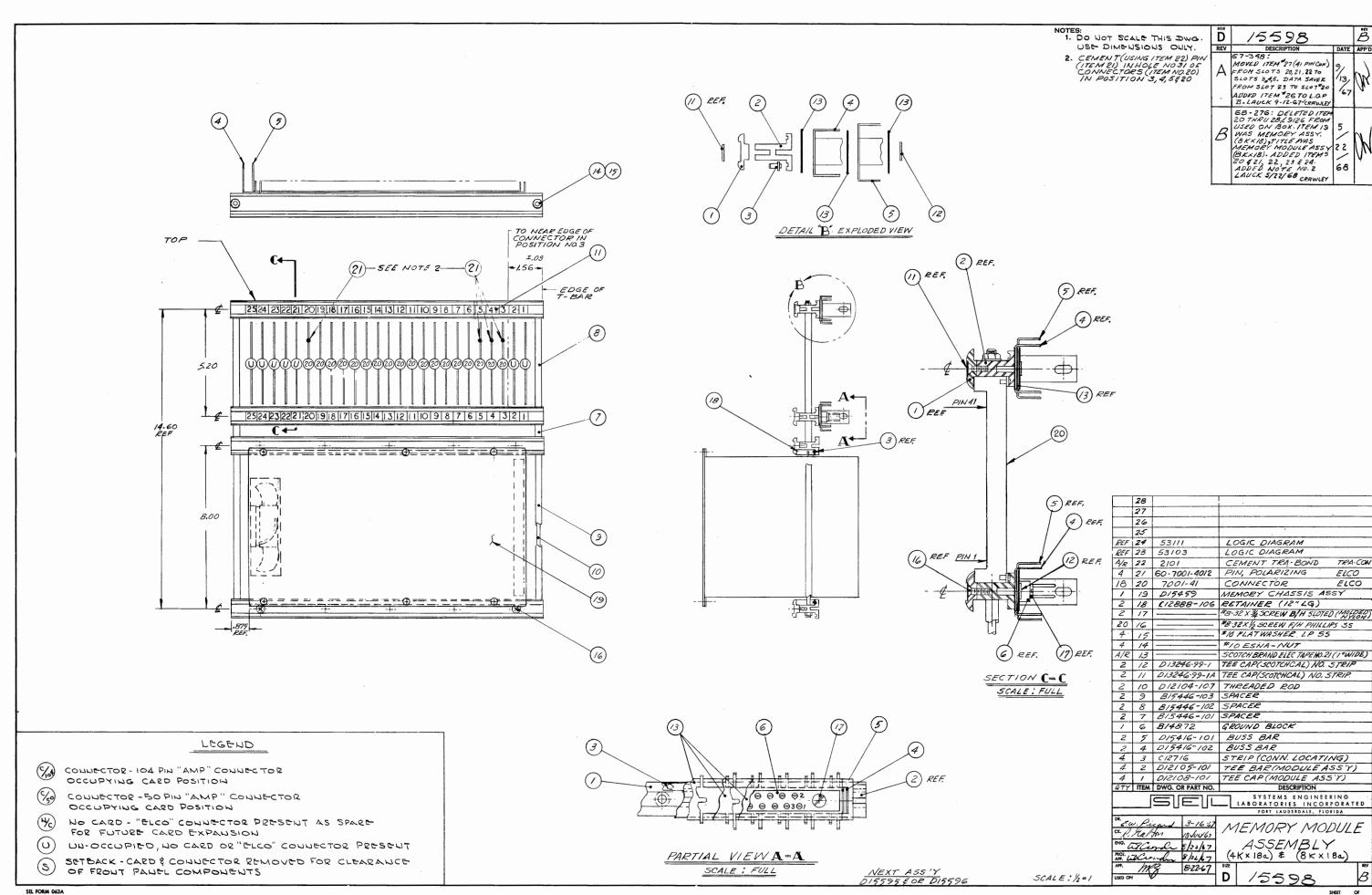
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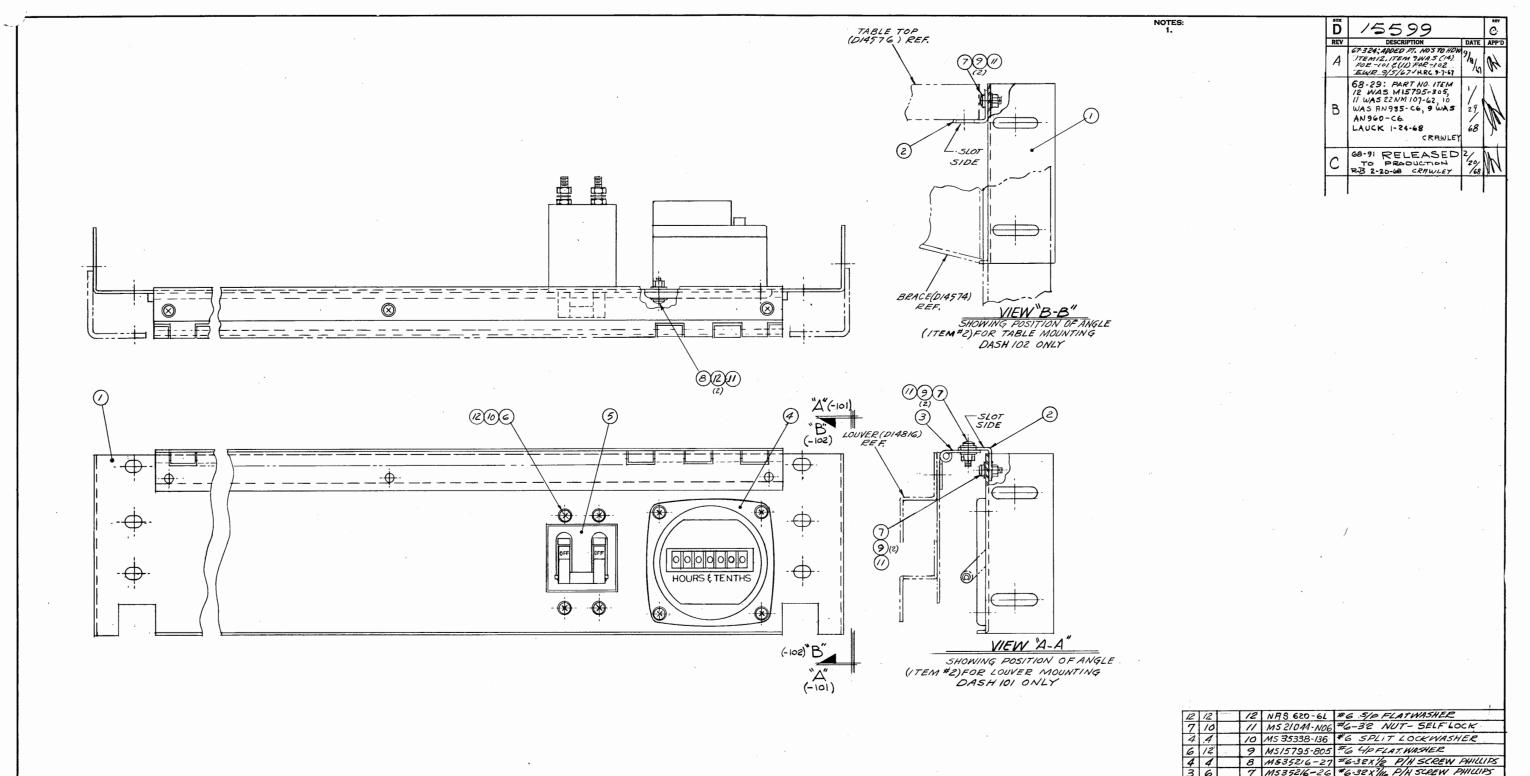
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SEE SHEET * 1 FOR REV.





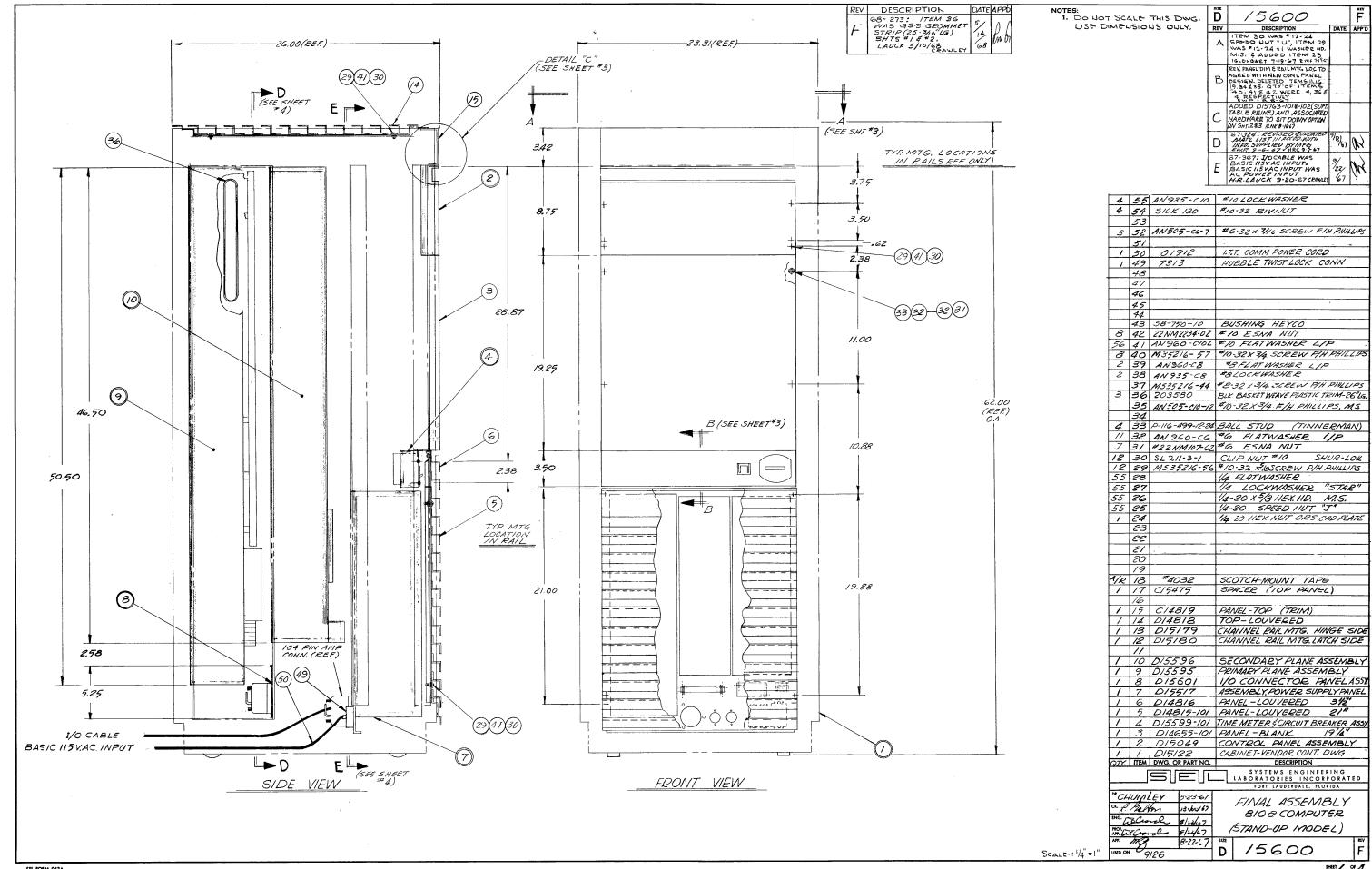




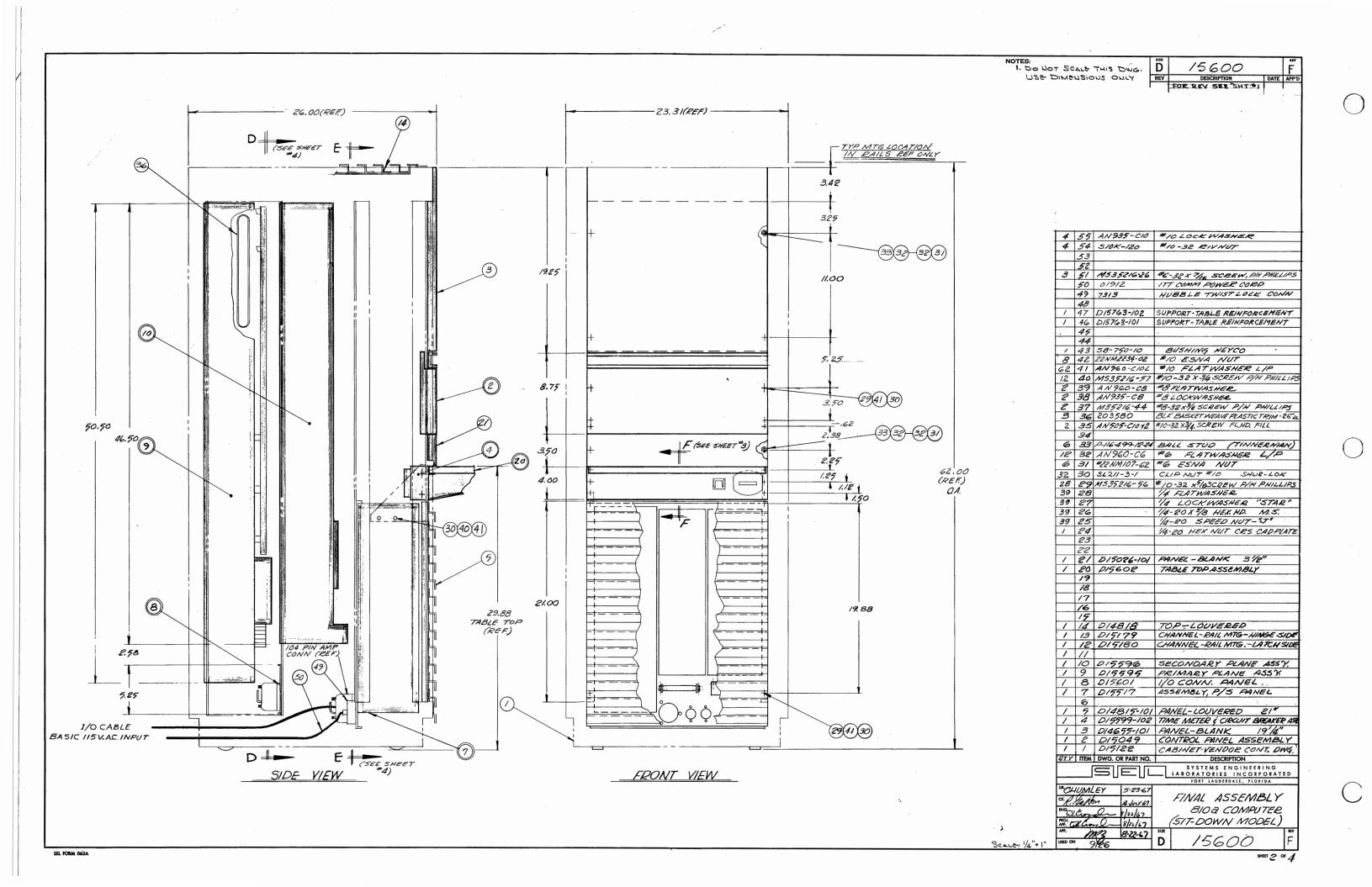
3 6 7 M53526-26 *6-32 X 1/6 P/H SCREW PHILLIPS 4 4 6 M535216-24 *6-32 X 5/6 P/H SCREW PHILLIPS 1 1 5 AM33MG6 CIRCUIT BREAKER, HEINEMANN 1 1 4 K42203-P4 METER, HAYDON 1 3 C15193 HINGE, LOUVER 1 1 2 C15192 ANGLE, HINGED LOUVER					
4 4 10 MS 35338-136 #6 SPLIT LOCKWASHER 6 12 9 MS15795-805 #6 40 FLAT WASHER 4 4 8 MS35216-27 #6-32 x/g P/H SCREW PHILLIPS 3 6 7 MS35216-26 #6-32 x/g P/H SCREW PHILLIPS 4 4 6 MS35216-24 #6-32 x/g P/H SCREW PHILLIPS 1 1 5 AM33MG6 CIRCUIT BREAKER, HENEMANN 1 1 4 K42203-P4 METER, HAYDON 1 3 C15193 HINGE, LOUVER 1 1 2 C15192 ANGLE, HINGED LOUVER 1 1 1 D15191 PANEL, METER & CIR BREAKE 977 977 TIEM DWG. OR PART NO. SYSTEMS ENGINEERING 1 DOSCRIPTION SYSTEMS ENGINEERING 1 ABORRATORIES INCORPORATED 1 TOTT LAUDEDALE, FLORIDA TOTT LAUDEDALE TOTT LAUDEDALE TOTT LAUDEDALE	12		12	NAS 650-67	#6 S/P FLATWASHER
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3 6 7 M535216-26 #6-32x7/16 P/H SCREW PHILLIPS 4 4 6 M535216-24 #6-32x7/16 P/H SCREW PHILLIPS 1 1 5 AM33MGG CRECUIT BREAKER, HENEMANN 1 1 4 K42203-P4 METER, HAVOON 1 3 C15193 HINGE, LOUVER 1 1 2 C15192 ANGLE, HINGED LOUVER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 D15191 PANEL, METER & CIR. BREAKER 1 D15191 PANEL, METER PANEL 1 D15191 ASSEMBLY 1 D15191 A	4		8	M535216-27	#6-32x1/2 P/H SCREW PHILLIPS
4 4 6 MS 35216-24 #6-32 X 16 P/H SCREW PHILLIPS 1 1 5 AM33MGG CIRCUIT BREAKER, HEINEMANN 1 1 4 K42203-P4 METER, HAYDON 1 3 C15193 HINGE, LOUVER 1 1 2 C15192 ANGLE, HINGED LOUVER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER & CIR. BREAKER 1 1 1 D15191 PANEL, METER BREAKER 1 1 1 D15191 PANEL, METER BREAKER 1 1 1 D15191 PANEL 1 D15191 PANEL 1 1 D15191 PANEL	6		7	M535216-26	#6-32 X/16 P/H SCREW PHILLIPS
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1 1 2 C15192 ANGLE, HINGED LOUVER 1 1 D15191 PANEL, METER & CIR BREAKE, 1 1 D15191 PANEL, METER & CIR BREAKE, 1 1 D15191 PANEL, METER & CIR BREAKE, 1 1 D15191 PANEL, METER BREAKE, 1 1 D15191 PANEL, METER BREAKE, 1 1 D15191 PANEL 1 D1519 PANEL 1	1		4	K42203-P4	METER, HAYDON
1 1 015191 PANEL, METER CR. BREAKE. 97% 97% ITEM DWG. OR PART NO. DESCRIPTION ILABORATORIES INCORPORATED FORT LAUDERDALE, FLORIDA BLANCE STATES AND ST	1		3	C15193	HINGE, LOUVER
TIEM DWG. OR PART NO. SYSTEMS ENGINEERING LABORATORIES INCORPORATED FORT LAUDERDALE, FLORIDA OR CHARLES SALVES METER PANEL ROLL STORY 1		2	C15192	ANGLE, HINGED LOUVER	
SYSTEMS ENGINEERING LABORATORIES INCORPORATED FORT LAUDERDALE, FLORIDA OR CHIPTON 3-23-67 OR CHIPTON 3-2	1		1	D15191	PANEL, METER & CIR. BREAKER
DR. CHARTON 3-13-67 CA PLANTA 3-44-67 DR. CHARTON	97%	\sim	ITEM	DWG. OR PART NO.	DESCRIPTION
Captain 340.467 METER PANEL 10 10 10 10 10 10 10 10 10 10 10 10 10 1	73				LABORATORIES INCORPORATED
WE William 3339 METER PANEL 100 METER P	2	DR.			POLI LAUDILUALI, ILONIUA
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8131 BIG. LECTURE St. 1/67 ASSEMBLY 131 M. M. M. M. 1/67 BIZ. 1/67 SIZE 181	6	a P.	(Ka)	1341467	,
2 M. Lating to 1/2:1/67	35	ENG.	T.C.	71 7 7	ASSEMBLY
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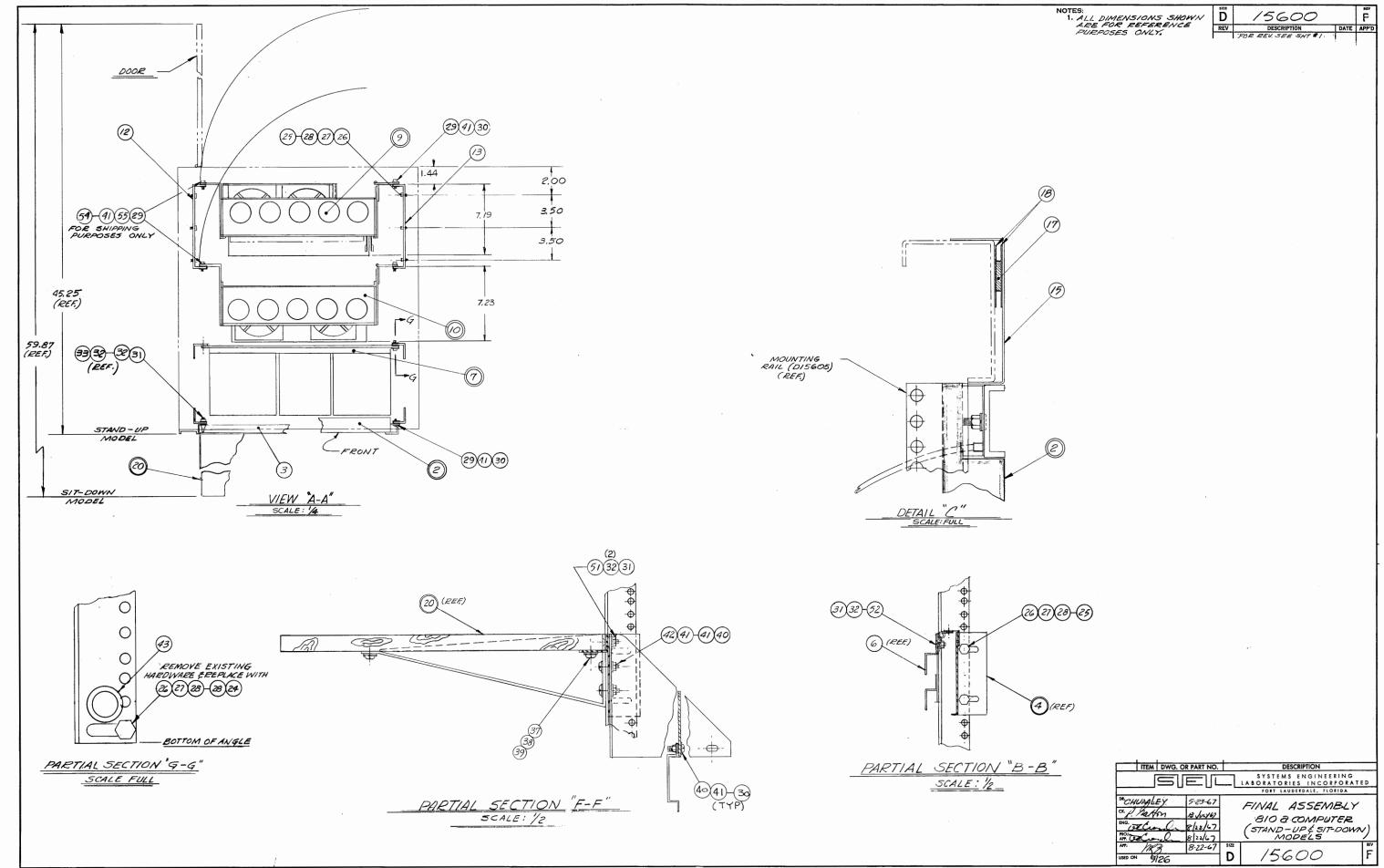
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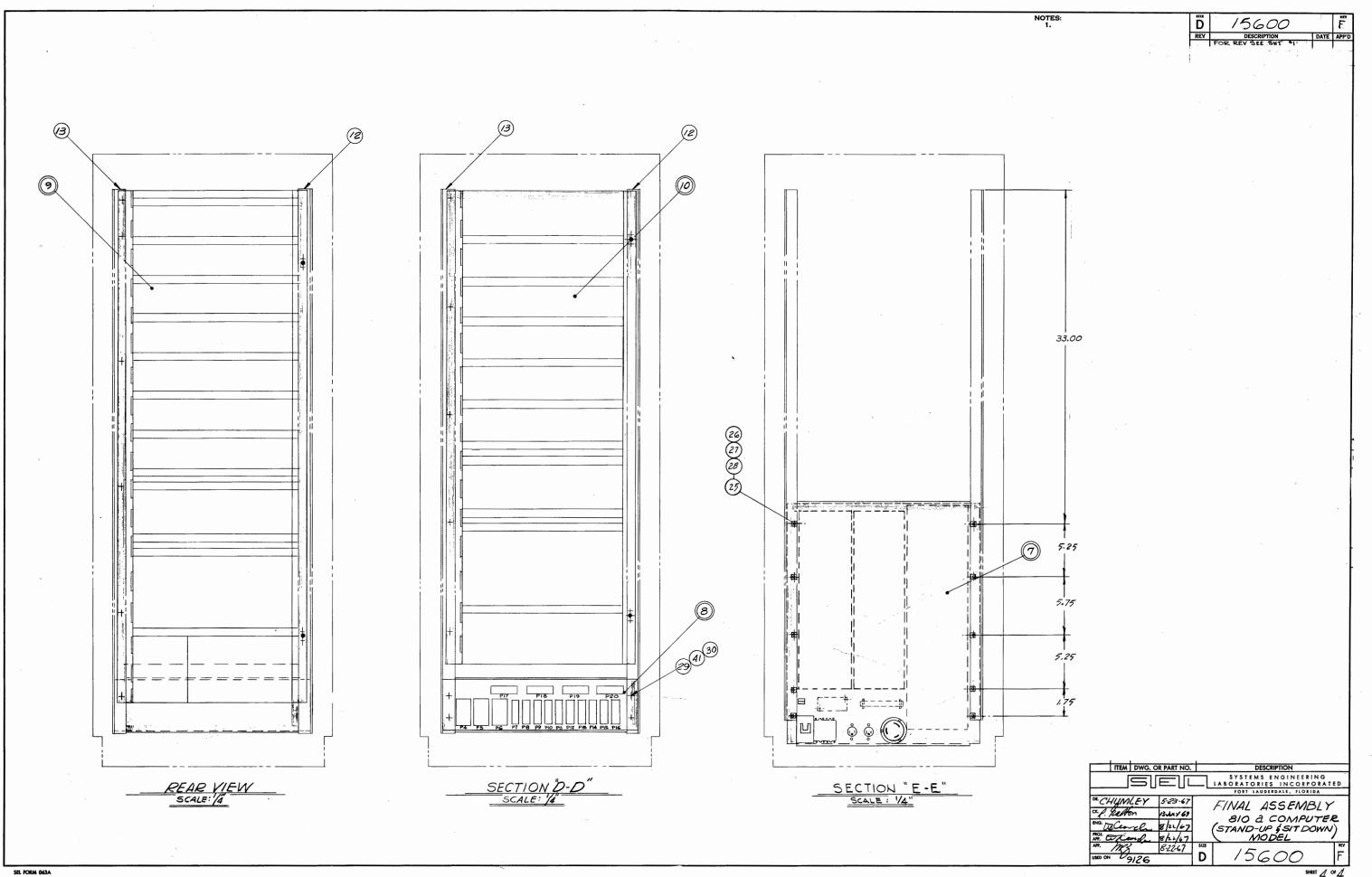
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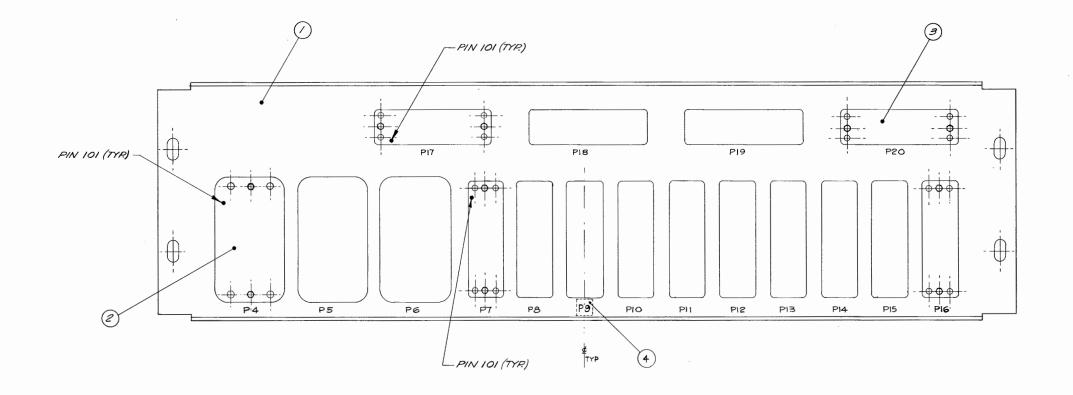




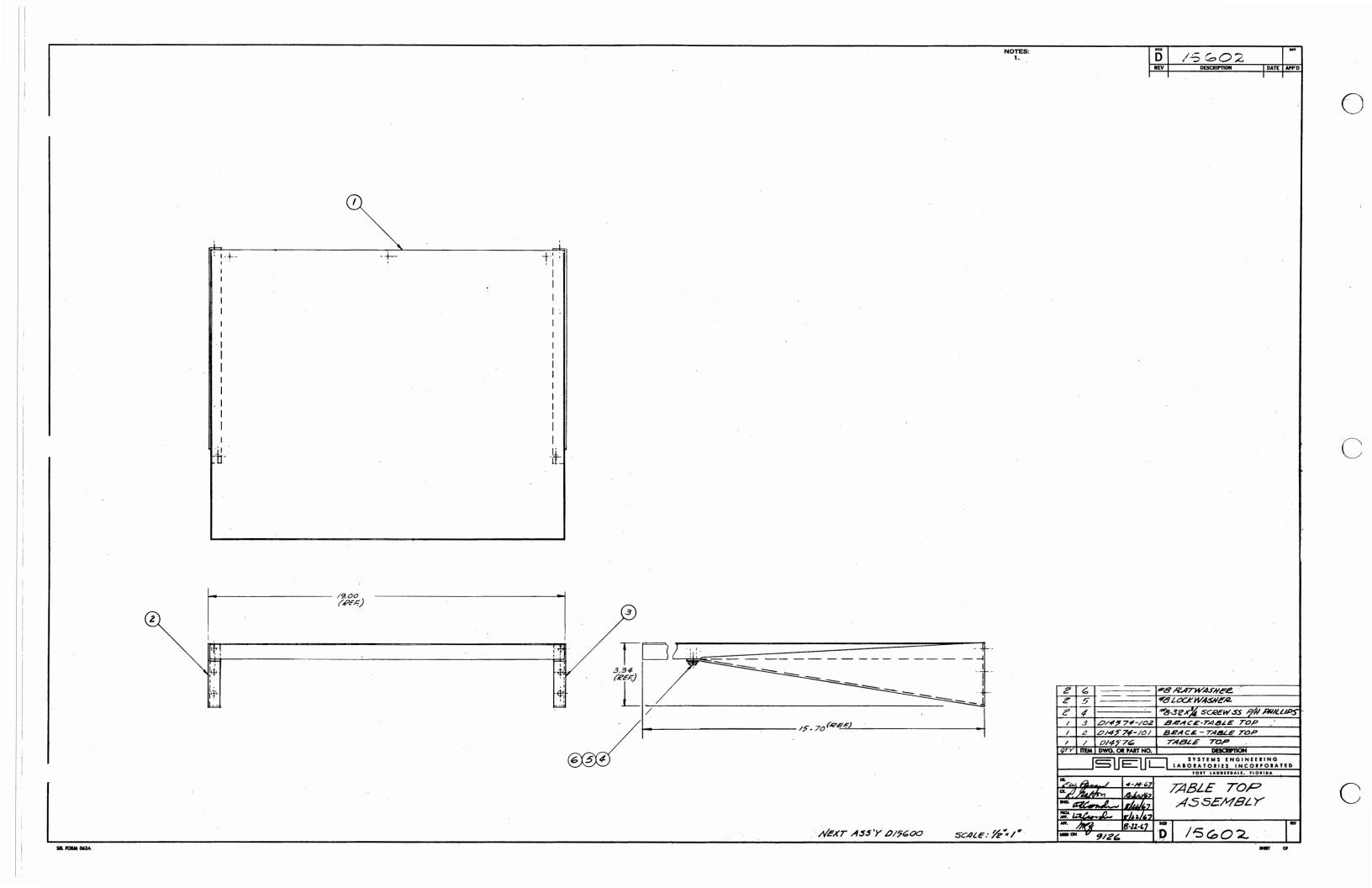


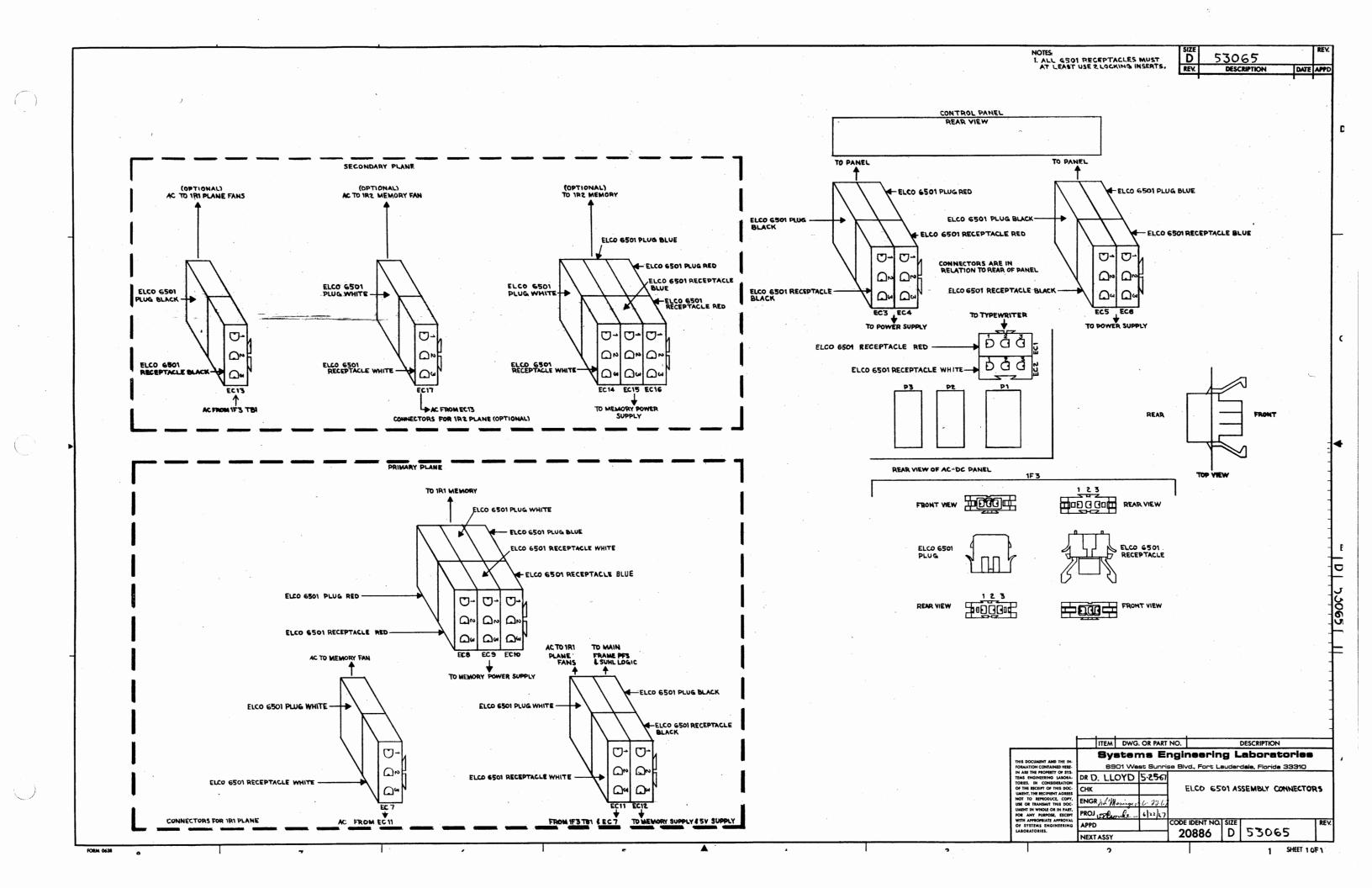
NOTES:

NEXT ASSY DISGOD



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4 3	200277-2 201037-1	50 F	PIN CONNECTOR (FEMALE)AM
7 -	201037-1	104	
3 2			PIN CONNECTOR (FEMALE)AMP
/ /	011566		
	D14566	PAN	VEL-1/O CONNECTOR
Y. ITEM	DWG. OR PART NO.		DESCRIPTION
	SIEIL	ι,	SYSTEMS ENGINEERING ABORATORIES INCORPORATED FORT LAUDERDALE, FLORIDA
CHUMI			ASSEMBLY
		/	/O CONNECTOR PANEL
" lette	omely Klester		PANEL
MRZ	8-22-67	D	15601 Ä
	CHUMIL P. Salv W.C.	CHUNTEY 5-22-67 P. Marton 13 Marson War Comple Blacks War Comple Blacks War B22-67	CHUNLEY 5-22-67 P. Meldon BAU167 "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks" "Wilcomela Blacks"





NOTES:
1. - REMOVE 18 WHEN P.I. 1 ADDED.
2. - ADD FOR 5TH BTC.
3. - REPLACE 1GE (17E)(8297)WITH
8204 - WHEN 'MPY-DIV INSTALLED.

MEMORY #1 4.- BASIC ONLY 5.- P. P. ONLY 6.- PARITY ONLY

52992

BESCRIPTION

68-199; LEPPATED TO LATEST CHANGES
THE HULL 4-19-68 RKM D REV

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	150	RTL-BET REGISTER STROBE	8213	1	
	130	RTL-A REGISTER STROBE	8214	L i	
\ \squaresquar	140	RTL-CARRY LOGIC	8215	1	
· .	120	RTL -INPUT/OUTPUT INSTRUCTION DECODE	8216	1	
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STALL ALARM	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR, CURENT DR & INHIBIT DR. RTL- MEMORYTIMING #1 RTL- MEMORYTIMING #2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINEDR. SW.	8B 2G 1G 1E 3F 4F 2 A K 6T 7F 16 A F 16 A F 17 B 17 B 17 B 18 A F	
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STALL ALARM	1 1 1 1 1 1 1 1 1 1 4 5	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8273-1 8273-1 8275-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING #1 RTL- MEMORY TIMING #2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5BIT. MEM. ADDRESS REG. & & KONTROL	88 4 4 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	·
STALL ALARM	1 1 1 1 1 1 4 4 5 5 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8272-1 8273-1 8274-1 8275-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. & 8 KONTROL RTL-MEM PAREPORECT BIT DA. REG. & INH. DR.	88 9 9 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	NOTE 6
STALL ALARM	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 1 4 4 5 5 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8272-1 8273-1 8274-1 8275-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	88 9 9 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	NOTE 6
MODULE STALL ALARM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8277-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM CONTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR, CURENT DR & INHIBIT DR. RTL- MEMORY TIMING #1 RTL- MEMORY TIMING #2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINEDR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5BIT MEM.ADDRESS REG. & 8 K CONTROL RTL-MEM PAR PROTECT BIT DA, REG. & INH. DR. +12Y & THRESHOLD REGULATOR CONNECTOR & RESISTOR CARD	BB 25 19	NOTE 6
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8276-1 8277-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC Z SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING # I RTL- MEMORY TIMING # 2 RTL- QUAD DATA REG. & INHIBIT DR. RTL-READ/WRITE MATRIX & LINE DR. SW. RTL-8 B BIT MEMORY ADDRESS REG. RTL-BBIT MEM ADDRESS REG. RTL-MEM PAR PROTECT BIT DA. REG. & INH. DR. +12V & THRESHOLD REG. ULATOR	B G G G G G G G G G G G G G G G G G G G	DITOL
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MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8272-1 8273-1 8274-1 8275-1 8276-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM CONTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR., CURENT DR. INHIBIT DR. RTL- MEMORY TIMING #1 RTL- QUAD DATA REG. È INHIBIT DR RTL-READ/WRITE MATRIX È LINE DR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5BIT. MEMORY ADDRESS REG. RTL-5BIT. MEM. ADDRESS REG. È SKONTROL RTL-MEM DARÈ PROTECT BIT DA. REG. INH. DR. +12V \$THRESHOLD REGULATOR CONNECTOR È RESISTOR CARD DATA SAVER	8B 29 19 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	NOTE 6
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MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 4 5 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8275-1 8275-1 8275-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM CONTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR., CURENT DR. INHIBIT DR. RTL- MEMORY TIMING #1 RTL- QUAD DATA REG. È INHIBIT DR RTL-READ/WRITE MATRIX È LINE DR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5BIT. MEMORY ADDRESS REG. RTL-5BIT. MEM. ADDRESS REG. È SKONTROL RTL-MEM DARÈ PROTECT BIT DA. REG. INH. DR. +12V \$THRESHOLD REGULATOR CONNECTOR È RESISTOR CARD DATA SAVER	8B 29 19 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	NOTEG
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8275-1 8275-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR., CURRENT DR. INHIBIT DR. RTL- MEMORY TIMING #1 RTL- MEMORY TIMING #2 RTL- QUAD DATA REG. ÉINHIBIT DR. RTL-READ/WRITE MATRIX ÉLINEDR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5 BIT. MEMORY ADDRESS REG. RTL-5 BIT. MEMORY ADDRESS REG. LIVE STILL MEMORY ADDRESS REG. RTL-1 BIT. MEM. ADDRESS REG. ÉS KONTROL RTL-MEM PARÉPRITECT BIT DA. REG. ÉINH. DR. +12 Y ÉTH RESHOLD REGULATOR CONNECTOR É RESISTOR CARD DATA SAVE.R DUAL CURRENT DRIVER	BB & G G G G G G G G G G G G G G G G G G	NOTE 6
MODULE STALL ALARM PROTECT MEMORY #1 BKX18	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8272-1 8273-1 8275-1 8276-1 8277-1 8276-1 8277-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING #1 RTL- MEMORY TIMING #2 RTL- QUAD DATA REG. & INHIBIT DR RTL- READ/WRITE MATRIX & LINE DR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5BIT MEM. ADDRESS REG. & 8 K CONTROL RTL-MEM PARE PROTECT BIT DA. REG. & INHIBIT LOR. HIZV & THRESHOLD REG. ULATOR CONNECTOR & RESISTOR CARD DATA SAVER DUAL CURRENT DRIVER LLA SENSE AMPLIFIER NO. 3	8B 3G 1G 1	DETOU
MODULE STALL ALARM PROGRAM PROTECT	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8254 8759 8269-1 8270-1 8271-1 8273-1 8274-1 8275-1 8275-1 8275-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR., CURRENT DR. INHIBIT DR. RTL- MEMORY TIMING #1 RTL- MEMORY TIMING #2 RTL- QUAD DATA REG. ÉINHIBIT DR. RTL-READ/WRITE MATRIX ÉLINEDR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5 BIT. MEMORY ADDRESS REG. RTL-5 BIT. MEMORY ADDRESS REG. LIVE STILL MEMORY ADDRESS REG. RTL-1 BIT. MEM. ADDRESS REG. ÉS KONTROL RTL-MEM PARÉPRITECT BIT DA. REG. ÉINH. DR. +12 Y ÉTH RESHOLD REGULATOR CONNECTOR É RESISTOR CARD DATA SAVE.R DUAL CURRENT DRIVER	BB & G G G G G G G G G G G G G G G G G G	, NOTE 6
MODULE STALL ALARM PROTECT MEMORY #1 BKX18	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	8749 8255 8256 8543 8253 8254 8759 8269-1 8270-1 8272-1 8273-1 8275-1 8276-1 8277-1 8276-1 8277-1 8276-1	IZCIRCUIT DIGITAL BUFFER STALL ALARM CONTROL STALL ALARM COUNTER RTL-2 NOR PP LOGIC I PP LOGIC 2 SCHMITT TRIGGER #2 LINE DR, CURRENT DR & INHIBIT DR. RTL- MEMORY TIMING #1 RTL- MEMORY TIMING #2 RTL- QUAD DATA REG. & INHIBIT DR RTL- READ/WRITE MATRIX & LINE DR. SW. RTL-8 BIT MEMORY ADDRESS REG. RTL-5BIT MEM. ADDRESS REG. & 8 K CONTROL RTL-MEM PARE PROTECT BIT DA. REG. & INHIBIT LOR. HIZV & THRESHOLD REG. ULATOR CONNECTOR & RESISTOR CARD DATA SAVER DUAL CURRENT DRIVER LLA SENSE AMPLIFIER NO. 3	8B 3G 1G 1	NOTE 6

NOTES: 52992 2. ADD FOR 5TH BTC DESCRIPTION 67-445: UPDATED TO LATEST 19 A CHANGES & A DDED SH. 2.
THIS SHEET WAS \$3020
J. PINEDO. 10-26-67 M.L. 130/67 67-531 : UPDATED TO LATEST CHANGES.
M. LOYD 11-27-67 COW 68-199; LOCATION OF CARD 8268 WAS 20F, LOCATION OF CARD 8545 WAS IIF. W. Masual 4-19-68 RKM IR2 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 OPTIONS 82634 82634 82634 82604 82604 8259 8259 8259 7,8,9D 8263 | 5 BIT DUAL CTR. 8262 BTC DATA XFER CONTROL*2 2-60 8261 BTC DATA XFER CONTROL *1 1-5D 8257 BTC INITIALIZE CØNTRØL 8258 BTC CYCLE CØNTRØL 8259 BTC ADDRESS CØNTRØL BTC *1 ID 8263 8263 8263 8262 8260 8259 8259 8258 50 3D 4D 8260 BTC LØAD ADDRESS CONTROL 25 24 23 22 21/20 19 18 17 16 15 14 13 53205-1 RIBBON CABLE CONNECTORS BTC # 2 8265 BPC CYCLE CØNTRØL
8266 BPC PRØTECT CØNTRØL
8267 BPC INITIAL ADDRESS CØNTRØL
8268 BTC PRIØRITY
8749 12 BUFFER 7E 19E SE NOTE 2 20EZIE 4,5,6E 8547 4N 4 3,13,15,17E NOTEZ 8557 4E 8545 INV 0,14,16,18E ILE ISE E 8705-2 GATED CABLE DRIVER 8269-1 LINE, CURRENT & INHIBIT DRIVERS 22E 6K 8270-1 MEMORY TIMING 1 6G 8271-1 MEMORY TIMING 2 8272-1 QUAD, DATA, REG INHIBIT DRIVER 79 15-18G 8273-1 WRITE MATRIX & LINE DRIVER 8274-1 8 BIT ADDRESS REGISTER 9-12G 146 8275-1 5 BIT MEM ADDRESS REG & CONTROL 139 8276-1 MEMORY PROTECT (P. P. ONLY) 19G MEMORY 8277-1 HIZV THRESHOLD REGULATOR 6K 8277-1 HIS THRESHOLD REGULATOR 8278-1 COUNECTOR & RESISTOR 8639-2 DATA SAVER 8959-2 DUAL CURRENT DRIVER 8962-6 MA SENSE AMP(BASIC ONLY) 8962-7 MA SENSE AMP(PARITY ONLY) 23 209 2K 2K 8962-1 MA SENSE AMP (P.P.) 8962-10 MA SENSE AMP G 2K 8272-2 QUAD, DATA REG ; INHIBIT DR. (PARLY) 8960-1 CONNECTOR CARD 196 35,45 53205-2 RIBBON CABLE CONNECTORS 5G 53205-1 RIBBON CABLE CONNECTORS 3,4G 83206 DATA TRANSFER CONTROL 83207 BRANCH CONTROL 25 24 23 22 21 20 19 18 17 16 15 14 13 12 11 10 9 8 7 6 5 4 3 2 1 6F 75 83205 STOP CONTROL SF 8278-1 83208 ADDRESS CONTROL/REG 83203 INITIALIZE CONTROL CGPI 95 8960-1 CORE STACK 3F 8960-1 83204 ADDRESS CONTROL 4F 8959-2 8260 LOAD ADDRESS CONTROL 5F 8269-1 8962-10 2 8962-6 PARITY PROG PRO 8962-7 8962-1 TITLE NO. REQD. CARD NO. TITLE NO. REQD. CARD NO. LOCATION K 6 8277-1 105 ITEM DWG. OR PART NO. DESCRIPTION SYSTEMS ENGINEERING
LABORATORIES INCORPORATED
FORT LAUDERDALE, FLORIDA CARD LOCATION a C Williams 10-31-67 ma. E. Villanti 6-2-67 PLANE IR2 MP. 20 Crown 10-31 52992

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